

Documentaion

VMCPU

Registers

Three general-purpose 32-bit registers

One unsigned 32-bit special register : IP

Genereal-Purpose registers:

- first_reg,
- second_reg,
- third_reg

Instruction Pointer:

- IP

Stack Pointer:

- St

Instructions

mov register data

Moves a value to the register

- 0x10: mov first_reg data
- 0x11: mov second_reg data
- 0x12: mov third_reg data

jmp address

Jumps to an address forward or backward

- 0x20: jmp address

int 1

Causes software interrupt:

- 0x30: int 1

LD reg imm32

Loads data from a virtual address specified by an immediate value to a register

- 0x30: ld first_reg imm32
- 0x31: ld second_reg imm32
- 0x32: ld third_reg imm32

ST reg imm32

Stores data at a specified virtual address in an immediate value from a register

- 0x40: first_reg imm32

- *0x41*: second_reg imm32
- *0x42*: third_reg imm32