

Documentaion

VMCPU

Registers

Three general-purpose 32-bit registers

One unsigned 32-bit special register : IP

Genereal-Purpose registers:

- first_reg, second_reg, third_reg

Instruction Pointer:

- IP

Instructions

mov register data

Moves a value to the register

- 0x10: mov first_reg data
- 0x11: mov second_reg data
- 0x12: mov third_reg data

jmp address

Jumps to an address forward or backward

- 0x20: jmp address

int 1

Causes software interrupt:

- 0x30: int 1