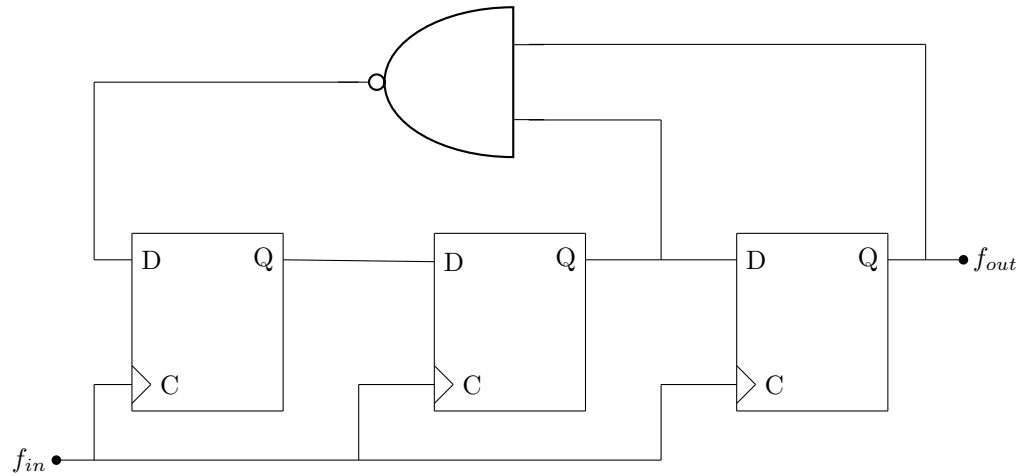


1. Which of the following statements is true about digital circuits shown in the figure (Gate EE-2018,36)



- (A) It can be used for dividing the input frequency by 3 .
(B) It can be used for dividing the input frequency by 5 .
(C) It can be used for dividing the input frequency by 7 .
(D) It cannot be reliably used as frequency divider due to disjoint internal cycles .