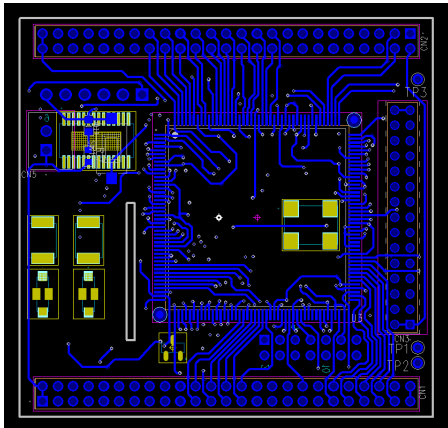


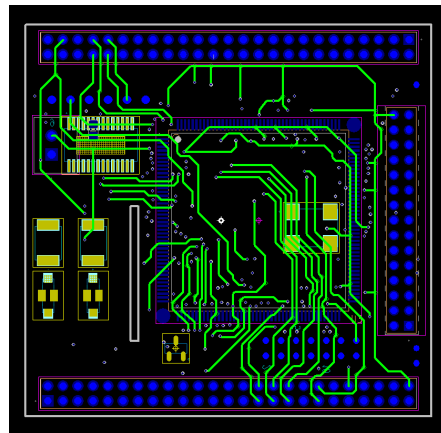
Work : DSP335 PCB Design



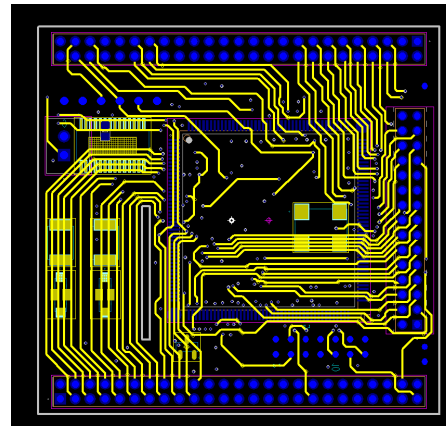
Layer 1/2/3/4 (나의 디자인)



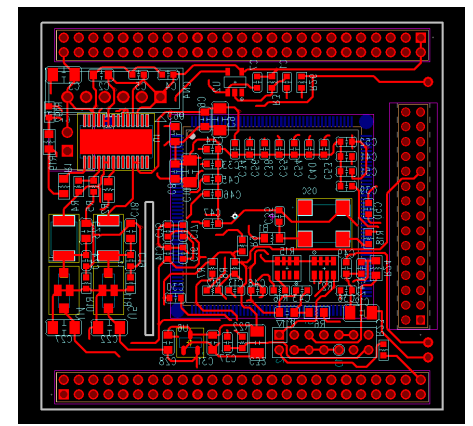
레이어 1



레이어 2

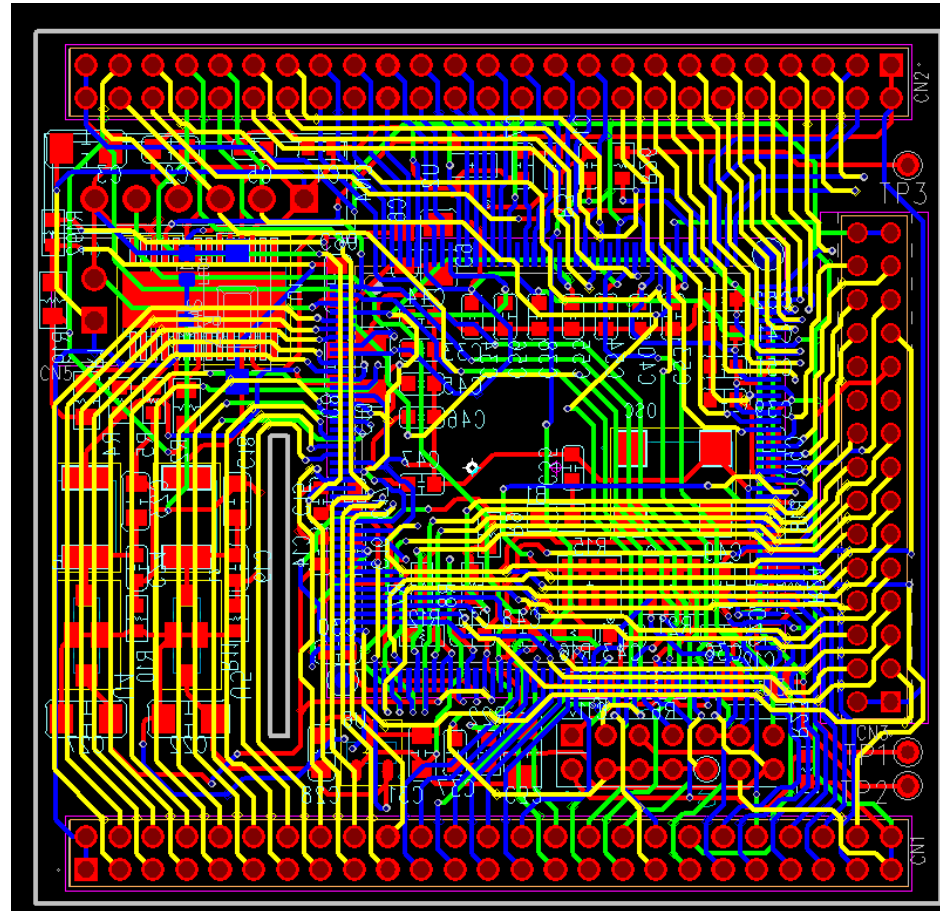


레이어 3

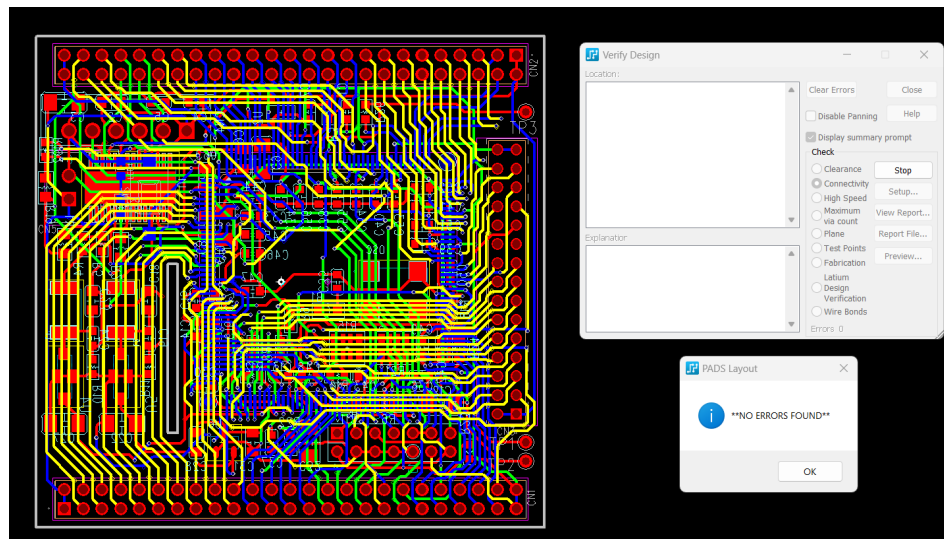


레이어 4

All Layers (모든 레이어)

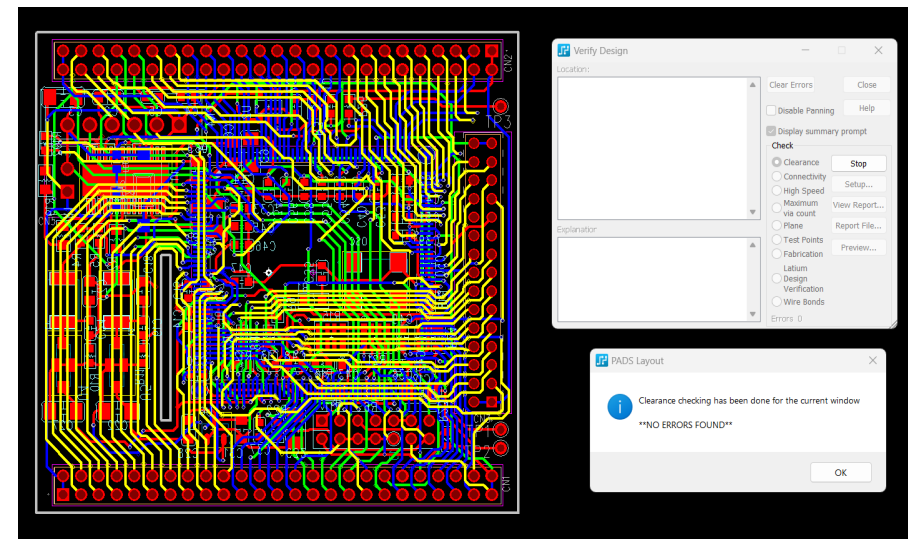


연결 확인 (Verify Connectivity)



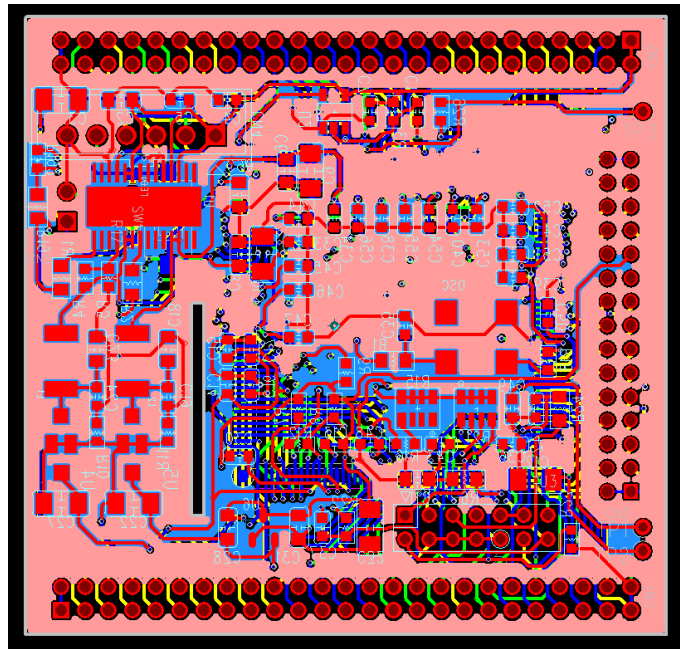
PCB에는 아무런 오류가 없습니다.

클리어런스 확인 (Verify Clearance)

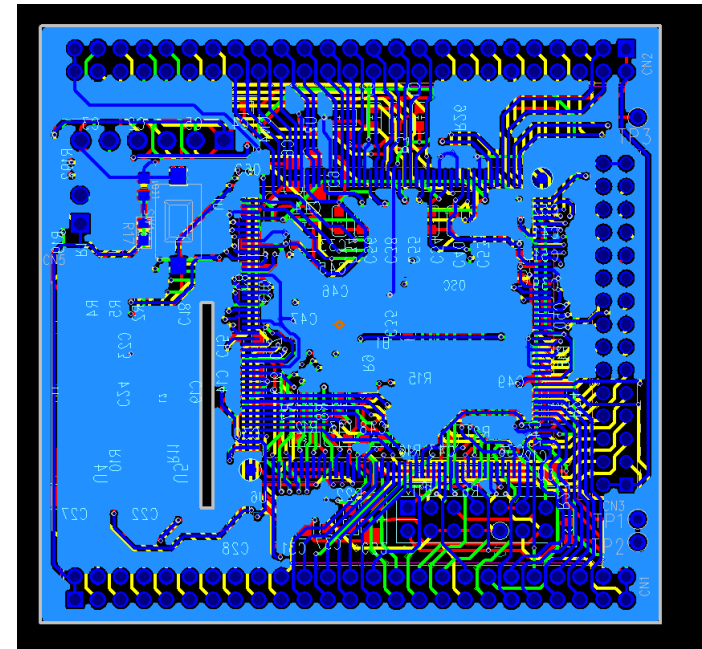


PCB에는 아무런 오류가 없습니다.

Final Output:



맨 아래 (Copper Poured)



맨 위 (Copper Poured)

Ceyhun Pempeci (재훈 펌페지)