VICTOR AFONSO DOS REIS

② victor.afonsoreis35@gmail.com♥ São José do Rio Preto-SP, Brazil



PROFESSIONAL EXPERIENCE

Telecommunication Engineering Intern Oualcomm

Apr 2019 - Dec 2019

São Paulo, Brasil

• 4G and 5G Protocol Analysis and device tests.

Engineering Intern
Intel Corporation

₩ Feb 2018 - Feb 2019

Munich, Germany

- Integration, verification and hardware solutions for G.Fast, VDSL and ADSL modem chips (Home Connected Division).
- Support for engineers in test definition, test development and test execution.
- Use of measurement, testing and modification equipment such as spectrum analyzer, oscilloscopes, loop simulators and soldering stations.
- Develop solutions to hardware problems.

ACADEMICS EXPERIENCE

Scholarship Holder Fundunesp/UNESP

math display="block"> Jan 2016 - Dec 2018

Study, development and implementation of codings 8b/10b and 64b/66b. Matlab coding was modeled (SIMULINK) and then implemented in an FPGA (Xilinx Kintex 7) using VHDL.

Undergraduate Researcher

São Paulo Research and Analysis Center (SPRACE)/UNESP

M Oct 2015 - Dec 2018

Student / Researcher in the field of electronic instrumentation for high energy physics.

Voluntary

PET Elétrica/UNESP

m Dec 2014 - Mar 2017

The main activity developed in the group was the Project Workshop in which I was leader of the activity. In this activity electronic projects were developed along with the freshmen of the course.

STRENGTHS

Python Matlab VHDL FPGA

5G 4G xDSL Spectrum Analyser

Telecommunication Systems

Network Analyser Call Box

Linux LaTeX C# Simulink

High Speed Communications Git

LANGUAGES

Portuguese	••••
English	••••
German	

EDUCATION

B.Sc. in Electrical Engineering UNESP-Ilha Solteira

Final Work: Study of the Serial Communication System LpGBT

REFEREES

Bruno Pereira Matheus

Nvidia

■ brunop@nvidia.com

+49 1520 2585764

Prof. Dr. Ailton Akira Shinoda

@ UNESP

+55 18 3743-1290

PUBLICATIONS

Modeling and Implementation in FPGA of 8b/10b Encoding.

SIIM/SPS. Nov 2017. Available in: <www.eventos.ufabc.edu.br/siimsps/files/id14.pdf>

Robustness Analysis and State Machine Modeling of 8b/10b Encoding. ERMAC. May 2017. Available in: www.fc.unesp.br/Home/Departamentos/Matematica/ermac/caderno-ermac_2017.pdf