

1 semantics

We model the state of a database by a time-stamp heap that is a partial function from locations to their histories. The history is a partial function from times to a set of events. A event is a triple consisting of the value being read or written, the operation, i.e. either read or write, and the transaction identifier. We use thread pool to model the concurrency. Each thread has a local stack and a local time, but a globally shared time-stamp heap. Therefore a thread pool is a partial functions from thread identifiers to the corresponding stack, time and transactions. The state of each transaction, i.e. local state, are a stack which is shared between transactions from the same thread, a heap that is a snapshot of the time-stamp heap, and fingerprints that are the heap locations being read and written.

$$\begin{aligned}
l \in \text{Loc} &\triangleq \mathbb{N} \\
v \in \text{Val} &\triangleq \mathbb{N} \uplus \text{Loc} \\
\text{Var} &\triangleq \{\mathbf{x}, \mathbf{y}, \dots\} \\
t \in \text{TimeStamp} &\triangleq \mathbb{N} \\
h \in \text{Heap} &\triangleq \text{Loc} \rightarrow \text{Val} \\
s \in \text{Stack} &\triangleq \text{Var} \rightarrow \text{Val} \\
o \in \text{Operation} &\triangleq \{\mathbf{r}, \mathbf{w}\} \\
\mathcal{T} \subseteq \text{TransID} &\triangleq \{\alpha, \beta, \dots\} \\
\text{ThreadID} &\triangleq \{i, j, \dots\} \\
rs \in \text{ReadSet}, ws \in \text{WriteSet} &\triangleq \mathcal{P}(\text{Loc}) \\
\bar{h} \in \text{TimeStampHeap} &\triangleq \text{Loc} \rightarrow (\text{TimeStamp} \rightarrow \mathcal{P}(\text{Val} \times \text{Operation} \times \text{TransID})) \\
(s, \bar{h}, t) \in \text{ThreadState} &\triangleq \text{Stack} \times \text{TimeStampHeap} \times \text{TimeStamp} \\
\eta \in \text{ThreadPool} &\triangleq \text{ThreadID} \rightarrow \text{Stack} \times \text{TimeStamp} \times \mathbb{P} \\
\Sigma \in \text{State} &\triangleq \text{TimeStampHeap} \times \text{ThreadPool} \\
\sigma = (s, h, rs, ws) \in \text{LocalState} &\triangleq \text{Stack} \times \text{Heap} \times \text{ReadSet} \times \text{WriteSet}
\end{aligned}$$

The arithmetic expression and boolean expression are standard and have no side effect.

$$\begin{aligned}
\mathbb{E} ::= & v \mid \mathbf{x} \mid \mathbb{E} + \mathbb{E} \mid \mathbb{E} * \mathbb{E} \mid \dots \\
\llbracket v \rrbracket_s &\triangleq v \\
\llbracket \mathbf{x} \rrbracket_s &\triangleq s(\mathbf{x}) \\
\llbracket \mathbb{E}_1 + \mathbb{E}_2 \rrbracket_s &\triangleq \llbracket \mathbb{E}_1 \rrbracket_s + \llbracket \mathbb{E}_2 \rrbracket_s \\
\llbracket \mathbb{E}_1 * \mathbb{E}_2 \rrbracket_s &\triangleq \llbracket \mathbb{E}_1 \rrbracket_s * \llbracket \mathbb{E}_2 \rrbracket_s \\
\mathbb{B} ::= & \text{true} \mid \text{false} \mid \mathbb{E} = \mathbb{E} \mid \mathbb{E} < \mathbb{E} \mid \text{not } \mathbb{B} \mid \mathbb{B} \text{ and } \mathbb{B} \mid \mathbb{B} \text{ or } \mathbb{B} \mid \dots \\
\llbracket \text{true} \rrbracket_s &\triangleq \text{true} \\
\llbracket \text{false} \rrbracket_s &\triangleq \text{false} \\
\llbracket \mathbb{E}_1 = \mathbb{E}_2 \rrbracket_s &\triangleq \llbracket \mathbb{E}_1 \rrbracket_s = \llbracket \mathbb{E}_2 \rrbracket_s \\
\llbracket \mathbb{E}_1 < \mathbb{E}_2 \rrbracket_s &\triangleq \llbracket \mathbb{E}_1 \rrbracket_s < \llbracket \mathbb{E}_2 \rrbracket_s \\
\llbracket \text{not } \mathbb{B} \rrbracket_s &\triangleq \neg \llbracket \mathbb{B} \rrbracket_s \\
\llbracket \mathbb{B}_1 \text{ and } \mathbb{B}_2 \rrbracket_s &\triangleq \llbracket \mathbb{B}_1 \rrbracket_s \wedge \llbracket \mathbb{B}_2 \rrbracket_s \\
\llbracket \mathbb{B}_1 \text{ or } \mathbb{B}_2 \rrbracket_s &\triangleq \llbracket \mathbb{B}_1 \rrbracket_s \vee \llbracket \mathbb{B}_2 \rrbracket_s \\
\mathbb{C} ::= & \text{skip} \mid \mathbf{x} := \mathbb{E} \mid [\mathbb{E}] := \mathbb{E} \mid \mathbf{x} := [\mathbb{E}] \mid \text{if } (\mathbb{B}) \mathbb{C} \text{ else } \mathbb{C} \mid \\
& \text{while } (\mathbb{B}) \mathbb{C} \mid \mathbb{C}; \mathbb{C}
\end{aligned}$$

The syntax and semantics of a single transaction are standard except *mutate* and *deref*. The *mutate* also adds the location being written to the write fingerprint set *ws* and the *deref* adds the location to *rs*. Note that there is no parallel composition, because it is within a transaction.

$$(-, -) \rightsquigarrow_l (-, -) \triangleq (\text{LocalState} \times \mathbb{C}) \times (\text{LocalState} \times \mathbb{C})$$

$$\frac{\llbracket \mathbb{E} \rrbracket_s = v}{(s, h, rs, ws), \mathbf{x} := \mathbb{E} \rightsquigarrow_l (s[\mathbf{x} \mapsto v], h, rs, ws), \text{skip}} \text{ass}$$

$$\begin{array}{c}
\frac{\llbracket E_1 \rrbracket_s = l \quad \llbracket E_2 \rrbracket_s = v \quad l \in \text{dom}(h)}{(s, h, rs, ws), [E_1] := E_2 \rightsquigarrow_l (s, h[l \mapsto v], rs, ws \cup \{l\}), \text{skip}} \text{ mutate} \\
\\
\frac{\llbracket E \rrbracket_s = l \quad v = h(l) \quad l \in \text{dom}(h)}{(s, h, rs, ws), \mathbf{x} := [E] \rightsquigarrow_l (s[\mathbf{x} \mapsto v], h, rs \cup \{l\}, ws), \text{skip}} \text{ deref} \\
\\
\frac{\llbracket B \rrbracket_s = \text{true}}{(s, h, rs, ws), \text{if } (B) \ C_1 \text{ else } C_2 \rightsquigarrow_l (s, h, rs, ws), C_1} \text{ ifelsetrue} \\
\\
\frac{\llbracket B \rrbracket_s = \text{false}}{(s, h, rs, ws), \text{if } (B) \ C_1 \text{ else } C_2 \rightsquigarrow_l (s, h, rs, ws), C_2} \text{ ifelsefalse} \\
\\
\frac{\llbracket B \rrbracket_s = \text{true}}{(s, h, rs, ws), \text{while } (B) \ C \rightsquigarrow_l (s, h, rs, ws), C; \text{while } (B) \ C} \text{ whiletrue} \\
\\
\frac{\llbracket B \rrbracket_s = \text{false}}{(s, h, rs, ws), \text{while } (B) \ C \rightsquigarrow_l (s, h, rs, ws), \text{skip}} \text{ whilefalse} \\
\\
\frac{}{(s, h, rs, ws), \text{skip}; C_2 \rightsquigarrow_l (s, h, rs, ws), C_2} \text{ seqskip} \\
\\
\frac{(s, h, rs, ws), C_1 \rightsquigarrow_l (s', h', rs', ws'), C'_1}{(s, h, rs, ws), C_1; C_2 \rightsquigarrow_l (s', h', rs', ws'), C'_1; C_2} \text{ seqnonskip}
\end{array}$$

A program is sequential and parallel composition of transactions. To give semantics for a program, we extend the syntax by adding an extra waiting command, $\text{wait}(i)$, as suffix. Intuitively, this $\text{wait}(i)$ indicates that current thread is waiting another thread identified by i until it commits all its transactions and then join the thread.

$$\begin{array}{l}
\mathbb{P} ::= \text{skip} \mid [\mathbb{C}] \mid \mathbb{P}; \mathbb{P} \mid \text{if } (B) \ \mathbb{P} \text{ else } \mathbb{P} \mid \text{while } (B) \ \mathbb{P} \mid \mathbb{P} \parallel \mathbb{P} \\
\\
\mathbb{P}^\uparrow ::= \text{wait}(i) \mid \mathbb{P} \mid \mathbb{P}^\uparrow; \text{wait}(i) \mid
\end{array}$$

We will explain the *commit*, *par* and *wait*, and the rest are straightforward. We give label to each transition, and these labels are only usefully for parallel composition.

The *commit* rule says that a transaction prophesies a starting time when this transaction takes a snapshot h_s and runs locally, and an ending time when it successfully commits ensured by the *allowcommit*.

The *par* rule forks a new thread and appends a $\text{wait}(i)$, parametrised by the new thread identifier i , at the merging point. The *wait* rule waits the thread i until it finishes, then joins the thread and updates the time to the maximum between the two threads. Note that these two rules are labelled with $\text{fork}(i, \mathbb{P})$ or $\text{join}(i, t)$ which are used by the semantics of a top level threadpool.

$$\begin{array}{l}
\iota \in \text{Label} \triangleq \text{id} \mid \text{cmt}(\alpha) \mid \text{fork}(i, \mathbb{P}) \mid \text{join}(i, t) \\
(-, -) \rightsquigarrow_t (-, -) \triangleq (\text{ThreadState} \times \mathbb{P}^\uparrow) \times \text{Label} \times (\text{ThreadState} \times \mathbb{P}^\uparrow) \\
\\
\text{startstate}(h, t) \triangleq \lambda l. v \\
\quad \text{where } \exists t' \leq t. h(l)(t') = \{(v, \mathbf{w}, -)\} \wedge \forall t'' \in (t', t). (-, \mathbf{w}, -) \notin h(l)(t'') \\
\text{allowcommit}(h, ws, rs, t_s, t_e) \triangleq \text{atomicop}(h, ws, rs, t_s, t_e) \wedge \text{consistent}(h, ws, rs, t_s, t_e) \\
\text{atomicop}(h, ws, rs, t_s, t_e) \triangleq \forall l_w \in ws. h(l_w)(t_e) \uparrow \wedge \forall l_r \in rs. (-, \mathbf{w}, -) \notin h(l_r)(t_s) \\
\text{consistent}(h, ws, rs, t_s, t_e) \triangleq \forall l_w \in ws, t \in (t_s, t_e). (-, \mathbf{w}, -) \notin h(l_w)(t) \wedge \\
\quad \forall \alpha. \nexists t_{\alpha s} < t_e, t_{\alpha e} > t_e. h(l_w)(t_{\alpha s}) = (-, \mathbf{r}, \alpha) \wedge h(l_w)(t_{\alpha e}) = (-, \mathbf{w}, \alpha) \wedge \\
\quad \exists t_{\min} = \min(\{t'' \mid t'' > t_e \wedge h(l_w)(t'') \downarrow\}). (-, \mathbf{r}, -) \notin h(l_w)(t_{\min}) \\
\text{commit}(h, h, ws, rs, \alpha, t_s, t_e) \triangleq \lambda l. \begin{cases} h(l) & l \notin ws \cup rs \\ h(l)[t_e \mapsto h(l)(t_e) \uplus \{(h(l), \mathbf{w}, \alpha)\}] & l \in ws \\ h(l)[t_s \mapsto h(l)(t_s) \uplus \{(h(l), \mathbf{r}, \alpha)\}] & l \in rs \end{cases} \\
\text{freshTransId}(h) \triangleq \alpha \text{ where } \alpha \notin \left\{ \alpha' \mid (-, -, \alpha') \in \bigcup_{l, t} h(l)(t) \right\}
\end{array}$$

$$\frac{t_s \geq t \quad t_e > t_s \quad h_s = \text{startstate}(\bar{h}, t_s) \quad (s, h_s, \emptyset, \emptyset), \mathbb{C} \rightsquigarrow_l^* (s', h_e, rs, ws), \text{skip} \quad \text{allowcommit}(\bar{h}, ws, rs, t_s, t_e) \quad \alpha = \text{freshTransId}(\bar{h}) \quad \bar{h}' = \text{commit}(\bar{h}, h_e, ws, rs, \alpha, t_s, t_e)}{(s, \bar{h}, t), [\mathbb{C}] \xrightarrow{\text{cmt}(\alpha)}_t (s', \bar{h}', t_e), \text{skip}} \text{commit}$$

$$\frac{[\mathbb{B}]_s = \text{true}}{(s, \bar{h}, t), \text{if } (\mathbb{B}) \mathbb{P}_1 \text{ else } \mathbb{P}_2 \xrightarrow{\text{id}}_t (s, \bar{h}, t), \mathbb{P}_1} \text{conditiontrue}$$

$$\frac{[\mathbb{B}]_s = \text{false}}{(s, \bar{h}, t), \text{if } (\mathbb{B}) \mathbb{P}_1 \text{ else } \mathbb{P}_2 \xrightarrow{\text{id}}_t (s, \bar{h}, t), \mathbb{P}_2} \text{conditionfalse}$$

$$\frac{[\mathbb{B}]_s = \text{false}}{(s, \bar{h}, t), \text{while } (\mathbb{B}) \mathbb{P} \xrightarrow{\text{id}}_t (s, \bar{h}, t), \text{skip}} \text{norep}$$

$$\frac{[\mathbb{B}]_s = \text{true}}{(s, \bar{h}, t), \text{while } (\mathbb{B}) \mathbb{P} \xrightarrow{\text{id}}_t (s, \bar{h}, t), \mathbb{P}; \text{while } (\mathbb{B}) \mathbb{P}} \text{rep}$$

$$\frac{}{(s, \bar{h}, t), \text{skip}; \mathbb{P}^\uparrow \xrightarrow{\text{id}}_t (s, \bar{h}, t), \mathbb{P}^\uparrow} \text{seqskip}$$

$$\frac{(s, \bar{h}, t), \mathbb{P}_1^\uparrow \xrightarrow{t}_t (s', \bar{h}', t'), \mathbb{P}_1^{\uparrow'}}{(s, \bar{h}, t), \mathbb{P}_1^\uparrow; \mathbb{P}_2^\uparrow \xrightarrow{t}_t (s', \bar{h}', t'), \mathbb{P}_1^{\uparrow'}; \mathbb{P}_2^{\uparrow'}} \text{seqnoskip}$$

$$\frac{}{(s, \bar{h}, t), \mathbb{P}_1 \parallel \mathbb{P}_2 \xrightarrow{\text{fork}(i, \mathbb{P}_2)}_t (s, \bar{h}, t), \mathbb{P}_1; \text{wait}(i)} \text{par}$$

$$\frac{}{(s, \bar{h}, t), \text{wait}(i) \xrightarrow{\text{join}(i, t')}_t (s, \bar{h}, \max\{t, t'\}), \text{skip}} \text{wait}$$

$$- \rightsquigarrow_g - \triangleq \text{State} \times \text{Label} \times \text{State}$$

The semantics of theadpool picks a thread to runs one step. If the step is a fork, it generates a new thread with a new stack and a local time that is the same as its parent thread. If it is a join, the threadpool passes the child's local time to its parent thread.

$$\frac{(s, \bar{h}, t), \mathbb{P}^\uparrow \xrightarrow{t}_t (s', \bar{h}', t'), \mathbb{P}^{\uparrow'} \quad \iota \in \{\text{id}, \text{cmt}(-)\}}{(\bar{h}, \eta \uplus \{i \mapsto (s, t, \mathbb{P}^\uparrow)\}) \xrightarrow{t}_g (\bar{h}', \eta \uplus \{i \mapsto (s', t', \mathbb{P}^{\uparrow'})\})} \text{single}$$

$$\frac{(s, \bar{h}, t), \mathbb{P}^\uparrow \xrightarrow{\text{fork}(i', \mathbb{P}'')}_t (s', \bar{h}', t'), \mathbb{P}^{\uparrow'}}{(\bar{h}, \eta \uplus \{i \mapsto (s, t, \mathbb{P}^\uparrow)\}) \xrightarrow{\text{fork}(i', \mathbb{P}'')}_g (\bar{h}', \eta \uplus \{i \mapsto (s', t', \mathbb{P}^{\uparrow'}), i' \mapsto (\lambda x. 0, t', \mathbb{P}'')\})} \text{par}$$

$$\frac{(s, \bar{h}, t), \mathbb{P}^\uparrow \xrightarrow{\text{join}(i', t'')}_t (s', \bar{h}', t'), \mathbb{P}^{\uparrow'}}{(\bar{h}, \eta \uplus \{i \mapsto (s, t, \mathbb{P}^\uparrow), i' \mapsto (s', t'', \text{skip})\}) \xrightarrow{\text{join}(i', t'')}_g (\bar{h}', \eta \uplus \{i \mapsto (s', t', \mathbb{P}^{\uparrow'})\})} \text{wait}$$

2 Proof of semantics

Lemma 2.1. A history cannot be overwritten, i.e. $\forall \bar{h}, \bar{h}', l, t. (\bar{h}, -) \rightsquigarrow_g (\bar{h}', -) \implies \bar{h}(l)(t) \subseteq \bar{h}'(l)(t)$

Proof. From the `allowcommit`. □

Lemma 2.2. For any time, it can have at most one write event and, if it has then no read events happen in the same time. This is $\forall \bar{h}, l, t. (-, \mathbf{w}, -) \in \bar{h}(l)(t) \implies \bar{h}(l)(t) = \{(-, \mathbf{w}, -)\}$

Proof. From the `allowcommit`. □

Lemma 2.3. All the reads of a transaction happen before all the writes. This is $\forall \bar{h}, l, l', t, t', \alpha. \bar{h}(l)(t) = (-, \mathbf{r}, \alpha) \wedge \bar{h}(l')(t') = (-, \mathbf{w}, \alpha) \implies t < t'$.

Proof. From the semantics that $t_s < t_e$. □

Lemma 2.4. All the reads of a transaction happen in the same time, so do all the writes. This is $\forall \bar{h}, l, l', t, t', \alpha, o \in \{\mathbf{r}, \mathbf{w}\}. \bar{h}(l)(t) = \bar{h}(l')(t') = (-, o, \alpha) \implies t = t'$.

Proof. From the `commit`. □

First we define session order `so`, or program order.

$$\begin{aligned}
 lc \in \text{LastCommit} &\triangleq \text{ThreadID} \rightarrow \mathcal{P}(\text{TransID}) \\
 sessionOrder^0(\bar{h}_{init}, \eta_{init}) &\triangleq \{(\emptyset, \emptyset, \bar{h}_{init}, \eta_{init}, \emptyset)\} \\
 sessionOrder^n(\bar{h}_{init}, \eta_{init}) &\triangleq \left\{ (\mathcal{T}, \mathbf{so}, \bar{h}, \eta \uplus \{i \mapsto -\}, lc) \mid \begin{aligned} &\exists \mathcal{T}', \mathbf{so}', \bar{h}', \eta', lc', \iota. \\ &(\mathcal{T}', \mathbf{so}', \bar{h}', \eta' \uplus \{i \mapsto -\}, lc') \in \\ &\quad sessionOrder^{n-1}(\eta_{init}, \bar{h}_{init}) \wedge \\ &(\bar{h}', \eta' \uplus \{i \mapsto -\}) \rightsquigarrow_g (\bar{h}, \eta \uplus \{i \mapsto -\}) \wedge \\ &\iota = \text{id} \implies \\ &(\mathcal{T} = \mathcal{T}' \wedge \mathbf{so} = \mathbf{so}' \wedge \eta = \eta' \wedge lc = lc') \wedge \\ &\exists \alpha. \iota = \text{cmt}(\alpha) \implies \\ &\left(\mathcal{T} = \mathcal{T}' \uplus \{\alpha\} \wedge \right. \\ &\quad \left. \mathbf{so} = \mathbf{so}' \uplus \{(\alpha', \alpha) \mid \alpha' \in lc'(i)\} \wedge \right. \\ &\quad \left. \eta = \eta' \wedge lc = lc'[i \mapsto \{\alpha\}] \right) \wedge \\ &\exists i'. \iota = \text{fork}(i', -) \implies \\ &\left(\mathcal{T} = \mathcal{T}' \wedge \mathbf{so} = \mathbf{so}' \wedge \eta = \eta' \uplus \{i' \mapsto -\} \wedge \right) \wedge \\ &\quad \left(lc = lc' \uplus \{i' \mapsto lc'(i)\} \right) \\ &\exists i''. \iota = \text{join}(i'', -) \implies \\ &\left(\mathcal{T} = \mathcal{T}' \wedge \mathbf{so} = \mathbf{so}' \wedge \eta = \eta' \uplus \{i'' \mapsto -\} \wedge \right) \wedge \\ &\quad \left(lc = lc'[i \mapsto lc'(i) \uplus lc'(i'')] \setminus \{i'' \mapsto -\} \right) \end{aligned} \right\} \\
 histories(\bar{h}_{init}, \eta_{init}) &\triangleq \left\{ (\mathcal{T}, \mathbf{so}, \bar{h}) \mid (\mathcal{T}, \mathbf{so}, \bar{h}, -, -) \in \bigcup_{n \in \mathbb{N}} sessionOrder^n(\bar{h}_{init}, \eta_{init}) \right\}
 \end{aligned}$$

Lemma 2.5. Transactions ordered by session order `so` appear in the same order in the time stamp heap, i.e. $\forall \bar{h}_{init}, \eta_{init}, (\mathcal{T}, \mathbf{so}, \bar{h}) \in histories(\bar{h}_{init}, \eta_{init}), \alpha, \alpha' \in \mathcal{T}, l. \exists t, t'. \bar{h}(l)(t) = (-, -, \alpha) \wedge \bar{h}(l)(t') = (-, -, \alpha') \implies t < t'$

Proof. From the definition of session order and the semantics, especially `commit`, `seqnoskip`, `par` and `wait`. □

Now we need to recover `ww`, `wr` and `rw` from `h`. We need to prove that there is no circle that does not contain contain adjacent `rw`. By contradiction, if such circle exists, we can prove that the time goes backward in the circle.

$$\begin{aligned}
 graph(\mathcal{T}, \mathbf{so}, \bar{h}) &\triangleq (\mathcal{T}, \mathbf{so}, \mathbf{wr}, \mathbf{ww}, \mathbf{rw}) \\
 &\text{where } \forall \alpha, \alpha' \in \mathcal{T}. \alpha \neq \alpha' \wedge \exists l, t, t' > t. \\
 &(\alpha, \alpha') \in \mathbf{wr} \implies t = \max(\{t'' \mid t'' < t\}), v. \bar{h}(l)(t) = (-, \mathbf{w}, \alpha) \wedge \bar{h}(l)(t') = (-, \mathbf{r}, \alpha') \wedge \\
 &(\alpha, \alpha') \in \mathbf{ww} \implies \bar{h}(l)(t) = (-, \mathbf{w}, \alpha) \wedge \bar{h}(l)(t') = (-, \mathbf{w}, \alpha') \wedge \\
 &(\alpha, \alpha') \in \mathbf{rw} \implies \bar{h}(l)(t) = (-, \mathbf{r}, \alpha) \wedge \bar{h}(l)(t') = (-, \mathbf{w}, \alpha')
 \end{aligned}$$

Lemma 2.6. Given threes transactions, α_1 , α_2 and α_3 , if there is a order between α_1 and α_2 , so does α_2 and α_3 , and at least one of these two orders are not read-write relation, then the time, associated with the events that determines the orders, strictly increases. The ABOVE NOT REALLY DESCRIBE THE PROPERTY!!! BUT LEAVE IT FOR NOW. BECAUSE MIGHT CHANGE THE PROOF STRATEGY.

This is, given $(\mathcal{T}, \text{so}, \text{wr}, \text{ww}, \text{rw})$ generated from h , it satisfies the following property:

$$\begin{aligned}
& \forall \alpha_1, \alpha_2, \alpha_3 \in \mathcal{T}. \exists \iota, \iota' \in \{\text{so}, \text{wr}, \text{ww}, \text{rw}\}, l_1, l_2, l_3, t_1, t_2, t_3, o_1, o_2, o_3. (\alpha_1, \alpha_2) \in \iota \wedge (\alpha_2, \alpha_3) \in \iota' \wedge \\
& h(l_1)(t_1) = (-, o_1, \alpha_1) \wedge h(l_2)(t_2) = (-, o_2, \alpha_2) \wedge h(l_3)(t_3) = (-, o_3, \alpha_3) \wedge t_1 < t_2 < t_3 \wedge \\
& \iota = \text{so} \wedge \iota' = \text{so} \implies l_2 = l_3 \wedge o_1, o_2, o_3 \in \{\text{w}, \text{r}\} \wedge \\
& \iota = \text{so} \wedge \iota' = \text{ww} \implies l_2 = l_3 \wedge o_1 \in \{\text{w}, \text{r}\} \wedge o_2 = o_3 = \text{w} \wedge \\
& \iota = \text{so} \wedge \iota' = \text{wr} \implies l_2 = l_3 \wedge o_1 \in \{\text{w}, \text{r}\} \wedge o_2 = \text{w} \wedge o_3 = \text{r} \wedge \\
& \iota = \text{so} \wedge \iota' = \text{rw} \implies l_2 = l_3 \wedge o_1 \in \{\text{w}, \text{r}\} \wedge o_2 = \text{r} \wedge o_3 = \text{w} \wedge \\
& \iota = \text{ww} \wedge \iota' = \text{so} \implies l_1 = l_2 \wedge o_1 = o_2 = \text{w} \wedge o_3 \in \{\text{w}, \text{r}\} \wedge \\
& \iota = \text{ww} \wedge \iota' = \text{ww} \implies l_1 = l_2 \wedge o_1 = o_2 = o_3 = \text{w} \wedge \\
& \iota = \text{ww} \wedge \iota' = \text{wr} \implies l_1 = l_2 \wedge o_1 = o_2 = \text{w} \wedge o_3 = \text{r} \wedge \\
& \iota = \text{ww} \wedge \iota' = \text{rw} \implies l_2 = l_3 \wedge o_1 = \text{w} \wedge o_2 = \text{r} \wedge o_3 = \text{w} \wedge \\
& \iota = \text{wr} \wedge \iota' = \text{so} \implies l_1 = l_2 \wedge o_1 = \text{w} \wedge o_2 = \text{r} \wedge o_3 \in \{\text{w}, \text{r}\} \wedge \\
& \iota = \text{wr} \wedge \iota' = \text{ww} \implies l_2 = l_3 \wedge o_1 = o_2 = o_3 = \text{w} \wedge \\
& \iota = \text{wr} \wedge \iota' = \text{wr} \implies l_2 = l_3 \wedge o_1 = o_2 = \text{w} \wedge o_3 = \text{r} \wedge \\
& \iota = \text{wr} \wedge \iota' = \text{rw} \implies l_2 = l_3 \wedge o_1 = \text{w} \wedge o_2 = \text{r} \wedge o_3 = \text{w} \wedge \\
& \iota = \text{rw} \wedge \iota' = \text{so} \implies l_1 = l_2 \wedge o_1 = \text{r} \wedge o_2 = \text{w} \wedge o_3 \in \{\text{w}, \text{r}\} \wedge \\
& \iota = \text{rw} \wedge \iota' = \text{ww} \implies l_2 = l_3 \wedge o_1 = \text{r} \wedge o_2 = o_3 = \text{w} \wedge \\
& \iota = \text{rw} \wedge \iota' = \text{wr} \implies l_2 = l_3 \wedge o_1 = \text{r} \wedge o_2 = \text{w} \wedge o_3 = \text{r}
\end{aligned}$$

3 blabla

$$P, Q \in \text{GlobalAssertion} \triangleq \begin{array}{c|c|c|c|c|c} \text{False} & \text{True} & \text{emp} & x \mapsto v & P * Q & \\ \hline P \wedge Q & P \vee Q & \exists x. P & P \implies Q & & \end{array}$$

$$p, q \in \text{LocalAssertion} \triangleq \begin{array}{c|c|c|c|c|c|c} \text{False} & \text{True} & \text{emp} & x \mapsto_w v & x \mapsto_r v & p * q & \\ \hline p \wedge q & p \vee q & \exists x. p & p \implies q & & & \end{array}$$

$$\begin{aligned} a \in \text{Action} &\triangleq \{p \rightsquigarrow q \mid p, q \in \text{LocalAssertion} \wedge p \text{ has no write tag}\} \\ a \in \text{Action} &\triangleq p \rightsquigarrow q \mid a; \text{Action} \end{aligned}$$

Logic expression

$$\llbracket e \rrbracket_\iota$$

Assume there are special logical variables, ranging l_1, l_2, \dots , that point to the corresponding locations in the global heap.

$$\begin{aligned} \llbracket \text{False} \rrbracket_\iota &\triangleq \emptyset \\ \llbracket \text{True} \rrbracket_\iota &\triangleq \text{Heap} \\ \llbracket x \mapsto v \rrbracket_\iota &\triangleq \{h \mid \exists t. h(\llbracket x \rrbracket_\iota) = (\llbracket v \rrbracket_\iota, t)\} \\ \llbracket P * Q \rrbracket_\iota &\triangleq \{h_P \uplus h_Q \mid h_P \in \llbracket P \rrbracket_\iota \wedge h_Q \in \llbracket Q \rrbracket_\iota\} \\ \llbracket P \wedge Q \rrbracket_\iota &\triangleq \{h \mid h \in \llbracket P \rrbracket_\iota \wedge h \in \llbracket Q \rrbracket_\iota\} \\ \llbracket P \vee Q \rrbracket_\iota &\triangleq \{h \mid h \in \llbracket P \rrbracket_\iota \vee h \in \llbracket Q \rrbracket_\iota\} \\ \llbracket \exists x. P \rrbracket_\iota &\triangleq \{h \mid \exists v. h \in \llbracket P \rrbracket_{\iota[x \mapsto v]}\} \\ \llbracket P \implies Q \rrbracket_\iota &\triangleq \{h \mid h \in \llbracket P \rrbracket_\iota \implies h \in \llbracket Q \rrbracket_\iota\} \\ \llbracket \text{False} \rrbracket_{s,\iota} &\triangleq \emptyset \\ \llbracket \text{True} \rrbracket_{s,\iota} &\triangleq \text{Heap} \times \text{Heap} \\ \llbracket \text{emp} \rrbracket_{s,\iota} &\triangleq \{(\emptyset, \emptyset)\} \\ \llbracket x \mapsto_r v \rrbracket_{s,\iota} &\triangleq \{(h_s, h_e) \mid \exists t. h_s(\llbracket x \rrbracket_{s,\iota}) = (\llbracket v \rrbracket_{s,\iota}, t) \wedge h_e = (\llbracket v \rrbracket_{s,\iota}, t)\} \\ \llbracket x \mapsto_w v \rrbracket_{s,\iota} &\triangleq \{(h_s, h_e) \mid \exists v_s, t_s, t_e. h_s(\llbracket x \rrbracket_{s,\iota}) = (v_s, t_s) \wedge h_e = (\llbracket v \rrbracket_{s,\iota}, t_e) \wedge t_s < t_e\} \\ \llbracket p * q \rrbracket_{s,\iota} &\triangleq \{(h_{ps} \uplus h_{qs}, h_{pe} \uplus h_{qe}) \mid (h_{ps}, h_{pe}) \in \llbracket p \rrbracket_{s,\iota} \wedge (h_{qs}, h_{qe}) \in \llbracket q \rrbracket_{s,\iota}\} \\ \llbracket p \wedge q \rrbracket_{s,\iota} &\triangleq \{(h_s, h_e) \mid (h_s, h_e) \in \llbracket p \rrbracket_{s,\iota} \wedge (h_s, h_e) \in \llbracket q \rrbracket_{s,\iota}\} \\ \llbracket p \vee q \rrbracket_{s,\iota} &\triangleq \{(h_s, h_e) \mid (h_s, h_e) \in \llbracket p \rrbracket_{s,\iota} \vee (h_s, h_e) \in \llbracket q \rrbracket_{s,\iota}\} \\ \llbracket \exists x. p \rrbracket_{s,\iota} &\triangleq \{(h_s, h_e) \mid \exists v. (h_s, h_e) \in \llbracket p \rrbracket_{s,\iota[x \mapsto v]}\} \\ \llbracket p \implies q \rrbracket_{s,\iota} &\triangleq \{(h_s, h_e) \mid (h_s, h_e) \in \llbracket p \rrbracket_{s,\iota} \implies (h_s, h_e) \in \llbracket q \rrbracket_{s,\iota}\} \\ \text{True} \hat{*} p &\triangleq p \\ \text{False} \hat{*} - &\triangleq \text{False} \\ x \mapsto_r v \hat{*} x \mapsto_r v &\triangleq x \mapsto_r v \\ x \mapsto_r - \hat{*} x \mapsto_w v &\triangleq x \mapsto_w v \\ (p_1 * q_1) \hat{*} (p_2 * q_2) &\triangleq (p_1 \hat{*} p_2) * (q_1 \hat{*} q_2) \\ (p \wedge q) \hat{*} r &\triangleq (p \hat{*} r) \wedge (q \hat{*} r) \\ (p \vee q) \hat{*} r &\triangleq (p \hat{*} r) \vee (q \hat{*} r) \\ (p \vee q) \hat{*} r &\triangleq (p \hat{*} r) \vee (q \hat{*} r) \\ \exists x. p \hat{*} p &\triangleq \exists x. (p \hat{*} r) \\ (p \implies q) \hat{*} r &\triangleq (p \hat{*} r) \implies (q \hat{*} r) \\ \vdash p, p' \text{ agree} &\iff \exists r, r', r'' \neq \text{False}. p * r \hat{*} p' * r' = r'' \\ R \vdash p, q \text{ merge } q' &\iff \forall p_R \rightsquigarrow q_R \in R. \vdash p, p_R \text{ agree} \wedge \vdash q, q_R \text{ agree} \\ &\implies (\exists r, r', r_R. (q * r) \hat{*} (q_R * r_R) \implies q' * r' \wedge q \implies q') \\ R \vdash q \text{ stable} &\iff \forall p_R \rightsquigarrow q_R \in R. \exists p. q \xrightarrow{\text{retag } r} p \wedge \vdash p, p_R \text{ agree} \wedge \\ &\exists r, r_R. (q * r) \hat{*} (q_R * r_R) = q * r \end{aligned}$$

$$\frac{P \xrightarrow{\text{tag } r} p \vdash \{p\} \mathbb{C} \{q\} \quad (p \rightsquigarrow q) \in G^* \quad R \vdash p, q \text{ merge } q' \quad R \vdash q' \text{ stable} \quad q' \xrightarrow{\text{notag}} Q}{R, G \vdash \{P\} \mathbb{C} \{Q\}} \text{ commit}$$

$$\begin{array}{c}
\frac{R, G \vdash \{P \wedge \mathbb{B}\} \mathbb{P}_1 \{Q\} \quad R, G \vdash \{P \wedge \neg \mathbb{B}\} \mathbb{P}_2 \{Q\}}{R, G \vdash \{P\} \text{ if } (\mathbb{B}) \mathbb{P}_1 \text{ else } \mathbb{P}_2 \{Q\}} \text{ choice} \\
\\
\frac{R, G \vdash \{P\} \mathbb{P}_1 \{Q'\} \quad Q' \implies P' \quad R, G \vdash \{P'\} \mathbb{P}_2 \{Q\}}{R, G \vdash \{P\} \mathbb{P}_1 ; \mathbb{P}_2 \{Q\}} \text{ seq} \\
\\
\frac{R \cup G_2, G_1 \vdash \{P_1\} \mathbb{P}_1 \{Q_1\} \quad R \cup G_1, G_2 \vdash \{P_2\} \mathbb{P}_2 \{Q_2\} \quad R \cup G_2 \vdash P_1 \text{ stable} \quad R \cup G_1 \vdash P_2 \text{ stable}}{R, G_1 \cup G_2 \vdash \{P_1 * P_2\} \mathbb{P}_1 \parallel \mathbb{P}_2 \{Q_1 * Q_2\}} \text{ concur} \\
\\
\frac{R, G \vdash \{P \wedge \mathbb{B}\} \mathbb{P} \{P\}}{R, G \vdash \{P\} \text{ while } (\mathbb{B}) \mathbb{P} \{P \wedge \neg \mathbb{B}\}} \text{ repeat}
\end{array}$$

4 blablabla

We use $l[-]$ to denote a location in either global or local heap.

$$\begin{array}{c}
\textcolor{red}{R} : l_x \mapsto_r - * l_y \mapsto_r 0 \rightsquigarrow l_x \mapsto_w 1 * l_y \mapsto_r 0 \\
\left[\begin{array}{l}
\{l_x \mapsto 0 * l_y \mapsto 0 \vee l_x \mapsto 1 * l_y \mapsto 0\} \\
\left\{ \begin{array}{l} l_x \mapsto_r 0 * l_y \mapsto_r 0 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 0 \end{array} \right\} \\
x := [l_x]; \\
\left\{ \begin{array}{l} l_x \mapsto_r 0 * l_y \mapsto_r 0 \wedge x = 0 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 0 \wedge x = 1 \end{array} \right\} \\
\text{if } (x = 0) \\
\quad [l_y] := 1; \\
\left\{ \begin{array}{l} l_x \mapsto_r 0 * l_y \mapsto_w 1 \wedge x = 0 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 0 \wedge x = 1 \end{array} \right\} \\
\text{MERGE} \\
\left\{ \begin{array}{l} l_x \mapsto_r 0 * l_y \mapsto_w 1 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 0 \vee \\ l_x \mapsto_w 1 * l_y \mapsto_w 1 \vee \\ l_x \mapsto_w 1 * l_y \mapsto_r 0 \end{array} \right\} \\
\left\{ \begin{array}{l} l_x \mapsto 0 * l_y \mapsto 1 \vee \\ l_x \mapsto 1 * l_y \mapsto 0 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 \end{array} \right\}
\end{array} \right] \\
\{l_x \mapsto 0 * l_y \mapsto 1 \vee l_x \mapsto 1 * l_y \mapsto 0 \vee l_x \mapsto 1 * l_y \mapsto 1\}
\end{array}
\quad \parallel \quad
\begin{array}{c}
\textcolor{red}{R} : l_x \mapsto_r 0 * l_y \mapsto_r - \rightsquigarrow l_x \mapsto_r 0 * l_y \mapsto_w 1 \\
\left[\begin{array}{l}
\{l_x \mapsto 0 * l_y \mapsto 0 \vee l_x \mapsto 0 * l_y \mapsto 1\} \\
\left\{ \begin{array}{l} l_x \mapsto_r 0 * l_y \mapsto_r 0 \vee \\ l_x \mapsto_r 0 * l_y \mapsto_r 1 \end{array} \right\} \\
y := [l_y]; \\
\text{if } (y = 0) \\
\quad [l_x] := 1; \\
\left\{ \begin{array}{l} l_x \mapsto_w 1 * l_y \mapsto_r 0 \vee \\ l_x \mapsto_r 0 * l_y \mapsto_r 1 \end{array} \right\} \\
\text{MERGE} \\
\left\{ \begin{array}{l} l_x \mapsto_w 1 * l_y \mapsto_r 0 \vee \\ l_x \mapsto_r 0 * l_y \mapsto_r 1 \vee \\ l_x \mapsto_w 1 * l_y \mapsto_w 1 \vee \\ l_x \mapsto_r 0 * l_y \mapsto_w 1 \end{array} \right\} \\
\left\{ \begin{array}{l} l_x \mapsto 0 * l_y \mapsto 1 \vee \\ l_x \mapsto 1 * l_y \mapsto 0 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 \end{array} \right\}
\end{array} \right]
\end{array}$$

***The second rely might be useful in term of killing the possible states of other thread, even though it is irrelevant for the current thread. About the problem, an ugly solution is: if the resource appear in rely, current thread should keep it whether uses it or not. However this solution is against the idea of separation logic, i.e. we do not need to take care of those resource untouched.

$$\begin{array}{c}
l_y \mapsto_r - \rightsquigarrow l_y \mapsto_w 1 \\
R : l_x \mapsto_r x * l_y \mapsto_r y * l_{tx2} \mapsto_r - * l_{ty2} \mapsto_r - \\
\rightsquigarrow l_x \mapsto_r x * l_y \mapsto_r y * l_{tx2} \mapsto_w x * l_{ty2} \mapsto_w y \\
\left\{ \begin{array}{l} l_x \mapsto 0 * l_y \mapsto 0 * l_{tx1} \mapsto 0 * l_{ty1} \mapsto 0 \vee \\ l_x \mapsto 0 * l_y \mapsto 1 * l_{tx1} \mapsto 0 * l_{ty1} \mapsto 0 \end{array} \right\} \\
[[l_x] := 1;] \\
\left\{ \begin{array}{l} l_x \mapsto 1 * l_y \mapsto 0 * l_{tx1} \mapsto 0 * l_{ty1} \mapsto 0 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 * l_{tx1} \mapsto 0 * l_{ty1} \mapsto 0 \end{array} \right\} \\
\left[\begin{array}{l} \left\{ \begin{array}{l} l_x \mapsto_r 1 * l_y \mapsto_r 0 * l_{tx1} \mapsto_r 0 * l_{ty1} \mapsto_r 0 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 1 * l_{tx1} \mapsto_r 0 * l_{ty1} \mapsto_r 0 \end{array} \right\} \\ \mathbf{x} := [l_x]; \\ [l_{tx1}] := \mathbf{x}; \\ \mathbf{y} := [l_y]; \\ [l_{ty1}] := \mathbf{y}; \\ \left\{ \begin{array}{l} l_x \mapsto_r 1 * l_y \mapsto_r 0 * l_{tx1} \mapsto_w 1 * l_{ty1} \mapsto_w 0 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 1 * l_{tx1} \mapsto_w 1 * l_{ty1} \mapsto_w 1 \end{array} \right\} \\ \text{MERGE} \\ \left\{ \begin{array}{l} l_x \mapsto_r 1 * l_y \mapsto_r 0 * l_{tx1} \mapsto_w 1 * l_{ty1} \mapsto_w 0 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 1 * l_{tx1} \mapsto_w 1 * l_{ty1} \mapsto_w 1 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_w 1 * l_{tx1} \mapsto_w 1 * l_{ty1} \mapsto_w 0 \end{array} \right\} \\ \left\{ \begin{array}{l} l_x \mapsto 1 * l_y \mapsto 0 * l_{tx1} \mapsto 1 * l_{ty1} \mapsto 0 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 * l_{tx1} \mapsto 1 * l_{ty1} \mapsto 1 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 * l_{tx1} \mapsto 1 * l_{ty1} \mapsto 0 \end{array} \right\} \end{array} \right] \\
\left\{ \begin{array}{l} l_x \mapsto 1 * l_y \mapsto 1 * \\ \left(\begin{array}{l} l_{tx1} \mapsto 1 * l_{ty1} \mapsto 0 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 1 \vee \\ l_{tx1} \mapsto 1 * l_{ty1} \mapsto 0 * l_{tx2} \mapsto 1 * l_{ty2} \mapsto 1 \vee \\ l_{tx1} \mapsto 1 * l_{ty1} \mapsto 1 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 1 \vee \\ l_{tx1} \mapsto 1 * l_{ty1} \mapsto 1 * l_{tx2} \mapsto 1 * l_{ty2} \mapsto 1 \end{array} \right) \end{array} \right\}
\end{array}
\quad \Bigg| \quad
\begin{array}{c}
l_x \mapsto_r - \rightsquigarrow l_x \mapsto_w 1 \\
R : l_x \mapsto_r x * l_y \mapsto_r y * l_{tx1} \mapsto_r - * l_{ty1} \mapsto_r - \\
\rightsquigarrow l_x \mapsto_r x * l_y \mapsto_r y * l_{tx1} \mapsto_w x * l_{ty1} \mapsto_w y \\
\left\{ \begin{array}{l} l_x \mapsto 0 * l_y \mapsto 0 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 0 \vee \\ l_x \mapsto 1 * l_y \mapsto 0 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 0 \end{array} \right\} \\
[[l_y] := 1;] \\
\left\{ \begin{array}{l} l_x \mapsto 0 * l_y \mapsto 1 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 0 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 0 \end{array} \right\} \\
\left[\begin{array}{l} \mathbf{x} := [l_x]; \\ [l_{tx2}] := \mathbf{x}; \\ \mathbf{y} := [l_y]; \\ [l_{ty2}] := \mathbf{y}; \\ \text{MERGE} \\ \left\{ \begin{array}{l} l_x \mapsto_r 0 * l_y \mapsto_r 1 * l_{tx2} \mapsto_w 0 * l_{ty2} \mapsto_w 1 \vee \\ l_x \mapsto_r 1 * l_y \mapsto_r 1 * l_{tx2} \mapsto_w 1 * l_{ty2} \mapsto_w 1 \vee \\ l_x \mapsto_w 1 * l_y \mapsto_r 1 * l_{tx2} \mapsto_w 0 * l_{ty2} \mapsto_w 1 \end{array} \right\} \\ \left\{ \begin{array}{l} l_x \mapsto 0 * l_y \mapsto 1 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 1 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 * l_{tx2} \mapsto 1 * l_{ty2} \mapsto 1 \vee \\ l_x \mapsto 1 * l_y \mapsto 1 * l_{tx2} \mapsto 0 * l_{ty2} \mapsto 1 \end{array} \right\} \end{array} \right]
\end{array}$$

Above, the 1 0 0 1 case will never happen. This is called long fork in snapshot isolation, for a single machine snapshot isolation, it should not happen.

We use $(-, -, -, -, -, -)$ to refer $x, y, tx1, ty1, tx2, ty2$.

$$\begin{array}{c}
l_y \mapsto_r - \rightsquigarrow l_y \mapsto_w 1 \\
R : l_x \mapsto_r x * l_y \mapsto_r y * l_{tx2} \mapsto_r - * l_{ty2} \mapsto_r - \\
\rightsquigarrow l_x \mapsto_r x * l_y \mapsto_r y * l_{tx2} \mapsto_w x * l_{ty2} \mapsto_w y \\
\left\{ \begin{array}{l} (0, 0, 0, 0, 0, 0) \vee (0, 1, 0, 0, 0, 0) \vee (0, 1, 0, 0, 0, 1) \\ (0, 0, 0, 0, 0, 0) \vee (0, 1, 0, 0, 0, 0) \vee (0, 1, 0, 0, 0, 1) \end{array} \right\} \\
[[l_x] := 1;] \\
\left\{ \begin{array}{l} (1, 0, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 1) \end{array} \right\} \\
\text{MERGE} \\
\left\{ \begin{array}{l} (1, 0, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 1) \\ (1, 0, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 1) \vee \\ (1, 0, 0, 0, 1, 0) \vee (1, 1, 0, 0, 1, 1) \\ (1, 0, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 1) \vee \\ (1, 0, 0, 0, 1, 0) \vee (1, 1, 0, 0, 1, 1) \end{array} \right\} \\
\mathbf{x} := [l_x]; \\
[l_{tx1}] := \mathbf{x}; \\
\mathbf{y} := [l_y]; \\
[l_{ty1}] := \mathbf{y}; \\
\left\{ \begin{array}{l} (1, 0, 1, 0, 0, 0) \vee (1, 1, 1, 1, 0, 0) \vee (1, 1, 1, 1, 0, 1) \vee \\ (1, 0, 1, 0, 1, 0) \vee (1, 1, 1, 1, 1, 1) \end{array} \right\} \\
\text{MERGE} \\
\left\{ \begin{array}{l} (1, 0, 1, 0, 0, 0) \vee (1, 1, 1, 1, 0, 0) \vee (1, 1, 1, 1, 0, 1) \vee \\ (1, 0, 1, 0, 1, 0) \vee (1, 1, 1, 1, 1, 1) \vee (1, 1, 1, 0, 0, 0) \\ (1, 0, 1, 0, 0, 0) \vee (1, 1, 1, 1, 0, 0) \vee (1, 1, 1, 1, 0, 1) \vee \\ (1, 0, 1, 0, 1, 0) \vee (1, 1, 1, 1, 1, 1) \vee (1, 1, 1, 0, 0, 0) \vee \\ (1, 1, 1, 0, 1, 1) \end{array} \right\} \\
\left\{ (1, 1, 1, 0, 1, 1) \vee (1, 1, 1, 1, 1, 1) \vee (1, 1, 1, 1, 0, 1) \right\}
\end{array}
\quad \Bigg| \quad
\begin{array}{c}
l_x \mapsto_r - \rightsquigarrow l_x \mapsto_w 1 \\
R : l_x \mapsto_r x * l_y \mapsto_r y * l_{tx1} \mapsto_r - * l_{ty1} \mapsto_r - \\
\rightsquigarrow l_x \mapsto_r x * l_y \mapsto_r y * l_{tx1} \mapsto_w x * l_{ty1} \mapsto_w y \\
\left\{ \begin{array}{l} (0, 0, 0, 0, 0, 0) \vee (1, 0, 0, 0, 0, 0) \vee (1, 0, 1, 0, 0, 0) \\ (0, 0, 0, 0, 0, 0) \vee (1, 0, 0, 0, 0, 0) \vee (1, 0, 1, 0, 0, 0) \end{array} \right\} \\
[[l_y] := 1;] \\
\left\{ \begin{array}{l} (0, 1, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 0) \vee (1, 1, 1, 0, 0, 0) \\ (0, 1, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 0) \vee (1, 1, 1, 0, 0, 0) \vee \\ (0, 1, 0, 1, 0, 0) \vee (1, 1, 1, 1, 0, 0) \\ (0, 1, 0, 0, 0, 0) \vee (1, 1, 0, 0, 0, 0) \vee (1, 1, 1, 0, 0, 0) \vee \\ (0, 1, 0, 1, 0, 0) \vee (1, 1, 1, 1, 0, 0) \end{array} \right\} \\
\mathbf{x} := [l_x]; \\
[l_{tx2}] := \mathbf{x}; \\
\mathbf{y} := [l_y]; \\
[l_{ty2}] := \mathbf{y}; \\
\left\{ \begin{array}{l} (0, 1, 0, 0, 0, 1) \vee (1, 1, 0, 0, 1, 1) \vee (1, 1, 1, 0, 1, 1) \vee \\ (0, 1, 0, 1, 0, 1) \vee (1, 1, 1, 1, 1, 1) \end{array} \right\} \\
\text{MERGE} \\
\left\{ \begin{array}{l} (0, 1, 0, 0, 0, 1) \vee (1, 1, 0, 0, 1, 1) \vee (1, 1, 1, 0, 1, 1) \vee \\ (0, 1, 0, 1, 0, 1) \vee (1, 1, 1, 1, 1, 1) \vee (1, 1, 0, 0, 0, 1) \\ (0, 1, 0, 0, 0, 1) \vee (1, 1, 0, 0, 1, 1) \vee (1, 1, 1, 0, 1, 1) \vee \\ (0, 1, 0, 1, 0, 1) \vee (1, 1, 1, 1, 1, 1) \vee (1, 1, 0, 0, 0, 1) \vee \\ (1, 1, 1, 1, 0, 1) \end{array} \right\}
\end{array}$$

$$\begin{bmatrix} [l_x] := 1; \\ [l_z] := 1; \end{bmatrix} \parallel \parallel \begin{bmatrix} [l_y] := 2; \\ [l_z] := 2; \end{bmatrix}$$