

Figure 1: The abstract execution \mathcal{X} is a element of the unit set $\mathbf{1}_{\mathcal{X}}$, and here $\mathcal{X} \bullet_{\mathcal{X}} \mathcal{X}' = \mathcal{X}'$ as the two abstract executions have the same structure the compositions of each nodes exist.

0.1 Program Semantics

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To model the global machine states, instead of heap-based states, we use abstract executions (Def. 0.1). A abstract execution is a graph where each node represents a committed transaction with a unique transaction identifier, and its associated events that have global effect, i.e. the first reads and last writes. There are three types of edges in the graph, a program order that is a total order for transactions from the same thread, a visibility relation that decides the observable history (a set of transactions) for each transaction, and an arbitration order that decides the actual global state that is not necessary to be the same as the observable state for each transaction [? ? ?].

Definition 0.1 (Runtime abstract executions and abstract executions). Assuming a set of *transactions identifiers* TransID $\triangleq \{t, ...\}$, the *transactions* $\mathcal{T} \in \text{Tx}$ is defined as a finite partial function from transactions identifiers TransID to valid sets of events Opset,

$$\mathcal{T} \in \text{Tx} \triangleq \text{TransID} \stackrel{fin}{\rightharpoonup} \text{Opset}$$

- An runtime abstract execution is a tuple $\hat{\mathcal{X}} = (\mathcal{T}, \mathcal{I}, \hat{po}, vis, ar) \in RunAbsExecs that satisfies the following conditions,$
 - The transactions \mathcal{T} is a partial function from transaction identifiers to their corresponding events (Def. ??).
 - Assume a countably infinite set of thread identifiers $i \in \mathcal{I} \subseteq \text{ThreadID}$. The runtime threads \mathcal{I} is a set of transaction identifiers.
 - The arbitration order $\operatorname{ar} \subseteq \operatorname{dom}(\mathcal{T}) \times \operatorname{dom}(\mathcal{T})$ is a strict, total order¹.
 - The runtime program order $\hat{po} \subseteq \text{dom}(\mathcal{T}) \times (\text{dom}(\mathcal{T}) \uplus \mathcal{I})$ is the union of several disjoint, strict total orders \hat{po}_i . That is, there exists a partition $\{\text{dom}(\mathcal{T})_i\}_{i\in I}$ of $\text{dom}(\mathcal{T})$ such that $\hat{po} = \biguplus_{i\in I} \hat{po}_i$, where \hat{po}_i is a strict, total order. It also requires $po \subseteq \text{ar}$.
 - The *visibility relation* $vis \subseteq dom(\mathcal{T}) \times dom(\mathcal{T})$ is a relation such that $vis \subseteq ar$.

The set of abstract executions $\mathcal{X} = (\mathcal{T}, po, vis, ar) \in AbsExecs$, where the program order $po \subseteq dom(\mathcal{T}) \times dom(\mathcal{T})$, is defined by erasing the runtime threads from the runtime abstract executions RunAbsExecs,

$$\mathsf{AbsExecs} \ \triangleq \ \big\{ \big\| \hat{\mathcal{X}} \big\| \ \big| \ \hat{\mathcal{X}} \in \mathsf{RunAbsExecs} \big\}$$

where the erasing function $[\cdot]$: RunAbsExecs \rightarrow AbsExecs converts a runtime abstract execution to an abstract execution by erasing the second element, *i.e.* the set of thread identifiers, and also any thread identifiers from the runtime program order,

$$\left\| \left(\mathcal{T}, \mathcal{I}, \hat{\mathsf{po}}, \mathsf{vis}, \mathsf{ar} \right) \right\| \ \triangleq \ \left(\mathcal{T}, \hat{\mathsf{po}} \setminus \left\{ (t, i) \ \middle| \ t \in \mathsf{dom}(\mathcal{T}) \land i \in \mathcal{I} \right\}, \mathsf{vis}, \mathsf{ar} \right)$$

For brevity, the $\hat{\mathcal{X}} \mid_{\mathcal{T}}$, $\hat{\mathcal{X}} \mid_{\mathcal{D}}$, $\hat{\mathcal{X}} \mid_{\mathsf{po}}$, $\hat{\mathcal{X}} \mid_{\mathsf{vis}}$ and $\hat{\mathcal{X}} \mid_{\mathsf{ar}}$ denote the corresponding elements in the tuple and

Recall that a relation $R \subseteq A \times A$ is a strict partial order if it is irreflexive and transitive. It is a strict total order if for any $a_1, a_2 \in A$, either $a_1 = a_2, (a_1, a_2) \in R$ or $(a_2, a_1) \in R$.

similarly for $\mathcal{X}|_{(.)}$. The composition of two runtime abstract executions, $\bullet_{\hat{\mathcal{X}}}$: RunAbsExecs \times RunAbsExecs \rightarrow RunAbsExecs, is defined as the follows,

$$\hat{\mathcal{X}}_{1} \bullet_{\hat{\mathcal{X}}} \hat{\mathcal{X}}_{2} \triangleq \begin{cases}
\left(\lambda t. \, \hat{\mathcal{X}}_{1}|_{\mathcal{T}}(t) \bullet_{\mathcal{O}} \, \hat{\mathcal{X}}_{2}|_{\mathcal{T}}(t), \hat{\mathcal{X}}_{1}|_{\mathcal{I}}, \hat{\mathcal{X}}_{1}|_{\mathsf{po}}, \hat{\mathcal{X}}_{1}|_{\mathsf{vis}}, \hat{\mathcal{X}}_{1}|_{\mathsf{ar}}\right) & \dagger \\
\text{undefined} & \text{otherwise}
\end{cases}$$

$$\dagger \equiv \operatorname{dom}(\hat{\mathcal{X}}_{1}|_{\mathcal{T}}) = \operatorname{dom}(\hat{\mathcal{X}}_{2}|_{\mathcal{T}}) \wedge \hat{\mathcal{X}}_{1}|_{\mathcal{I}} = \hat{\mathcal{X}}_{2}|_{\mathcal{I}}$$

$$\wedge \hat{\mathcal{X}}_{1}|_{\mathsf{po}} = \hat{\mathcal{X}}_{2}|_{\mathsf{po}} \wedge \hat{\mathcal{X}}_{1}|_{\mathsf{vis}} = \hat{\mathcal{X}}_{2}|_{\mathsf{vis}} \wedge \hat{\mathcal{X}}_{1}|_{\mathsf{ar}} = \hat{\mathcal{X}}_{2}|_{\mathsf{ar}}$$

The set of the units is $\mathbf{1}_{\mathcal{X}} \triangleq \{\mathcal{X} \mid \forall t. \, \mathcal{X}|_{\mathcal{T}} \ (t) = \mathbf{1}_{\mathcal{O}}\}$. Then, the order between two runtime abstract executions $\hat{\mathcal{X}}_1$ and $\hat{\mathcal{X}}_2$ is defined as point-wise set inclusions,

$$\hat{\mathcal{X}}_{1} \sqsubseteq \hat{\mathcal{X}}_{2} \quad \stackrel{\text{def}}{\Longleftrightarrow} \quad (\forall t. \, \hat{\mathcal{X}}_{1}|_{\mathcal{T}} \, (t) \implies \hat{\mathcal{X}}_{2}|_{\mathcal{T}} \, (t)) \wedge \hat{\mathcal{X}}_{1}|_{\mathcal{I}} \subseteq \hat{\mathcal{X}}_{2}|_{\mathcal{I}} \\ \wedge \hat{\mathcal{X}}_{1}|_{\mathsf{po}} \subseteq \hat{\mathcal{X}}_{2}|_{\mathsf{po}} \wedge \hat{\mathcal{X}}_{1}|_{\mathsf{vis}} \subseteq \hat{\mathcal{X}}_{2}|_{\mathsf{vis}} \wedge \hat{\mathcal{X}}_{1}|_{\mathsf{ar}} \subseteq \hat{\mathcal{X}}_{2}|_{\mathsf{ar}}$$

Last, by erasing the runtime, the order between two abstract executions is defined as the follows,

$$\mathcal{X}_1 \sqsubseteq \mathcal{X}_2 \quad \stackrel{\text{def}}{\Longleftrightarrow} \quad \exists \hat{\mathcal{X}}_1, \hat{\mathcal{X}}_2. \, \mathcal{X}_1 = \|\hat{\mathcal{X}}_1\| \wedge \mathcal{X}_2 = \|\hat{\mathcal{X}}_2\| \wedge \hat{\mathcal{X}}_1 \sqsubseteq \hat{\mathcal{X}}_2$$

We parametrise the consistency models in our semantics. A consistency model contains two parts, a resolution policy and a consistency guarantee (Def. 0.2) [?]. Given a abstract execution \mathcal{X} , a set of observable transactions T and an address a, the resolution policy $\mathcal{P}(\mathcal{X}, T, a)$ decides the observable values for the address a through some computation on the observable transactions T. A common resolution policy is last-write-win that if a transaction observes several writes for the same address, it always reads the last write (by the arbitration order). The consistency guarantee gives the minimum constraint for the visibility relation.

Definition 0.2 (Consistency Models). A Consistency model is a tuple, $\mathcal{M} = (\mathcal{P}, \mathcal{F}) \in \text{ConsisModels}$, including a resolution policy and a consistency guarantee. A resolution policy \mathcal{P} is a function such that for a given address a and a set of observable transactions from a abstract execution, it returns a set of possible observable values.

RESPOS
$$\triangleq \left\{ \mathcal{P} \mid \mathcal{P} \in ABSEXECS \times \mathcal{P} (TRANSID) \times ADDR \rightarrow \mathcal{P} (VAL) \right\} \\ \wedge \forall \mathcal{X}, T, a. \, \mathcal{P}(\mathcal{X}, T, a) \downarrow \Longrightarrow T \subseteq \text{dom}(\mathcal{X}|_{\mathcal{T}}) \right\}$$

A consistency guarantee \mathcal{F} is a function such that for an abstract execution, it returns a relation which corresponds to the minimum visibility relation.

Note that a well-formed consistency guarantee must not violate the arbitrary order. The order \sqsubseteq between two consistency model $\mathcal{M}_1, \mathcal{M}_2$ is defined as the follows,

$$(\mathcal{P}_1, \mathcal{F}_1) \sqsubseteq (\mathcal{P}_2, \mathcal{F}_2) \quad \stackrel{\text{def}}{\iff} \quad \forall \mathcal{X}, T, a.$$
$$\mathcal{P}_2(\mathcal{X}, T, a) \subseteq \mathcal{P}_1(\hat{\mathcal{X}}, T, a) \land \mathcal{F}_1(\mathcal{X}) \subseteq \mathcal{F}_2(\mathcal{X})$$

The bottom element is $\perp \triangleq (\lambda(\mathcal{X}, T, a). \text{VAL}, \lambda(\mathcal{X}). \emptyset)$, which means one is able to observe any arbitrary value for each address and there is no constraint for visibility relation.

Definition 0.3 (Thread transition labels). Given the set of thread identifiers ThreadID (Def. 0.1), the set of thread transition labels, $\iota \in \text{Label}$, is defined by the following grammar, where P denotes a program (Def. ??), the t demotes a transaction identifier and σ denotes a thread stack (Def. ??),

$$\iota \in \mathtt{LABEL} \ ::= \ \mathsf{id} \mid \mathtt{cmt}(t) \mid \mathtt{fork}(i,\mathtt{P}) \mid \mathtt{join}(i,\sigma)$$

Definition 0.4 (Thread semantics). Given the thread identifiers $i \in \text{ThreadID}$, the set of intermediate programs, $\hat{P} \in \text{IPROG}$, is defined by the following grammar:

$$\hat{P} ::= P \mid \hat{P} ; \mathtt{wait}(i)$$

Given the set of consistency model ConsisModels (Def. 0.2), the set of thread stacks ThdStks (Def. ??) and runtime abstract executions RunabsExecs (Def. 0.1), the per-thread operational semantics of programs,

$$\rightarrow$$
 : ConsisModels × ThreadID ×
$$((\text{ThdStks} \times \text{RunAbsExecs}) \times \text{IProg}) \times \text{Label} \times ((\text{ThdStks} \times \text{RunAbsExecs}) \times \text{IProg})$$

is defined in Fig. 2.

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The PCOMMIT rule "substitutes" the dummy node for the thread i in the runtime abstract execution $\hat{\mathcal{X}}$ with a newly allocated transaction t with its associated events \mathcal{O} . To obtain the events \mathcal{O} , it prophesies a set of observable transactions T, which will be added into the visibility relation later. Given the observable transactions, the obs_states function computes possible initial heaps, by applying the resolution policy \mathcal{P} for each address. Note that the resolution policy might return more than one value for an address, so there are more than many possible initial heaps. Given the transaction code T, first pick an initial heap h and then given the transaction semantics (Fig. ??), we can get the events set \mathcal{O} . To extend the runtime abstract execution, it replaces the dummy node i with the new transaction t, links all transaction from the observable transactions T to the new transaction, and puts the new transaction at the end of arbitration order. Then, it extends the program order by adding back the dummy node i after the transaction t for preserving program order for the future transactions from the same thread.

The PAR rule forks a new thread and inserts the appropriate joining point by appending the auxiliary $\mathtt{wait}(i)$ command, where the parameter i denotes the identifier of the newly forked thread. The WAIT rule dually awaits the termination of the child thread i indicated by the auxiliary $\mathtt{wait}(i)$ command, and subsequently updates the thread stack. Note that these two rules are labelled with the $\mathtt{fork}(i,P)$ and $\mathtt{join}(i,\sigma)$ which are used by the semantics of the thread pool described shortly (Fig. 3).

In order to model concurrency, we use thread pools. A thread pool is a finite partial function from thread identifiers to triples of the form (σ, \hat{P}) . That is, each thread is associated with a thread stack σ and an intermediate program \hat{P} to be executed.

Definition 0.5 (Thread pools). Given the set of thread stacks ThdStks (Def. ??) and intermediate programs IPROG (Def. 0.4), a *thread pool* is a a finite partial function from thread identifiers to triples of thread stacks and intermediate programs, $\eta \in \text{TPOOL} \triangleq \text{ThreadID} \xrightarrow{fin} \text{ThdStks} \times \text{IPROG}$.

Definition 0.6 (Thread pool semantics). Given the set of consistent models ConsisModels (Def. 0.2), runtime abstract executions RunAbsexecs (Def. 0.1), transition labels Label (Def. 0.3) and thread pools TPool (Def. 0.5), the *thread pool semantics*,

⇒: ConsisModels × (RunAbsExecs × TPool) × Label × (RunAbsExecs × TPool)

is defined in Fig. 3.

The thread pool operational semantics is given in Fig. 3, where an arbitrary thread in the pool η is picked to run for one step. If the next execution step is a thread fork, then a new thread i' is allocated in the pool to be executed with its thread stack copied from its parent (forking) thread. Conversely, when the next execution step is the joining of thread i', then i' is removed from the thread pool and the stack from the child thread merges into the parent thread.

57 0.2 Soundness and Completeness

Given a consistency model, we can define the set of abstract executions that satisfy a consistency model (Def. 0.7) [? ? ?]. The Lemma 0.8 is for sanity check.

Definition 0.7 (Valid abstract executions). Given a consistency model $(\mathcal{P}, \mathcal{F}) = \mathcal{M} \in \text{ConsisModels}$ (Def. 0.2), the set of valid abstract executions under the model, denoted by $[\![\mathcal{M}]\!]$, is defined as the follows,

$$\llbracket (\mathcal{P},\mathcal{F}) \rrbracket \quad \triangleq \quad \left\{ \mathcal{X} = (\mathcal{T},\mathsf{po},\mathsf{vis},\mathsf{ar}) \; \middle| \; \begin{array}{l} \mathcal{F}(\mathcal{X}) \subseteq \mathsf{vis} \wedge \forall t,a,v. \\ (\mathsf{R},a,v) \in \mathcal{T}(t) \implies v \in \mathcal{P}(\mathcal{X},\mathcal{X}|_{\mathsf{vis}}(t),a) \end{array} \right\}$$

where $\mathcal{X}|_{\text{vis}}(t)$ returns all the predecessors of t with respect to the visibility relation. This is, for any relation R.

$$R(x) \triangleq \{x' \mid (x', x) \in R\}$$

Lemma 0.8 (Consistency models monotonicity). The abstract executions allowed by a stronger consistency model is allowed by a weaker consistency model, this is,

$$\mathcal{M}_1 \sqsubseteq \mathcal{M}_2 \implies \llbracket \mathcal{M}_2 \rrbracket \subseteq \llbracket \mathcal{M}_1 \rrbracket$$

Proof. For any abstract execution \mathcal{X} that satisfies the stronger consistency model $\mathcal{M}_2 = (\mathcal{P}_2, \mathcal{F}_2)$, first, it has $\mathcal{F}_2(\mathcal{X}) \subseteq \mathcal{X}|_{\text{vis}}$ by the Def. 0.7. Given the hypothesis $\mathcal{M}_1 \sqsubseteq \mathcal{M}_2$ and the order definition (Def. 0.2), we know $\mathcal{F}_1(\mathcal{X}) \subseteq \mathcal{F}_2(\mathcal{X})$ so that $\mathcal{F}_1(\mathcal{X}) \subseteq \mathcal{X}|_{\text{vis}}$. Second, assume a transaction t and any of its read event with an address a and a v such that $(R, a, v) \in \mathcal{T}(t)$ therefore $v \in \mathcal{P}_2(\mathcal{X}, \mathcal{X}|_{\text{vis}}(t), a)$. Similarly by the hypothesis $\mathcal{M}_1 \sqsubseteq \mathcal{M}_2$, we know $\mathcal{P}_2(\mathcal{X}, \mathcal{X}|_{\text{vis}}(t), a) \subseteq \mathcal{P}_1(\mathcal{X}, \mathcal{X}|_{\text{vis}}(t), a)$, thus $v \in \mathcal{P}_1(\mathcal{X}, \mathcal{X}|_{\text{vis}}(t), a)$. Therefore we have $\mathcal{X} \in [\![\mathcal{M}_1]\!]$.

$$t \in \operatorname{fresh}\left(\hat{\mathcal{X}}\right) \quad T \subseteq \operatorname{dom}(\hat{\mathcal{X}}|_T) \quad h \in \operatorname{obs_states}\left(\hat{\mathcal{X}}, T, \mathcal{P}\right) \\ \sigma \vdash (\emptyset, h, \emptyset), \mathsf{T} \quad \leadsto^* (\tau, h', \mathcal{O}), \operatorname{skip} \quad \hat{\mathcal{X}}' = \operatorname{new_abs_exec}\left(\hat{\mathcal{X}}, i, \mathcal{O}, t, A, \mathcal{F}\right) \\ \hline (\mathcal{P}, \mathcal{F}), i \vdash (\sigma, \hat{\mathcal{X}}), \left[\mathsf{T}\right] \quad \to_{\operatorname{ext}(i)} \left(\sigma[\operatorname{ret} \mapsto \tau(\operatorname{ret})], \hat{\mathcal{X}}'), \operatorname{skip} \\ \hline \frac{v = \left[\mathbb{E}\right]_{\sigma} \quad \mathsf{x} \in \operatorname{ThdVars}}{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \mathsf{x} := \mathsf{E} \quad \to_{\operatorname{id}} \left(\sigma[\mathsf{x} \mapsto v], \hat{\mathcal{X}}), \operatorname{skip} \right)} \quad \operatorname{PASSIGN} \\ \hline \frac{\mathbb{E}\mathbb{E}_{\sigma} = 0}{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \operatorname{assume}(\mathsf{E}) \quad \to_{\operatorname{id}} \left(\sigma, \hat{\mathcal{X}}\right), \operatorname{skip}} \quad \operatorname{PASSUME} \\ \hline \frac{P' \in \left\{\mathsf{P}_1, \mathsf{P}_2\right\}}{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \mathsf{P}_1 + \mathsf{P}_2 \quad \to_{\operatorname{id}} \left(\sigma, \hat{\mathcal{X}}\right), \mathsf{P}'} \quad \operatorname{PCHOICE} \\ \hline \overline{\mathcal{M}}, i \vdash (\sigma, \hat{\mathcal{X}}), \mathsf{P}^* \quad \to_{\operatorname{id}} \left(\sigma, \hat{\mathcal{X}}\right), \operatorname{skip} + (\mathsf{P}; \mathsf{P}^*)} \quad \operatorname{PLoop} \\ \hline \overline{\mathcal{M}}, i \vdash (\sigma, \hat{\mathcal{X}}), \operatorname{skip}; \hat{\mathsf{P}} \quad \to_{\operatorname{id}} \left(\sigma, \hat{\mathcal{X}}\right), \hat{\mathsf{P}}'} \quad \operatorname{PSEQSKIP} \\ \hline \frac{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \operatorname{skip}; \hat{\mathsf{P}} \quad \to_{\operatorname{id}} \left(\sigma, \hat{\mathcal{X}}\right), \hat{\mathsf{P}}'_1; \hat{\mathsf{P}}_2}{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \hat{\mathsf{P}}_1; \hat{\mathsf{P}}_2 \quad \to_{\operatorname{torac}(i', \mathsf{P}_2)} \left(\sigma, \hat{\mathcal{X}}'\right), \hat{\mathsf{P}}'_1; \hat{\mathsf{P}}_2} \quad \operatorname{PSEQ} \\ \hline \hat{\mathcal{M}}, i \vdash (\sigma, \hat{\mathcal{X}}), \mathsf{P}_1 \parallel \mathsf{P}_2 \quad \to_{\operatorname{torac}(i', \mathsf{P}_2)} \left(\sigma, \hat{\mathcal{X}}'\right), \mathsf{P}_1; \operatorname{wait}(i') \quad \operatorname{PPAR} \\ \hline \sigma = \sigma_1 \uplus \sigma_f \qquad \sigma' = \sigma_2 \uplus \sigma_f \qquad \hat{\mathcal{X}} = \operatorname{erase_thread}\left(\hat{\mathcal{X}}', i'\right) \\ \hline \mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \operatorname{wait}(i') \quad \to_{\operatorname{join}(i',\sigma')} \left(\sigma_1 \uplus \sigma_2 \uplus \sigma_f, \hat{\mathcal{X}}'\right), \operatorname{skip} \right) \quad \operatorname{PWAIT} \\ \hline \end{array}$$

SX: The stack could be polluted by the child thread, but they must agree?

where,

$$\begin{aligned} & \operatorname{consis}\left(\hat{\mathcal{X}},i,t,\mathcal{F}\right) & \triangleq & i \in \hat{\mathcal{X}}|_{\mathcal{I}} \wedge \forall t'. ((t',t) \in \mathcal{F}(\lfloor \mid \hat{\mathcal{X}} \rfloor)) \implies (t',t) \in \hat{\mathcal{X}}|_{\mathtt{vis}}) \\ & \operatorname{new_abs_exec} & : & \begin{pmatrix} \operatorname{RunAbsExecs} \times \operatorname{ThreadID} \times \operatorname{TransID} \times \\ \mathcal{P}\left(\operatorname{OPS}\right) \times \mathcal{P}\left(\operatorname{TransID}\right) \times \operatorname{ConGuar} \end{pmatrix} \rightarrow \operatorname{RunAbsExecs} \\ & \operatorname{new_abs_exec}\left(\hat{\mathcal{X}},i,t,\mathcal{O},T,\mathcal{F}\right) & \triangleq & \begin{cases} \hat{\mathcal{X}}' & \operatorname{consis}\left(\hat{\mathcal{X}}',i,t,\mathcal{F}\right) \\ \operatorname{undefined} & \operatorname{otherwise} \end{cases} \\ & \hat{\mathcal{X}}' & \equiv & \begin{pmatrix} \hat{\mathcal{X}}|_{\mathcal{T}} \uplus \left\{t \mapsto \mathcal{O}\right\}, \hat{\mathcal{X}}|_{\mathcal{I}}, \hat{\mathcal{X}}|_{\mathtt{po}} \uplus \left\{(t',t) \mid (t',i) \in \hat{\mathcal{X}}|_{\mathtt{po}}\right\} \uplus \left\{(t,i)\right\}, \\ \hat{\mathcal{X}}|_{\mathtt{vis}} \uplus \left\{(t',t) \mid t' \in T\right\}, \hat{\mathcal{X}}|_{\mathtt{ar}} \uplus \left\{(t',t) \mid t' \in \operatorname{dom}(\hat{\mathcal{X}}|_{\mathcal{T}})\right\} \\ & \operatorname{obs_states} & : & \operatorname{RunAbsExecs} \times \mathcal{P}\left(\operatorname{TransID}\right) \times \operatorname{ResPos} \rightarrow \mathcal{P}\left(\operatorname{FPHEaP}\right) \\ & \operatorname{obs_states}\left(\hat{\mathcal{X}},T,\mathcal{P}\right) & \triangleq & \left\{h \mid \forall a,v.v \in \mathcal{P}(\lfloor \mid \hat{\mathcal{X}} \rfloor,T,a) \iff h(a)=v\right\} \\ & \operatorname{fresh}\left(\hat{\mathcal{X}}\right) & \triangleq & \left\{t \mid \neg t \in \operatorname{dom}(\hat{\mathcal{X}}|_{\mathcal{T}})\right\} \\ & \operatorname{extend_thread}\left(\hat{\mathcal{X}},i,i'\right) & \triangleq & \left(\hat{\mathcal{X}}|_{\mathcal{T}},\hat{\mathcal{X}}|_{\mathcal{I}} \uplus \left\{i'\right\}, \hat{\mathcal{X}}|_{\mathtt{po}} \uplus \left\{(t,i') \mid (t,i) \in \hat{\mathcal{X}}|_{\mathtt{po}}\right\}, \hat{\mathcal{X}}|_{\mathtt{vis}}, \hat{\mathcal{X}}|_{\mathtt{ar}}) \\ & \operatorname{erase_thread}\left(\hat{\mathcal{X}},i\right) & \triangleq & \left(\hat{\mathcal{X}}|_{\mathcal{T}},\hat{\mathcal{X}}|_{\mathcal{I}} \ \lor \left\{i\right\}, \hat{\mathcal{X}}|_{\mathtt{po}} \ \lor \left\{(t,i) \mid t \in \operatorname{dom}(\hat{\mathcal{X}}|_{\mathcal{T}})\right\}, \hat{\mathcal{X}}|_{\mathtt{vis}}, \hat{\mathcal{X}}|_{\mathtt{ar}}) \end{aligned} \end{aligned}$$

Figure 2: Per-thread operational semantics

$$\frac{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \hat{\mathbf{P}} \rightarrow_{\iota} (\sigma', \hat{\mathcal{X}}'), \hat{\mathbf{P}}' \quad \iota \in \left\{ \mathtt{id}, \mathtt{cmt}(-) \right\}}{\mathcal{M} \vdash (\hat{\mathcal{X}}, \eta \uplus \left\{ i \mapsto (\sigma, \hat{\mathbf{P}}) \right\}) \Rightarrow_{\iota} (\hat{\mathcal{X}}, \eta \uplus \left\{ i \mapsto (\sigma', \hat{\mathbf{P}}') \right\})} \text{ PSINGLE}}$$

$$\frac{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \hat{\mathbf{P}} \rightarrow_{\mathtt{fork}(i', \mathbb{P})} (\sigma, \hat{\mathcal{X}}'), \hat{\mathbf{P}}'}{\mathcal{M} \vdash (\hat{\mathcal{X}}, \eta \uplus \left\{ i \mapsto (\sigma, \hat{\mathbf{P}}) \right\}) \Rightarrow_{\mathtt{fork}(i', \mathbb{P})} (\hat{\mathcal{X}}', \eta \uplus \left\{ i \mapsto (\sigma, \hat{\mathbf{P}}'), i' \mapsto (\sigma, \mathbb{P}) \right\})} \text{ PFORK}}$$

$$\frac{\mathcal{M}, i \vdash (\sigma, \hat{\mathcal{X}}), \hat{\mathbf{P}} \rightarrow_{\mathtt{join}(i', \sigma')} (\sigma'', \hat{\mathcal{X}}'), \hat{\mathbf{P}}'}{\mathcal{M} \vdash (\hat{\mathcal{X}}, \eta \uplus \left\{ i \mapsto (\sigma, \hat{\mathbf{P}}), i' \mapsto (\sigma', \mathtt{skip}) \right\})} \Rightarrow_{\mathtt{join}(i', \sigma'')} (\hat{\mathcal{X}}', \eta \uplus \left\{ i \mapsto (\sigma'', \hat{\mathbf{P}}') \right\})} \text{ PJOIN}}$$

Figure 3: Thread pool semantics

Theorem 0.9 (Soundness of the semantics). For any runtime abstract abstract execution $\hat{\mathcal{X}}$ that satisfies a consistency model \mathcal{M} , if the semantics under consistency model \mathcal{M} take one step to a new abstract execution $\hat{\mathcal{X}}'$, the new execution should satisfy the consistency model. This is,

$$\begin{split} \forall \mathcal{M}, i, \hat{\mathcal{X}}, \hat{\mathcal{X}}', \sigma, \sigma', \hat{\mathbf{P}}, \hat{\mathbf{P}}', \iota. \\ & \|\hat{\mathcal{X}}\| \in [\![\mathcal{M}]\!] \land \mathcal{M}, i \vdash (\hat{\mathcal{X}}, \sigma), \hat{\mathbf{P}} \rightarrow_{\iota} (\hat{\mathcal{X}}', \sigma'), \hat{\mathbf{P}}' \implies \|\hat{\mathcal{X}}'\| \in [\![\mathcal{M}]\!] \end{split}$$

- 66 Proof. We prove it by induction on the derivations.
- Base Case PCOMMIT.

By the PCOMMIT rule, it has $\hat{P} = [T]$, $\hat{P}' = \text{skip}$ and $\iota = \text{cmt}(t)$, for some transaction code T and identifier t. Let variables $\mathcal{X} = \|\hat{\mathcal{X}}\|$ and $\mathcal{X}' = \|\hat{\mathcal{X}}'\|$ in the following discussion. We need to prove the follows,

$$\forall t', t'', a, v.$$

$$(\mathbf{R}, a, v) \in \mathcal{X}'|_{\mathcal{T}}(t') \implies v \in \mathcal{P}(\mathcal{X}', \mathcal{X}'|_{\mathbf{vis}}(t'), a)$$

$$(t', t'') \in \mathcal{F}(\mathcal{X}') \implies (t', t'') \in \mathcal{X}'|_{\mathbf{vis}}$$

$$(0.1)$$

First for the Eq. (0.1), it only needs to check the new transaction t as others are proved directly from the hypothesis. Given an initial heap h, a set of events \mathcal{O} associated with the new transaction t, a set of transactions T observed by the new transaction t, assume these variables satisfy the follows,

$$egin{aligned} (\sigma, au,h'.\ \sigma dash (\emptyset,h,\emptyset), \mathtt{T} \leadsto^* (au,h',\mathcal{O}), \mathtt{skip} \wedge h \in \mathsf{obs_states}\left(\hat{\mathcal{X}},T,\mathcal{P}
ight) \end{aligned}$$

therefore the following hold,

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$$(R, a, v) \in \mathcal{O} \implies h(a) = v$$

Because by the transaction semantics (Fig. ??), a transaction only records the first read event for each address. It can be proved by induction on the derivations for transaction operational semantics, where the only rule that involves read event is TREAD. If the new read event is included in the events set after flushing read (Def. ??), i.e. $(R, a, v) \in (\mathcal{O} \lhd (R, a, v))$, this means there is no other read and write to the same address before, so that the value v associate with the address a is the initial value, i.e. h(a), and after that no other read event can over-write. By the obs_states function (Fig. 2), where $h(a) = v \iff \mathcal{P}(\mathcal{X}, T, a)$, the following hold,

$$(R, a, v) \in \mathcal{O} \implies \mathcal{P}(\mathcal{X}, T, a)$$

Since the new_abs_exec function only extends abstract execution, which means $\mathcal{X} \sqsubseteq \mathcal{X}'$, so that,

$$(R, a, v) \in \mathcal{O} \implies \mathcal{P}(\mathcal{X}', T, a)$$

By the obs_states function, we have $\mathcal{O} = \mathcal{X}'|_{\mathcal{T}}(t)$ and $T = \mathcal{X}'|_{\text{vis}}(t)$ therefore we prove Eq. (0.1).

Second for Eq. (0.2), the visibility relation of the new abstract execution $\mathcal{X}'|_{\text{vis}}$ contains the minimum relation required by the consistency guarantee. Similarly, it is sufficient to consider those visibility edges related to the new transaction t. Note that for any transaction $t'' \in \text{dom}(\mathcal{X}' \mid_{\mathcal{T}})$, it has $(t,t'') \notin \mathcal{F}(\mathcal{X}')$. Given new_abs_exec function, so that $(t'',t) \in \mathcal{X}' \mid_{\text{ar}}$. Then, given the consistency guarantee (Def. 0.2), it cannot violate arbitration order, this is, $\mathcal{F}(\mathcal{X}') \subseteq \mathcal{X}' \mid_{\text{ar}}$. Thus, it is safe to assume a transaction $t' \in \text{dom}(\mathcal{X}' \mid_{\mathcal{T}})$ such that $(t',t) \in \mathcal{F}(\mathcal{X}')$. By the consist predicate, we have $(t',t) \in \mathcal{F}(\mathcal{X}') \Longrightarrow (t',t) \in \mathcal{X}' \mid_{\text{vis}}$, so that we prove Eq. (0.2).

Base Case Passign, Passume, Pchoice, Ploop, Pseqskips.

For these base cases, the runtime abstract execution remains the same, *i.e.* $\hat{\mathcal{X}} = \hat{\mathcal{X}}'$, so they trivially hold because of the hypothesis.

Base Case PPAR, PWAIT.

For these two base cases, since the extend_thread and erase_thread functions only change relations related to the corresponding threads, therefore *i.e.* $\|\hat{\mathcal{X}}\| = \|\hat{\mathcal{X}}'\|$, so they hold because of the hypothesis.

Inductive Case PSEQ.

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It is proved directly by applying the I.H.

For sanity check and also proving the completeness of this semantics, we first prove the semantics is monotonic (Lemma 0.10), which means that for any reduction that can happen in the stronger consistency model, it can also happen in the weaker one.

Lemma 0.10 (Semantics monotonicity). Given an initial runtime abstract execution $\hat{\mathcal{X}}$, if it can transfer to an abstract execution $\hat{\mathcal{X}}'$ by reducing one step of the semantics under stronger consistency model \mathcal{M}_2 , it is also possible by reducing one step of the semantics under a weaker consistency model \mathcal{M}_1 .

$$\begin{split} \forall \mathcal{M}_{1}, \mathcal{M}_{2}, i, \hat{\mathcal{X}}, \hat{\mathcal{X}}', \hat{\mathbf{P}}, \hat{\mathbf{P}}', \sigma, \sigma', \iota. \\ \mathcal{M}_{2}, i \vdash (\hat{\mathcal{X}}, \sigma), \hat{\mathbf{P}} \rightarrow_{\iota} (\hat{\mathcal{X}}', \sigma'), \hat{\mathbf{P}}' \wedge \mathcal{M}_{1} \sqsubseteq \mathcal{M}_{2} \\ \Longrightarrow \mathcal{M}_{1}, i \vdash (\hat{\mathcal{X}}, \sigma), \hat{\mathbf{P}} \rightarrow_{\iota} (\hat{\mathcal{X}}', \sigma'), \hat{\mathbf{P}}' \end{split}$$

7 Proof. We prove it by induction on the derivations. The only interesting case is the PCOMMIT rule.

Base Case PCOMMIT.

Let variables $(\mathcal{P}_1, \mathcal{F}_1) = \mathcal{M}_1$ and $(\mathcal{P}_2, \mathcal{F}_2) = \mathcal{M}_2$ respectively. Given an initial runtime abstract execution $\hat{\mathcal{X}}$, a set of observable transactions T, a new transaction identifier t and a thread identifier i, by the PCOMMIT rule (Fig. 3), it is sufficient to prove, first, all the observable states under the stronger consistency model can also be observed under the weaker one,

$$\mathsf{obs_states}\left(\hat{\mathcal{X}}, T, \mathcal{P}_2\right) \subseteq \mathsf{obs_states}\left(\hat{\mathcal{X}}, T, \mathcal{P}_1\right) \tag{0.3}$$

and second if the new runtime abstract execution $\hat{\mathcal{X}}'$ exists under the stronger concurrency model, it should also exist under weaker one,

$$\operatorname{consis}\left(\hat{\mathcal{X}}',t,i,\mathcal{F}_{2}\right) \implies \operatorname{consis}\left(\hat{\mathcal{X}}',t,i,\mathcal{F}_{1}\right) \tag{0.4}$$

To prove Eq. (0.3), assume an observable heap h under stronger consistency model \mathcal{M}_2 , which means $h \in$ obs_states $(\hat{\mathcal{X}}, T, \mathcal{P}_2)$. Then, assume an address a and the corresponding value v such that h(a) = v. By the obs_states function (Fig. 2) and resolution policy (Def. 0.2), it is known that $v \in \mathcal{P}_2(\|\hat{\mathcal{X}}\|, T, a)$. Because of $\mathcal{P}_2(\|\hat{\mathcal{X}}\|, T, a) \subseteq \mathcal{P}_1(\|\hat{\mathcal{X}}\|, T, a)$ (Def. 0.2), we have $v \in \mathcal{P}_1(\|\hat{\mathcal{X}}\|, T, a)$, so $h \in$ obs_states $(\hat{\mathcal{X}}, T, \mathcal{P}_1)$. For Eq. (0.4), assume the consis $(\hat{\mathcal{X}}, t, i, \mathcal{F}_2)$ predicate holds and assume an edge $(t', t) \in \mathcal{F}_2(\|\hat{\mathcal{X}}\|)$ for some t'. Thus, the edge (t', t) will be included in the visibility relation of the new runtime abstract execution, i.e. $(t', t) \in \hat{\mathcal{X}}'|_{\text{vis}}$. Since $\mathcal{F}_1(\|\hat{\mathcal{X}}\|) \subseteq \mathcal{F}_2(\|\hat{\mathcal{X}}\|)$, so $(t', t) \in \mathcal{F}_1(\|\hat{\mathcal{X}}'\|) \Longrightarrow (t', t) \in \hat{\mathcal{X}}'|_{\text{vis}}$ holds. This means for any edges that satisfy the consistency guarantee for stronger model, they also satisfy the weaker consistency guarantee, thus the Eq. (0.4) holds. Combining Eq. (0.3) and Eq. (0.4), we prove PCOMMIT.

Base Case PASSIGN, PASSUME, PCHOICE, PLOOP, PSEQSKIPS, PPAR, PWAIT.

These base cases do not depend on the consistency model, so they trivial hold because of the hypothesis. **Inductive Case PSEQ.**

It is proved directly by applying the I.H.

Lemma 0.11 (Preservation of the consistency model).

$$\begin{split} \forall \mathcal{M}_1, \mathcal{M}_2, i, \hat{\mathcal{X}}, \hat{\mathcal{X}}', \sigma, \sigma', \hat{\mathbf{p}}, \hat{\mathbf{p}}', \iota. \\ \mathcal{M}_1, i \vdash (\hat{\mathcal{X}}, \sigma), \hat{\mathbf{p}} \rightarrow_{\iota} (\hat{\mathcal{X}}', \sigma'), \hat{\mathbf{p}}' \wedge \lfloor \lfloor \hat{\mathcal{X}}' \rfloor \in \llbracket \mathcal{M}_2 \rrbracket \implies \lfloor \lfloor \hat{\mathcal{X}} \rfloor \rfloor \in \llbracket \mathcal{M}_2 \rrbracket \end{split}$$

Proof. We prove it by induction on the derivations. The only interesting case is the COMMIT.

Base Case COMMIT.

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By the rule it has $\hat{P} = [T]$, $\hat{P}' = \text{skip}$ and $\iota = \text{cmt}(t)$. We prove this case by deriving contradiction. Assume $\|\hat{\mathcal{X}}\| \notin [\mathcal{M}_2]$, which means that there exists an edge (t', t'') such that it is in the consistency guarantee, $(t', t'') \in \mathcal{F}_2(\|\hat{\mathcal{X}}\|)$ but not is not included in the visibility relation.

$$(t', t'') \notin \hat{\mathcal{X}}|_{\text{vis}} \tag{0.5}$$

Another possibility is that there is a read event from a transaction, $(\mathbb{W}, a, v) \in \hat{\mathcal{X}}|_{\mathcal{T}}(t')$ where the value is not observable under the stronger consistency model, *i.e.*

$$v \notin \mathcal{P}_2(\|\hat{\mathcal{X}}\|, \hat{\mathcal{X}}|_{\text{vis}}(t'), a)$$
 (0.6)

Because the rule only extend the runtime abstract execution $\hat{\mathcal{X}} \subseteq \hat{\mathcal{X}}'$, this means the edge (t', t'') is not in the runtime abstract execution after reduction $\hat{\mathcal{X}}'$, or the transaction t' reads a unobservable value,

$$(t',t'') \notin \hat{\mathcal{X}}'|_{\mathtt{vis}} \ \forall v \notin \mathcal{P}_2(\|\hat{\mathcal{X}}'\|,\hat{\mathcal{X}}'|_{\mathtt{vis}} \ (t'),a)$$

Both cases lead to $\hat{\mathcal{X}}' \notin \llbracket \mathcal{M}_2 \rrbracket$ so there is contradiction to the hypothesis. Therefore we have the proof for this base case.

Base Case PASSIGN, PASSUME, PCHOICE, PLOOP, PSEQSKIPS.

For these base cases, the runtime abstract execution remains the same, i.e. $\hat{\mathcal{X}} = \hat{\mathcal{X}}'$, so they trivially hold because of the hypothesis.

Base Case PPAR, PWAIT.

For these two base cases, since the extend_thread and erase_thread functions change edges only related to the corresponding threads, therefore $i.e. \|\hat{\mathcal{X}}\| = \|\hat{\mathcal{X}}'\|$, so it holds because of the hypothesis.

Inductive Case PSEQ.

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It is proved directly by applying the I.H.

The completeness means that if an abstract execution $\hat{\mathcal{X}}$ satisfies a consistency model, it always is possible to produce such execution through the semantics under the corresponding consistency model for some initial configurations. To define the completeness, we introduce *anarchic semantics*, which is the semantics under the bottom element for consistency model \bot as for the fact that there is no constraint for the visibility relations and for the observable value for each address.

SX: citation for anarchic semantics?

The Theorem 0.12 says, given some initial configurations, after one step under the anarchic semantics, if one is "lucky" that it ends up with a runtime abstract execution $\hat{\mathcal{X}}'$ that satisfies a consistency model \mathcal{M} , it is possible to get the same result using the semantics specifically for the consistency model \mathcal{M} .

Theorem 0.12 (Completeness of the semantics). For any initial configuration $((\hat{\mathcal{X}}, \sigma), \hat{\mathsf{P}})$, after one step under the anarchic semantics, i.e. the semantics under the bottom element \bot , it ends up with $\hat{\mathcal{X}}'$, and if the new runtime abstract execution $\hat{\mathcal{X}}'$ satisfies a consistency model \mathcal{M} , then there is a corresponding step using semantics under consistency model \mathcal{M} .

$$\begin{split} \forall \mathcal{M}, i, \hat{\mathcal{X}}, \hat{\mathcal{X}}', \sigma, \sigma', \hat{\mathbf{P}}, \hat{\mathbf{P}}', \iota. \\ & \perp, i \vdash (\hat{\mathcal{X}}, \sigma), \hat{\mathbf{P}} \rightarrow_{\iota} (\hat{\mathcal{X}}', \sigma'), \hat{\mathbf{P}}' \wedge \|\hat{\mathcal{X}}'\| \in [\![\mathcal{M}]\!] \\ & \Longrightarrow \mathcal{M}, i \vdash (\hat{\mathcal{X}}, \sigma), \hat{\mathbf{P}} \rightarrow_{\iota} (\hat{\mathcal{X}}', \sigma'), \hat{\mathbf{P}}' \end{split}$$

Proof. For any runtime abstract execution after one step such that $\|\hat{\mathcal{X}}'\| \in [\![\mathcal{M}]\!]$, by the Lemma 0.11, it is known that the initial configuration also satisfies the consistency model, this is, $\|\hat{\mathcal{X}}\| \in [\![\mathcal{M}]\!]$. Since \bot is the bottom element such that $\bot \sqsubseteq \mathcal{M}$, because the semantics are monotonic with respect to the order of consistency model (Lemma 0.10), we have the proof.