

Aim of this lab: Analyse and evaluate the array operations using 8086 microprocessors.

OBJECTIVE - ① : ① Find the largest Number (8 bit Number) from a given array of size N.

① ASSEMBLY CODE :

- 1). data
- 2) Count db 04h ; Count = array size ($N=4$)
- 3) value db ~~10h, 0ch, 05h, 15h~~, 0ch, 05h, 15h ; array elements
- 4) res db ? ; variable to store the result
- 5). code
- 6) MAIN PROC
- 7) MOV AX, data ; AX \leftarrow address of data segment
- 8) MOV DS, AX ; DS \leftarrow AX
- 9) MOV CL, Count ; CL \leftarrow Count (load array size)
CL \leftarrow CL - 1 {since 1st element is loaded}
- 10) DEC CL ;
- 11) LEA SI, value ; SI \leftarrow offset of value {point to start of array}
- 12) MOV AL, [SI] ; AL \leftarrow value [0]
- 13) up: inc SI ; SI \leftarrow SI + 1
- 14) cmp AL, [SI] ; Compare AL with value [SI]
- 15) JNC nxt ; if AL \geq value [SI], jump to nxt
- 16) mov AL, [SI] ; AL \leftarrow value [SI] (new max found)

17) `nxt : dec CL ; CL <= CL - 1`
18) `jnz UP ; if CL != 0, jump to UP (repeat loop)`
19) `LEA DI, res ; DI <= offset of res`
20) `Mov [DI], AL ; res <= AL (store max value in res)`
21) `END MAIN ; end of program.`

② INPUT / OUTPUT ANALYSIS :

(a) Input :

Count = 4
value = $[09h, 10h, 08h, 03h] \rightarrow [9, 16, 8, 3]$ (in decimal)

(b) Working :

initial $AL = 9$

Compare with next values:

$9 < 16 \rightarrow AL = 16$

$16 > 8 \rightarrow$ no change

$16 > 3 \rightarrow$ no change

(c) Output :

$res = 10h \rightarrow 16$ (maximum value)

③ OBSERVATION TABLE

① Input:

Sl. No.	<u>Memory Location</u>	(Data) Operand
1.	1001h	09h
2.	1002h	10h (16)
3.	1003h	05h (5)
4.	1004h	03h (3)

② Output:

Sl. No.	<u>Memory Location</u>	(Data) Operand
1.	1005h	10h (16)

OBJECTIVE - ② - Arrange the elements (8 bit number) of a given array of size N in ascending/ descending order.

④ ASSEMBLY CODE :

1). DATA

- 2) Count DB 06 ; Define Count = 6 (number of elements)
 3) Value DB 0Fh, 09h, 14h, 45h, 3Fh, 24h; array of 8 bit elements

4). CODE

5) MAIN PROC

- 2) mov AX, DATA ; AX ← Address of data segment
 8) mov DS, AX ; DS ← AX (Initialise Data Segment Register)
 9) lea DI, count ; DI ← offset address of count variable.
 10) mov CH, [DI] ; CH ← value at [DI] ⇒ CH ← count (06h)

- 11) DEC CH ; $CH \leftarrow CH - 1 \Rightarrow CH \leq 05H$
- 12) UP2: MOV CL, CH ; $CL \leftarrow CH$ (initial loop counter)
- 13) LEA SI, value ; $SI \leftarrow$ offset address of every 1st array element.
- 14) UP1: MOV AL, [SI] ; $AL \leftarrow$ value at $[SI]$ (current element)
- 15) CMP AL, [SI+1] ; Compare AL with $[SI+1]$ (next element)
- 16) JC DOWN ; if $AL < [SI+1]$, jump to DOWN (no swap)
above for ascending ORDER.
~~& JNC DOWN ; if $AL \geq [SI+1]$, jump to DOWN (no swap)~~
↳ for DECENDING ORDER
- 17) MOV DL, [SI+1] ; $DL \leftarrow [SI+1]$ (stores nt element in DL)
- 18) XCHG [SI], DL ; Exchange DL with $[SI] \Rightarrow [SI] \leftrightarrow DL$
- 19) MOV [SI+1], DL ; $[SI+1] \leftarrow DL$ (original $[SI]$)
- 20) DOWN: INC SI ; $SI \leftarrow SI + 1$ (move to next element)
- 21) DEC CL ; $CL \leftarrow CL - 1$ (decrease inner loop counter)
- 22) JNZ UP1 ; if $CL \neq 0$, jump to UP1 (cont. inner loop)
- 23) DEC CH ; $CH \leftarrow CH - 1$ (dec outer loop counter)
- 24) JNZ UP2 ; if $CH \neq 0$, jump to UP2 (next outer loop part)
- 25) END MAIN ;

② INPUT / OUTPUT ANALYSIS:

(a) Input:

- Count = 6

- Value = [0FH, 09h, 14h, 45h, 3Fh, 24h]

→ [15, 9, 20, 69, 63, 36] (in decimal)

(b) Program Action:

- The program uses bubble sort logic

- Compares adjacent elements

- if current element is greater than next, they are swapped.
(asc order)

- if current element is smaller than next, they are swapped (descending order)

- This is repeated (Count - 1) times to sort the entire array in required order.

(c) Output:

- Value - (for ascending order)

[09h, 0FH, 14h, 24h, 3Fh, 45h] → [9, 15, 20, 36, 63, 69]
(in decimal)

- Value - for descending order)

[45h, 3Fh, 24h, 14h, 0FH, 09h] → [69, 63, 36, 20, 15, 9]
(in decimal)

③ OBSERVATION TABLE: (a) for ascending orders

<u>Input</u>			<u>Output</u>		
S.No.	Memory location	Data	S.No.	Memory location	Data
①	0710 : 0000	06	①	0710 : 0000	06
②	0710 : 0001	0F	②	0710 : 0001	09
③	0710 : 0002	09	③	0710 : 0002	0F
④	0710 : 0003	14	④	0710 : 0003	14
⑤	0710 : 0004	45	⑤	0710 : 0004	24
⑥	0710 : 0005	3F	⑥	0710 : 0005	3F
⑦	0710 : 0006	24	⑦	0710 : 0006	45

(b) for Descending Order:

<u>Input</u>			<u>Output</u>		
S.No.	Memory location	Data	S.No.	Memory location	Data
①	0710 : 0000	06	①	0710 : 0000	06
②	0710 : 0001	0F	②	0710 : 0001	45
③	0710 : 0002	09	③	0710 : 0002	3F
④	0710 : 0003	14	④	0710 : 0003	24
⑤	0710 : 0004	45	⑤	0710 : 0004	14
⑥	0710 : 0005	3F	⑥	0710 : 0005	0F
⑦	0710 : 0006	24	⑦	0710 : 0006	09

CONCLUSION: In this lab, we implemented fundamental 8086 assembly program like finding the largest / smallest number and arranging the elements in ascending / descending order. We learnt how data processed using registers & memory, improving our understanding on assembly programming.

POST LAB:

(Q1) What are the directives available for data declaration in 8086 microprocessors?

Soln: The directives available are:

DB (Define Byte): Allocate 1 Byte of memory

DD (Define Doubleword): Allocate 4 bytes

DQ (Define Quadword): Allocate 8 bytes

DT (Define Ten bytes): Allocate 10 bytes

DW (Define word): Allocate 2 bytes.

(Q2) State the difference b/w ENP, ENDP, ENDS directives?

Soln) END

Indicates the end of assembly program. It's placed at the last line of Program

- END Program (whole).

END P

marks the end of a procedure within the program. It used when defining the procedures

- END Procedure)

ENDS

signifies the end of a segment (a block of code, data / stack)

- ~~END~~ END segment

(Q3) find the sum & average of a given array of size N.

Soln) .DATA

ARRAY DB 10, 20, 30, 40, 50

N DB 5

SUM DW 0

AVG DW 0

.CODE

MAIN:

Mov AX, @DATA

Mov DS, AX

Mov SI, 0

Mov CX, N

Mov BX, 0

FIND_SUM:

Mov AL, ARRAY[SI]

Add BL, AL

Inc SI

Loop FIND_SUM

Mov SUM, BX

Mov AX, BX

Mov CX, N

Mov DX, 0

Div CX

END MAIN.