# Computer Organization and Architecture (EET2211)

## LAB V: Analyze and evaluate different Array operations using ARM processor.

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Branch:		Section:	
S. No.	Name	Registration No.	Signature

Marks:	/10

Remarks:

**Teacher's Signature** 

### I. OBJECTIVE:

- 1. Find the largest number in each array of size N.
- 2. Find the smallest number in each array of size N.
- 3. Separate Even numbers and odds numbers in an array of size N.

### II. PRE-LAB

Note: For each objective in prelab describe the following points:

- Write the pseudocode.
- Write the assembly code with description (ex. Mov ax,3000h ax<-3000h)
- Examine & analyze the input/output of assembly code.

### III. LAB

Note: For each objective in prelab do the following job and assessment:

- Write the Assembly language program (ALP)
- Observations (with screenshots)

## IV. CONCLUSION

### V. POST LAB

- 1. Explain briefly condition codes (flags) of ARM processor.
- 2. Which condition codes (flags) is considered for the following branch instructions?
  - a. B Label
  - b. BEQ label
  - c. BLT label