A runtime environment for reversible parallel programs

Takashi Ikeda Shoji Yuen

Nagoya University

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Background

Reversible execution of programs.

- Debugging parallel programs is not that easy.
 Replay may select different interleaving combination.
- Additional information is kept in order to recover the computation.
- Stack Annotation[HoeyUlidowski17, HoeyUlidowskiY18, HoeyUlidowski19]
- ► Runtime by reversible abstract machine for process calculi[Lienhardt+12] .

Backtrack reversibility:

Keeping the log of the detailed interleaving tracking back according to the log.

Motivation

- Inspired by [HoeyUlidowski19] and Hoey's PhD work.
- Reversing simple parallel programs at high-level with simple block constructs.
- In [HoeyUlidowski19], the operational semantics is defined by SOS rules for syntactic constructs with annotations. (forward/backward)
- The execution mechanism is usually much simpler with variable UPDATES controlled by GOTO's. Not-structured.

Aiming at simpler implementation of reversible runtime:

- Goto control structures
- Preserving variable updates history
- ▶ Built-in Concurrency (Python Library)



Contents

• A simple programming language with parallel blocks

Reversing Jumps and Updates

• Demo (7min 30sec video)

Concluding remarks

Motivating Example[HoeyUlidowski19]

Airline Ticket Sales simulation:

Two travel agents sell 3 seats.

The agents sell 4 seats due to race.

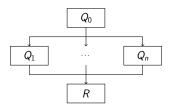
When seats=1, P1 at line 9 and P2 at line 17.

```
1: var seats:
 2: var agent1;
 3: var agent2;
 4: seats=3;
 5: agent1=1;
 6: agent2=1;
 7: par{
       while (agent1==1) do
 8:
            if (seats>0) then
 9:
10:
                seats=seats-1;
11:
           else
12:
                agent1=0;
13:
           fi:
14:
       od
```

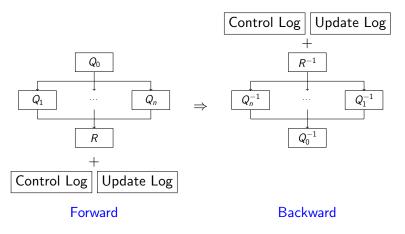
```
15: }{
16:
      while (agent2==1) do
17:
           if (seats>0) then
18:
               seats=seats-1;
19:
           else
20:
               agent2=0;
21:
           fi:
22:
      od
23: }
24: remove agent2;
25: remove agent1;
26: remove seats;
```

Programming Language with Parallel Composition

```
\begin{split} P &::= DQR \mid DQ \text{ par } \{Q\}(\{Q\})^+ R \\ D &::= (\text{var } X\,;)^* \\ R &::= (\text{remove } X\,;)^* \\ Q &::= (S;)^* S \\ S &::= \text{skip } \mid X = E \mid \text{if } C \text{ then } Q \text{ else } Q \text{ fi } \mid \text{ while } C \text{ do } Q \text{ od } E &::= X \mid n \mid E \text{ op } E \mid (E) \\ C &::= B \mid C \text{ && } C \mid \text{not } C \mid (C) \\ B &::= E = E \mid E < E \end{split}
```



Reversible Execution of Programs

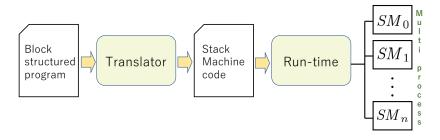


Just reverse the forward executions to be backward

Additional information needs be kept in extra memory.

Lower Level execution with concurrency

- Each block is sequentially executed by a stack machine.
- ► The backward computation is executed by the same stack machine with additional information.
- ▶ The operational semantics does not change for both directions.



Stack Machine Code (Operations)

Forward	ipush	i	Load immediate <i>i</i>
	load	V	Load v to the stack top
	store	V	Store the stack top to ν and pushes the previous to the value stack.
	јрс	a	Jump to a if the stack top is 0
	jmp	a	Jump to <i>a</i> always
	ор	n	Apply op_n to the stack
Backward	rjmp		Reverse jump. Jump to the address popped of the label stack.
	restore	V	Pops the value of v from the value stack.

Stack Machine Code(Directives)

label	n	Target of jmp and jpc and pushes the address to the label stack where n is the program length.
alloc	V	Alloc v to the environment and set v popping from the initial stack.
free	V	Dealloc v from the environment and push v to the final stack.

par	0	Beginning of a parallel block. Al-	
		locate one stack machine.	
par	1	End of a parallel block. Synchro-	
		nize termination with other paral-	
		lel blocks.	

Code Inversion

Generate the code for backward from forward

 $s: Forward \Rightarrow i(s): Backward$

$$i(s) = \begin{cases} \varepsilon & \text{if } s = \varepsilon \\ i(s')inv(c) & \text{if } s = cs' \end{cases}$$

```
\begin{array}{ll} \operatorname{inv}(\langle\operatorname{store}\,v\rangle) = \langle\operatorname{restore}\,v\rangle, & \operatorname{inv}(\langle\operatorname{jpc}\,a\rangle) = \langle\operatorname{label}\,0\rangle, \\ \operatorname{inv}(\langle\operatorname{jmp}\,a\rangle) = \langle\operatorname{label}\,n\rangle, & \operatorname{inv}(\langle\operatorname{label}\,n\rangle) = \langle\operatorname{rjmp}\,0\rangle, \\ \operatorname{inv}(\langle\operatorname{par}\,0\rangle) = \langle\operatorname{par}\,1\rangle, & \operatorname{inv}(\langle\operatorname{par}\,1\rangle) = \langle\operatorname{par}\,0\rangle, \\ \operatorname{inv}(\langle\operatorname{alloc}\,v\rangle) = \langle\operatorname{free}\,v\rangle, & \operatorname{inv}(\langle\operatorname{free}\,v\rangle) = \langle\operatorname{alloc}\,v\rangle \end{array}
```

For other c, $inv(\langle c n \rangle) = \langle nop 0 \rangle$

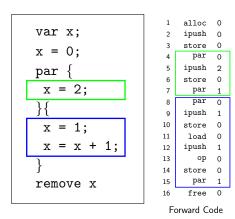
```
var x;
par {
x = 2;
x = x + 1;
remove x
```

At termination, x is either 2 or 3. x = 3, when x=2 is executed between x=1 and x=x+1.

```
var x;
remove x
```

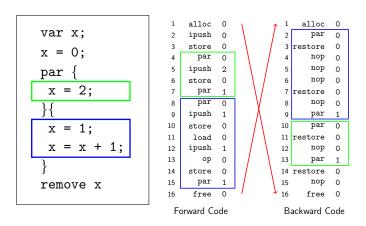
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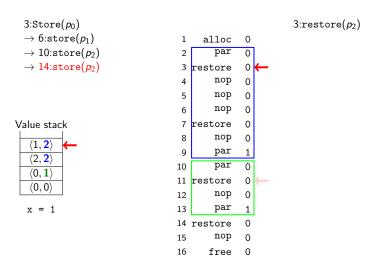
x is 3, when x=2 is executed between x=1 and x=x+1.

Forward execution:

```
3:Store(p_0)
    \rightarrow 6:store(p_1)
    \rightarrow 10:store(p_2)
    \rightarrow 14:store(p_2)
Value stack
       \langle 1, 2 \rangle
       \langle 2, \mathbf{2} \rangle
       \langle 0, \mathbf{1} \rangle
       \langle 0, 0 \rangle
     x = 2
```

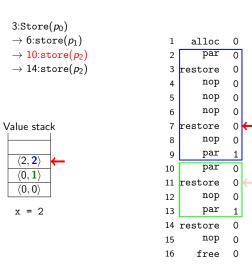
```
alloc
              0 ←
       par
              0
   restore
              0
       nop
 4
              0
       nop
              0
       nop
              0
 6
              0
  restore
 8
       nop
              0
       par
              0
10
       par
  restore
11
       nop
              0
12
       par
13
14 restore
              0
15
       nop
              0
16
      free
              0
```

Forward execution:



Forward execution:

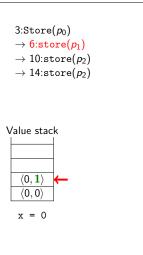
Backward execution:

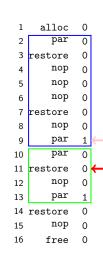


 $3:restore(p_2)$ $\rightarrow 7:restore(p_2)$

Forward execution:

Backward execution:

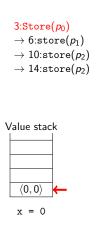


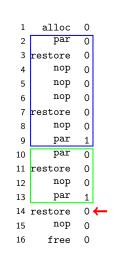


 $3:restore(p_2)$ $\rightarrow 7:restore(p_2)$ $\rightarrow 11:restore(p_1)$

Forward execution:

Backward execution:





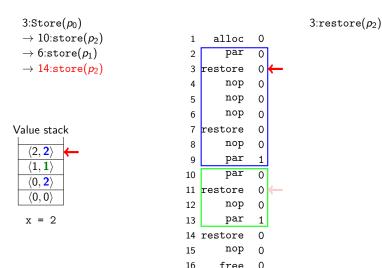
 $3:restore(p_2)$ \rightarrow 7:restore(p_2) \rightarrow 11:restore(p_1) \rightarrow 14:restore(p_0)

Forward execution:

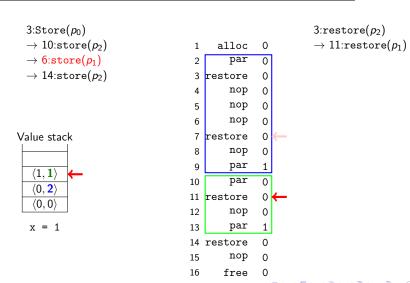
```
3:Store(p_0)
   \rightarrow 10:store(p_2)
   \rightarrow 6:store(p_1)
   \rightarrow 14:store(p_2)
Value stack
      \langle 2, 2 \rangle
      \langle 0, \mathbf{2} \rangle
      \langle 0, 0 \rangle
    x = 3
```

```
alloc
        par
              0
   restore
              0
       nop
 4
              0
       nop
 5
              0
       nop
 6
              0
   restore
              0
       nop
 8
              0
        par
 9
10
        par
  restore
              0
12
       nop
              0
13
        par
14 restore
15
       nop
16
      free
```

Forward execution:

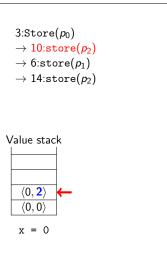


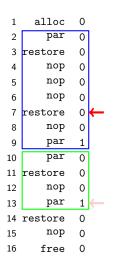
Forward execution:



Forward execution:

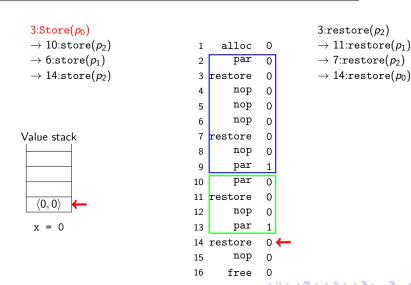
Backward execution:



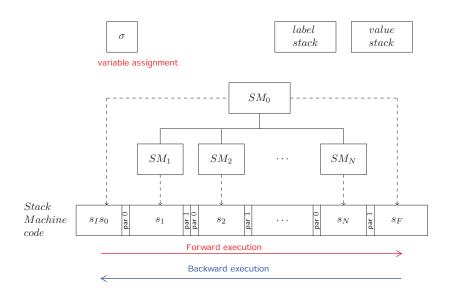


 $3:restore(p_2) \rightarrow 11:restore(p_1) \rightarrow 7:restore(p_2)$

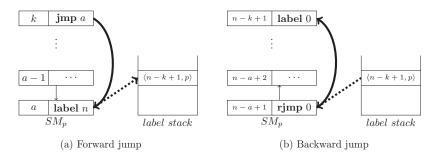
Forward execution:



Execution by Stack Machines

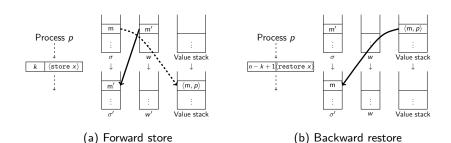


Reversing Jumps



- (a) label remembers the control to push the address of the jump origin with the process number p.
- (b) rjmp jumps back by poping up the address to return on process p.

Reversing Updates



- (a) store updates the environment from σ to σ' popping the stack top and store the old value to the value stack along with the process number p.
- (b) restore pops the old value from the value stack for the recorded process p.

Runtime Semantics

$$\frac{PC^1 \in s_0, s_I, s_F, (PC^1, PC'^1, w^1, \rho^1, \xi^1)_{\sigma^1} \xrightarrow{s(PC^1)}_{(0,N)} (PC^2, PC'^2, \rho^2, \xi^2)_{\sigma^2}}{\langle \mathsf{Exec}_s(PC^1, PC'^1, w^1), \rho^1, \xi^1 \rangle \to \langle \mathsf{Exec}_s(PC^2, PC'^2, w^2), \rho^2, \xi^2 \rangle} \, [\mathsf{Init}]$$

$$\frac{s(PC_0) = \langle \mathsf{par} \ 0 \rangle}{\langle \mathsf{Exec}_s(PC_0, PC'_0, w_0), \rho_0, \xi_0 \rangle \to} \, [\mathsf{Fork}]$$

$$\frac{s(PC_0) = \langle \mathsf{par} \ 0 \rangle}{\langle \mathsf{Exec}_s(loc(s_1), PC_0, \varepsilon) \| \cdots \| \mathsf{Exec}_s^N(loc(s_N), PC_0, \varepsilon), \sigma, \rho_0, \xi_0 \rangle} \, [\mathsf{Fork}]$$

$$\frac{(PC_p^1, PC'_p^1, w_p, \rho, \xi)_{\sigma} \xrightarrow{s(PC_p^1)}_{p}}_{\langle \mathsf{Exec}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_p^1, PC'_p^1, w_p) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, \varepsilon), \sigma, \rho, \xi \rangle}{\langle \mathsf{Exec}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_p^2, PC'_p^2, w'_p) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, \varepsilon), \sigma', \rho', \xi' \rangle}} \, \frac{\langle \mathsf{Pxec}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle}{\langle \mathsf{Exec}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle}} \, [\mathsf{Merge}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle} \, [\mathsf{Merge}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle} \, [\mathsf{Merge}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle} \, [\mathsf{Merge}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle} \, [\mathsf{Merge}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle} \, [\mathsf{Merge}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_N, PC'_N, w_N), \sigma, \rho, \xi \rangle \to \langle \mathsf{Exec}_s(loc(s_F), 0, \sigma), \rho, \xi' \rangle} \, [\mathsf{Merge}_s^1(PC_1, PC'_1, w_1) \| \cdots \| \mathsf{Exec}_s^N(PC_1, PC'_1, w_1)$$

Implementation

- ► Stack machine code generation:
 - Translator from Block-structured programs to Stack Machine codes is implemented by JAVACC.
- Stack Machine by Python
 - Running SMs with Python Multi-process module.
 - Execute VMs backward poping the value stack and the label stack.
 - nops are executed concurrently.

Demo

https://github.com/syuen1/RevRunTimeEnv

- Running the airline example with our SMs.
- ▶ For better understanding, we present an experimental GUI.

Concluding remarks

Summary:

- Executing a simple parallel program with Stack Machines
- Stack machine codes are flat and not-structured.
- Code for a backward execution are generated from the code for the forward execusion by changing the operations one by one and reversing the code sequence.
- Backward execution only preserve updates points. Other operations are discarded (unlike JANUS). Enough for the basic debugging?

Future work:

- Development of a debugger. Moving forward/backward at update points.
- Extend the parallel program syntax.
 Recursion with dynamic process invocation
- ▶ Show the basic properties for reversibility.