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# Supplemental Material for Submission #169

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## ABSTRACT

Android packers have been widely adopted by developers to protect apps from being plagiarized. At the same time, attackers have abused packers to “protect” malware and evade the detection. Although recent proposed methods could *unpack* apps protected by traditional packers through direct memory dumping, the quickly-evolving commercial packers adopt a virtual machine (VM) based protection, which stores the customized bytecode instead of the original Dalvik bytecode in the memory. This defeats memory dumping mechanism. However, little is known about whether such packers can provide enough protection to benign apps, or whether we can still analyze the malware “protected” by them.

In this paper, we aim to shed light on these questions, and take the first step towards recovering the original Dalvik bytecode from VM-protected apps and reconstructing the Dex file, which can be further analyzed by the off-the-shelf tools. In particular, we propose a novel semantics-based solution consisting of two phases: a training phase to collect necessary information of a target packer’s VM and a recovering phase to recover the original Dalvik bytecode by leveraging the obtained information in the training phase. We develop a prototype named *Paréma* and evaluate it through commercial VM-based Android packers and packed malware. The experimental result demonstrated its effectiveness.

## A MOTIVATING EXAMPLE

To illustrate the basic idea of VM-based packing technique in this section and that of our semantics-based unpacking approach in §B), we design an example following the basic packing mechanism of the VM-based packer in [45].

Fig. 8 shows the method *vmpFunc()* before and after being protected. This function first assigns constants 20 and 25 to the integer variables *a* and *b*, respectively, and then assigns the summarization of these two variables to the variable *s*. As shown in Fig. 4, the packing process consists of two steps: (1) turning the DCode to the intermediate bytecode. In particular, the opcodes are changed according to the fixed rules whereas the operands are unaltered. For example, the opcode 0x13 is converted to 0xca; (2) converting the intermediate bytecode to PCode with an app-specific parameter through *xor*. Since the second step introduces the app-specific parameter, there is no fixed mapping relationship between the PCode instructions and the DCode instructions for all apps protected by this packer.

Moreover, as shown in Fig. 8, the original method *vmpFunc()* is replaced by a native method that serves as the entry of the context switch between PVM and Android runtime. That is, the execution context goes into PVM for interpreting the PCode instructions of the VM-protected method when the method is invoked, and returns to Android runtime from PVM when the method returns.

We also implement an indirect-threaded interpreter (i.e., Fig. 2(b)) for this VM-based packer, and show two handlers (i.e., *PHandler\_Const16()* and *PHandler\_AddInt()*) in Fig. 9, which are

<pre>1 void vmpFunc(...) { 2   int a = 20, b = 25; 3   int s = a + b; 4   ... 5 }</pre>	<pre>1 native void vmpFunc(int m_id);</pre>
Original implementation	After VM-based obfuscation

Figure 8: The method before and after VM-protection in Dex file.

used to interpret the PCode instructions in method *vmpFunc()*. Being invoked by *vmpFunc()*, the function *execute()* (Line 43) is the entrance of the interpreter and its argument *m\_id* represents ID of the invoked VM-protected method, which is used by the interpreter to locate the PCode of the method to be interpreted. More precisely, the interpreter first locates the memory region PCode storing the PCode of the target method by invoking function *lookup\_pcode()* (Line 47) and then obtains the app-specific parameter *pwd* through function *init\_decoding\_factor()* (e.g., 0x8080) (Line 49). The virtual program register *vpc* points to the PCode to be decoded. To interpret the PCode instruction indexed by *vpc* (Line 14, 34, and 52), the interpreter firsts recover the intermediate bytecode by calculating the *xor* of PCode and the app-specific parameter (i.e., *pwd*), and then calls the proper PHandlers according to the opcodes (Line 16, 35, 49 in Fig. 9) of the intermediate bytecode.

This interpreter utilizes an array to emulate the virtual registers (i.e., Line 11 and 31 in Fig. 9). Since this interpreter is indirect-threaded for better performance than the decode-dispatch interpreter, each PHandler has its own decode-dispatch procedure, such as Line 5-14 in *PHandler\_Const16()* and Line 24-34 in *PHandler\_AddInt()*. Hence, there is no a decode-dispatch loop in the CFG, which has been used to reverse engineer PVM for protecting desktop programs [33].

To explain how PCode is executed by PVM as shown in Fig. 9, We take the first instruction *c208 081c* (e.g., *const/16 v0, #int 20*) as an example and list the corresponding results in Fig. 10. The first bytecode *c208* is translated to the intermediate code *ca00* with the app-specific parameter *pwd* (i.e., 0x0808) (Line 52). Then, according to the recovered opcode *ca*, the corresponding PHandler *PHandler\_Const16* is called (Line 55). In *PHandler\_Const16*, after *xoring* the two bytecodes and the app-specific parameter (Line 5-6 in Fig. 9), we obtain the operands 0x00 and 0x0014 (Line 8-9) according to the syntax of PCode instruction *Const/16*, which is represented by opcode *ca*. Finally, the semantics of this instruction is executed by the PHandler with the operands (Line 11).

## B EXAMPLE

We use the motivating example in §A to illustrate our solution of recovering the VM-protected DCode.

**Training.** To reverse engineer the interpreter shown in Fig. 9 and learn the rules (Table 1) for recovering the VM-protected DCode, we first implement several training apps that put all possible DCode instructions into the methods to be protected by the VM-based packer and then upload them to the packer’s server. After obtaining the VM-protected training apps, we run them and collect the

```

1509 1 int vRegs[]; // The virtual register array
1510 2 int PHandlers[]; // The PHandler table
1511 3 int pwd; // The app-specific decoding factor
1512 4 void PHandler_Const16(int vpc, int16_t PCode[]) {
1513     5 int16_t bytecode1 = PCode[vpc] ^ pwd;
1514     6 int16_t bytecode2 = PCode[vpc+1] ^ pwd;
1515     7 // Parse the operands
1516     8 int8_t vreg = bytecode1 & 0xff;
1517     9 int16_t value = bytecode2;
1518     10 // Execute the semantics
1519     11 vRegs[vreg] = value;
1520     12 vpc += 2; // Point the next instruction
1521     13 // Decode the next instruction
1522     14 int8_t next_opcode = (PCode[vpc] ^ pwd & 0xff00) >> 0x8;
1523     15 // Dispatch PHandler for the next instruction
1524     16 void (*pHandler)(int, int16_t*);
1525     17 pHandler = (void*)(int, int16_t*)PHandlers[next_opcode];
1526     18 if (pHandler != NULL):
1527     19     pHandler(vpc, PCode); // Invoke the target PHandler
1528     20 return;
1529     21 }
1530     22
1531     23 void PHandler_AddInt(int vpc, int16_t PCode[]) {
1532     24 int16_t bytecode1 = PCode[vpc] ^ pwd;
1533     25 int16_t bytecode2 = PCode[vpc+1] ^ pwd;
1534     26 // Parse the operands
1535     27 int8_t vreg1 = bytecode1 & 0xff;
1536     28 int8_t vreg2 = (bytecode2 & 0xff00) >> 0x8;
1537     29 int8_t vreg3 = bytecode2 & 0xff;
1538     30 // Execute the semantics
1539     31 vRegs[vreg1] = vRegs[vreg2] + vRegs[vreg3];
1540     32 vpc += 2; // Point to the next instruction
1541     33 // Decode the next instruction
1542     34 int8_t next_opcode = (PCode[vpc] ^ pwd & 0xff00) >> 0x8;
1543     35 // Dispatch PHandler for the next instruction
1544     36 void (*pHandler)(int, int16_t*);
1545     37 pHandler = (void*)(int, int16_t*)PHandlers[next_opcode];
1546     38 if (pHandler != NULL):
1547     39     pHandler(vpc, PCode); // Invoke the target PHandler
1548     40 return;
1549     41 }
1550     42 // Argument is the ID of the target vm-protected method
1551     43 void execute(int16_t m_id) {
1552     44 // Initialize the virtual PC, registers and the handler table
1553     45 int vpc = 0;
1554     46 // Lookup the PCode of the target method according to m_id
1555     47 int16_t *PCode = lookup_pcode(m_id);
1556     48 init_virtual_registers(vRegs);
1557     49 init_decoding_factor(&pwd); // pwd=0x0808 in this example
1558     50 init_PCode_handlers(PHandlers);
1559     51 // Decode the first instruction
1560     52 int8_t opcode = (PCode[vpc] ^ pwd & 0xff00) >> 0x8;
1561     53 // Dispatch PHandler for the first instruction
1562     54 void (*pHandler)(int, int16_t*);
1563     55 pHandler = (void*)(int, int16_t*)PHandlers[opcode];
1564     56 if (pHandler != NULL):
1565     57     pHandler(vpc, PCode); // Invoke the target PHandler
1566     58 return;
1567     59 }

```

Figure 9: An indirect-threaded interpreter provided by the PVM to interpret the PCode instructions, and the implementations two handlers (i.e., *PHandler\_Const16()* and *PHandler\_AddInt()*) are shown.

PCode	App-specific factor	Intermediate code	PHandler	Operands
1 c208 081c	^ 0x0808	ca00 0014	PHandler_Const16	0x00, 0x0014
2 c209 0811		ca01 0019	PHandler_Const16	0x01, 0x0019
3 8b0a 0809		8302 0001	PHandler_AddInt	0x02, 0x00, 0x01
...		...	...	...

Figure 10: The decode-dispatch procedure of the VM-protected methods when the packed app runs on the smartphone.

execution trace. Then, we identify the rules (i.e., PH2D, P2PH and PAM) by analyzing the execution traces through step ①–⑤ in Fig. 3.

PAM represents the PCode addressing mechanism implemented in the function *lookup\_pcode()* (Line 47 in Fig. 9), and P2PH refers to the decode-dispatch procedure (Line 14, 34 and 52 in Fig. 9). PH2D, the

mapping from PHandler to the DCode instruction, is learnt through comparing the DCode instructions in the original training apps and the PHandlers of the corresponding PCode instructions in the packed versions. For example, the “const/16” DCode instruction is translated to a “const/16” PCode instruction in the packed app, which will be executed by the function *PHandler\_Const16()*. Hence, if *PHandler\_Const16()* is invoked, it means the original DCode instruction is “const/16”. Moreover, by analyzing the execution trace of *PHandler\_Const16()*, we can further reverse engineer the syntax of PCode, of which the low byte of the first bytecode and the second bytecode are the operands (Line 8–9 in Fig. 9).

**Recovering.** To recover the VM-protected DCode of a target app packed by this packer, Parema first runs to collect the required information, such as the *pwd* (Line 49 in Fig. 9) and PHandlers (Line 50 in Fig. 9) involved in the decode and dispatch procedure. In step ①, Parema leverages PAM to find the PCode of this VM-protected method according to the method ID (i.e., *m\_id*). In step ②, Parema utilizes P2PH to generate the offset signatures of the PHandlers with the PCode, *pwd* and PHandlers. In step ③, Parema recovers the DCode according to offset signatures of the PHandlers based on PH2D.

## C IMPLEMENTATION

We implement our solution in a prototype named Parema, which has a dynamic tracking module and a static analysis module, with around 9k lines of C/C++ code and 3k lines of Python script, calculated by CLOC [10]. Parema adopts the similar mechanism of PackerGrind [39] to dynamically collect the DCode in the memory, which is not protected by the VM-based approach. Specifically, we wrap the function ART runtime functions (i.e., *ArtMethod::Invoke()*) and instrument the memory modification statements (i.e., *IR\_Load* and *IR\_Store*) using Valgrind. Then we collect the Dex items in the wrapping function by parsing the Dex files in memory [6]. We reconstruct the original Dex file by combining the DCode recovered from VM-protected methods and that collected from the memory.

### C.1 Dynamic Tracking Module

Based on Valgrind [30], the dynamic tracking module monitors the behaviors of a packed app, logs its execution trace, and collects Dex data without VM-based protection. Parema runs the packed apps on real mobile devices, and collects the execution trace. To track the IR statements and expressions, we insert IR statements before each IR statement to invoke a helper function for instrumentation. Parema logs all the executed IR statements and the following information from Android framework, Android runtime, and the underlying Linux system to facilitate the analysis and DCode recovery: (1) The events in Android framework (e.g., invocations of the Java methods in an app and/or the Android framework). Such information helps us locate and verify the PHandlers; (2) The events in Android runtime and the invocations of JNI reflection functions. For instance, by tracking the invocation of *art\_quick\_generic\_jni\_trampoline*, we know that the execution context switches from Android runtime to PVM. Moreover, the JNI reflection functions help us identify and analyze the method invocation instructions; (3) The events in the underlying Linux system, including (a) the memory related operations, such as *malloc()*, *free()*, *sys\_map()*, and *sys\_unmap()*, which

**Table 8: The performance evaluation with CF-Bench [4].**

	Native Scores			Java Scores			Overall Scores		
	Mean	STD	Slowdown	Mean	STD	Slowdown	Mean	STD	Slowdown
<b>Base</b>	29285.53	1825.26		10570.57	2084.82		18056.20	1690.00	
Valgrind	2740.33	167.49	10x	2416.00	145.24	4x	2545.33	151.45	7x
Parema	753.67	43.37	39x	674.79	36.27	15x	709.97	40.71	25x

will be used for generating the offset signatures of the PHandlers. (b) The modifications and translations of the data in the memory. These information can be obtained by monitoring the data translation functions (e.g., *strcpy()* and *memcpy()*), and they will help us locate the interested data, such as the Dex data items. (c) Other special behaviors of the packers, such as *sys\_ptrace()* for anti-debug and *sys\_exec()* to execute special commands. We collect them by monitoring the system calls.

## C.2 Static Analysis Module

The static analysis module aims at accomplishing two major tasks. First, in the training phase, it follows the approach introduced in §?? to recognize P2PH and PH2D and identify PAM by analyzing the execution trace of the VM-protected training apps. Second, in the recovering phase, it first recovers the original DCode of the target apps following the mechanism in §3.3.1 and §3.3.2, and then utilizes the mechanism in §3.3.3 to reconstruct the Dex files with the recovered DCode and the Dex data collected from the memory.

## D PERFORMANCE

We evaluate the overhead of Parema’s dynamic tracking subsystem through running CF-Bench [4] without DBI (Base), with only Valgrind, and with Parema, respectively. From the results shown in Table 8, we observe that Parema brings 39x and 15x slowdown to the native score and Java score receptively, and the overall slowdown is 25x. Note that even we just run Valgrind without Parema, it introduces 7x overall slowdown. Therefore, Parema just brings 3.7 (25x/7x) additional overall slowdown to Valgrind. Compared with the emulator-based DBI systems [41] that bring 11-34x slowdown, the performance of Parema is reasonable and acceptable.

## E ENHANCING VM-BASED PROTECTION

By analyzing the latest publically available VM-based Android packers (i.e., Qihoo and Baidu), we find, although they obviously increase the bar of unpacking, the apps packed by these packers are still possible to be unpacked by Parema. These VM-based Android packers adopt one-to-one mapping between the DCode instructions and the PCode instructions, and the PCode instructions adopt similar syntax as the DCode instructions. Although they add app-specific parameters to the decode-dispatch processes, the factors can also be recognized by comparing the decode-dispatch processes of the different training apps and then identified from the execution trace of the target apps during recovering. There are still various techniques that the packers can utilize to become more secure. For example, they can translate one DCode instruction into various PCode instructions, which have completely different syntaxes from the PCode instruction. They can also add the instruction-specific factors instead of the app-specific parameters. In addition, applying

the app-specific PVM to the packed apps can also make the packers more sophisticated.

## F CFGS VS. SEGS

Fig. 11 shows the CFGs and the SEGs generated from the execution trace of a training app packed by Qihoo and Baidu, respectively. The two CFGs contain 396 nodes/509 edges (Fig. 11(a)) and 835 nodes/979 edges (Fig. 11(c)), separately. By contrast, there are only 48 nodes/69 edges and 22 nodes/35 edges in the SEGs as shown in Fig. 11(b) and Fig. 11(d).



