#1. Determine bias points in the following circuits. In which of the following circuits, the base current can be neglected?

B=100, IVbe, on 1=0.7°, IVce, sat1=0.2°

$$V_{in} = \frac{1.5 \times 10}{1.5 + 1} = 6$$

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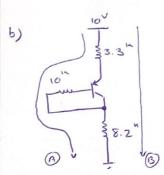
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-10 + 4.7 Lc + VCE + 10" Lc = 0 Ic: 0.53" VCE = 2.209" > VCE, sat



KUL @ A: -10+3.3 [c+0.7+10] B+8.2 [c... | KUL @ A: -10+1 [c+0.7+39] 56 ([c) | 100)

KVL@B: -10+331c+VcE+8.21c=0

#2, calculate the input resistance (R:) as well as the voltage gain (Au = Vo) in the circuit shown below. VA = ∞ , VT = 25 mV , B=100 capacitor in de : open circuit : hint . capacitor in ac : short circuit Vin Prok dc Analysis: $\frac{15^{V}}{3}$ | $\frac{15^{V}$ KUL: -15 + 1" Ic + VCE = 0 1c=7.85 > $\int_{0}^{\infty} \frac{I_{c}}{V_{T}} = 40 I_{c} = 286 \text{ mmho}$ $V_{T} = \frac{\beta}{9m} = \frac{100}{286} = 0.34$ $V_{T} = \frac{V_{A}}{I_{c}} = \infty$ Av = Vout = -9mRc = -286 (10" | 1") = -259.99 Rin = 200 | | rn = 200 | 0.34 = 0.34 #3. In the following circuit, calculate the Voltage gain, input resistance and output resistance. VBE: 0.6, VCE, sat: 0.2, VA: 100, VT: 25", B: 100 KVL: -12 + 2.2 [c + VCE + 0.68 [c = 0

=> VCE : 4.8 > VCE, sat

ac Analysis:
$$g_{m} = 40 I_{c} = 40 \cdot 2.5 = 100^{mmho}$$
. $r_{\pi} = \frac{\beta}{9m} = \frac{100}{100} = 1^{K}$

$$r_{0} = \frac{V_{A}}{I_{c}} = \frac{100}{100} = 1^{K} \cdot 40^{K}$$

$$R_{0} = \frac{V_{0}ut}{V_{in}} = \frac{-R_{c}}{R_{c} + r_{m}} = \frac{-(2.2^{K}||10^{K}|)}{0.68^{K} + \frac{1}{100}} = -2.61$$

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$$R_{0} = \frac{10^{K}}{100} = 1^{K}$$

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$$\frac{h_{int}}{R_{int}} = \frac{1}{\sqrt{3}} R_{E}$$

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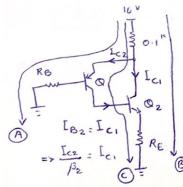
$$\frac{1}{\sqrt{3$$

#4. In the following circuit:

a) Determine the bias points of the transistors. Assume RE=0

b) Calculate the maximum value of RE for which Q, remains the active region.

Vcc = 16 , Rc = 0.1 , RB = 1500 , VcE, sat = 0.2 , VBE, ON = 0.7 , B = 160



a)
$$\text{KVL} \otimes A : -16 + 0.1^{K} \left(I_{c_{1}} + I_{c_{2}} \right) + 0.7 + 1500^{K} I_{B_{1}} = 0$$

$$= > 9.475 I_{c_{1}} + 0.1 I_{c_{2}} = 15.3 I_{c_{2}} = \beta_{2} I_{c_{1}} > I_{c_{1}} = 0.51^{MA}$$

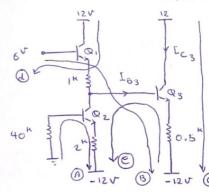
$$I_{c_{1}} = 0.51^{MA}$$

$$I_{c_{2}} = 103^{MA}$$

KVL @ C: -16 + 0.1 ([c1+[c2]) + VCE1 + 0.7 = 0 => VCE1 = 4.95 > VCE, sat

$$\begin{split} & I_{G2} = I_{C1} = 0.51 \implies \frac{I_{C2}}{\beta_2} = 0.51 \\ & = > I_{C2} = 200 (0.51) = 102^{MA} \\ & \text{KVL Q A : } -16 + 0.1 \text{ (} I_{C1}, I_{C2}) + \text{V}_{CE_1} + 0.7 + R_E I_{C2} = 0.51 \\ & = > 102 R_E = 5.049 - \text{VCE} \\ & R_E = \frac{5.049 - \text{VCE}}{102} \frac{\text{if VCE}}{R_E - \text{max}} R_E = \frac{5.049 - 0.2}{102} \end{split}$$

#5. In the circuit shown below, the transistor are the same. Determine the bias points.



KVL @ A = 40 (
$$\frac{\Gamma_{cz}}{100}$$
) + 0.6 - 12 = +2 $\frac{\Gamma_{cz}}{100}$ => $\frac{\Gamma_{cz}}{100}$ = $\frac{\Gamma_{cz}}{100}$

$$KVL \otimes A = 40^{\circ} \left(\frac{L_{c2}}{100}\right) + 0.6 - 12 \otimes + 2^{\circ} L_{c2} = 0$$

$$= \sum_{c_{2}} L_{c_{2}} = 4.75^{\circ} = L_{c_{1}}$$

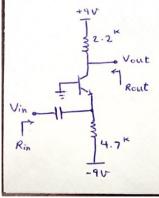
$$KVL \otimes B : -6 + 0.6 + 1^{\circ} L_{c_{1}} + 0.6 + 0.5^{\circ} L_{c_{3}} - 12 = 0$$

$$= \sum_{c_{3}} L_{c_{3}} = 24.1^{\circ} A$$

$$KVL \otimes C : -12 + V_{c_{3}} + 0.5 L_{c_{3}} - 12 = 0$$

$$= \sum_{c_{3}} V(E_{0}) = 11.95^{\circ}$$

#6. Calculate the Voltage gain, input resistance and output resistance of the following scheme.

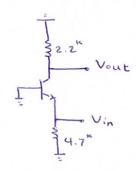


KVL @ B:
$$+0.7 + 4.7^{\circ} [c - 9 = 0]$$

=> $I_{c} = 1.76^{\circ}$

KVL @ B: $-9 + 2.2^{\circ} [c + V_{ce} + 4.7^{\circ} [c - 9 = 0]$

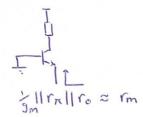
=> $V_{ce} = 5.85^{\circ}$ > V_{ce} , sat



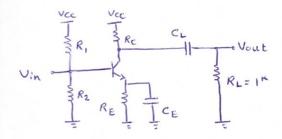
Rout =
$$4.7^{k}$$
 | $70.4 \times 2.2^{k} = 154.88$

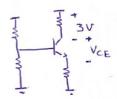
Rout = 2.2^{k} | $\infty = 2.2^{k}$

Rin = 4.7^{k} | $7m = 0.013^{k} = 13.9^{s}$



#7. In the following circuit, the voltage gain and the dc voltage drop on Rc is -48 % and 3, Respectively. Determine Rc. VCE, sat = 0 , V7 = 25 mV, R1 = 1 k





de Analysis: $\frac{1}{3}$ $\frac{3}{R_c}$ $\frac{3}{R_c}$ $\frac{3}{R_c}$ $\frac{3}{R_c}$ $\frac{3}{R_c}$ $\frac{1}{R_c}$

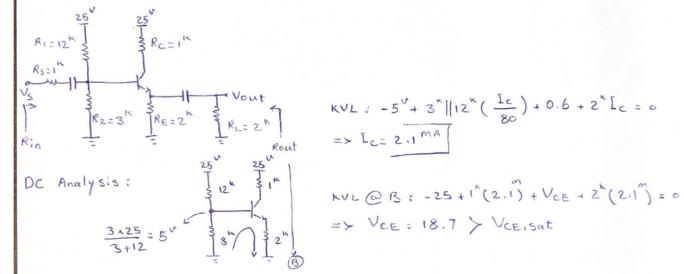
ac Analysis:

$$\frac{1}{R_{c}} = \frac{120}{R_{c}} \left(\frac{R_{c} || R_{c}}{R_{c}} \right) = -48$$

$$\frac{1}{R_{c}} = \frac{120}{R_{c}} \left(\frac{R_{c} || R_{c}}{R_{c}} \right) = -48 = R_{c} = 1.5^{k}$$

#8. In the following circuit:

- a) calculate the voltage gain (Vbe, on = 0.7)
- b) determine the output voltage (vo) swing.
- c) Modify Ri in order to maximize the or output voltage swing VBE: 0.6 , VCE, Sat = 0.2", VA: 00, VT = 25", B = 80



$$KVL: -5^{V} + 3^{K}||12^{K}(\frac{I_{c}}{80}) + 0.6 + 2^{K}I_{c} = 0$$

$$= > I_{c} = 2.1^{MA}$$

ac Analysis:
$$V_{S} = \frac{1}{2} \left[\frac{3}{2} \right] \times \frac{3}{2} \times$$

$$\int_{B=80}^{B=80} F_{\pi} = \frac{B}{J_{m}} = 0.95^{K}$$

$$Av = \frac{V_{out}}{V_{S}} = \frac{V_{out}}{V_{A}} \times \frac{V_{A}}{V_{S}} = \left[\frac{R_{E}}{R_{E} + r_{m}}\right] \times \left[\frac{(3^{k}||12^{k})||r_{R} + (1+\beta)R_{E}}{(3^{k}||12^{k}||r_{R} + (1+\beta)R_{E}) + 1}\right]$$

$$= \left[\frac{2^{k}||2^{k}|}{2^{k}||2^{k}|}\right] \times \left[\frac{3^{k}||12^{k}||80.95^{k}|}{(3^{k}||12^{k}||80.95^{k}) + 1^{k}}\right] \approx 0.98 \times 0.7 = 0.686^{k}$$

b) Swing Vo: Swing VcE = min { VcE, a - VcE, sat, Rac Ic}

$$Rac = \begin{bmatrix} R_{E} + R_{C} \end{bmatrix} \text{ in ac}$$

$$= min \left\{ 18.7 - 0.2, 2 \times 2.1^{ma} \right\} = min \left\{ 18.5, 4.2 \right\}$$

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$$= \sqrt{18.7 - 0.2} = \sqrt{18.7 - 0$$

DC Analysis:
$$V_{\overline{H}} = \frac{3 \times 25}{3 + R_1}$$

$$A$$

$$KVL @ A : \frac{-75}{R_1+3} + 0.6 + 2(5^m) = 0 => 10.6 R_1 = 43.2 => R_1 = 4^m$$