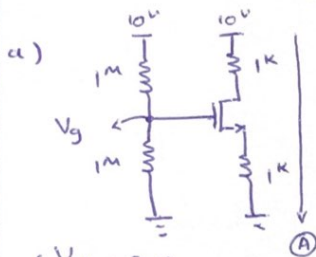


## Assignment 2 :

#1. In the following circuits, determine the bias points of the transistors.



$$V_g = \frac{1^M \times 10}{1+1} = 5^V \Rightarrow I_D = \frac{\mu}{2} \frac{W}{L} (V_{gs} - V_{th})^2$$

$$I_D = \frac{0.5}{2} (V_g - V_s - 2)^2 = \frac{1}{4} (3 - 1^K I_D)^2 \Rightarrow \begin{cases} I_D = 1^mA \\ I_D = 9^mA \end{cases}$$

if  $I_D = 1^mA$ :  $V_s = 1^V \Rightarrow V_{gs} = 5 - 1 = 4^V \checkmark \rightarrow V_{gs} > V_{th}$

if  $I_D = 9^mA$ :  $V_s = 9^V \Rightarrow V_{gs} = 5 - 9 = -4^V \times$

KVL @ A:  $-10 + 1^K I_D + V_{os} + 1^K I_D = 0 \Rightarrow \boxed{V_{os} = 8^V}$

hint: \* nmos:

$$\begin{cases} \text{Sat: } \frac{\mu}{2} \frac{W}{L} (V_{gs} - V_{th})^2 = I_D \\ \text{Triode: } \frac{\mu}{2} \frac{W}{L} [2(V_{gs} - V_{th})V_{os} - V_{os}^2] = I_D \end{cases}$$

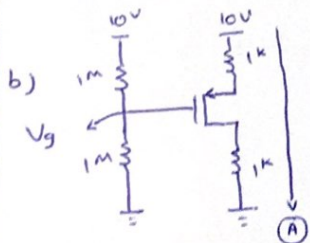
\* PMOS:

$$\begin{cases} I_D = \frac{\mu}{2} \frac{W}{L} (V_{gs} - |V_{th}|)^2 : \text{Sat} \\ I_D = \frac{\mu}{2} \frac{W}{L} [2(|V_{gs} - V_{th}|)V_{os} - V_{os}^2] : \text{Triode} \end{cases}$$

\*  $\beta = \mu C_{ox} \frac{W}{L}$   
 $= K'$

\* In Triode regin:  $|V_{gs} - V_{th}| > |V_{os}|$

\* In Saturation regin:  $|V_{os}| \geq |V_{gs} - V_{th}|$  OR  $V_{SD} \geq V_{SG} - |V_{th}|$



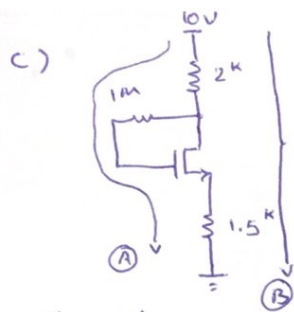
$$V_g = \frac{1^M \times 10}{2} = 5^V \Rightarrow I_D = \frac{\mu}{2} \frac{W}{L} (V_{gs} - |V_{th}|)^2 = \frac{2}{2} (5 - V_s - 1)^2$$

$$= (4 - 0.5 I_D)^2 \Rightarrow \begin{cases} I_D = 16^mA \\ I_D = 4^mA \end{cases}$$

if  $I_D = 16^mA$ :  $V_s = 20^V \Rightarrow V_{sg} = -3^V < |V_{th}|$

if  $I_D = 4^mA$ :  $V_s = 8^V \Rightarrow V_{sg} = 3^V > |V_{th}|$

KVL @ A:  $-10 + 0.5^K I_D + V_{SD} + 1^K I_D = 0 \Rightarrow V_{SD} = 4^V \geq V_{SG} - |V_{th}|$



$$\text{KVL @ A: } -10 + 2^k I_D + 1^M I_G + V_{GS} + 1.5^k I_D = 0$$

$$\boxed{3.5^k I_D + V_{GS} = 10} \quad (1)$$

$$I_D = \frac{\mu_n C_{ox}}{2} \frac{W}{L} (V_{GS} - V_{th})^2 \xrightarrow{(1)} I_D = \frac{1}{2} (10 - 3.5^k I_D - 1)^2$$

$$\begin{cases} V_{th} = 1^V \\ \mu_n C_{ox} \frac{W}{L} = 1 \frac{mA}{V^2} \end{cases}$$

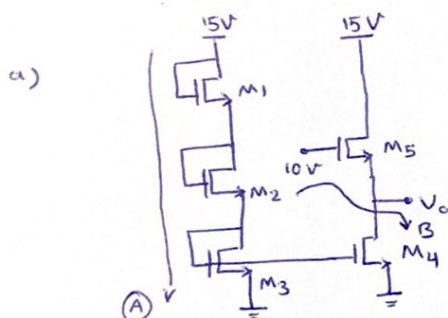
$$\begin{cases} I_D = 2^{mA} \checkmark \\ I_D = 3.3^{mA} \end{cases}$$

$$\text{if } I_D = 2^{mA} \rightarrow V_{GS} = 10 - 3.5(2) = 3^V > V_{th} \checkmark$$

$$\text{if } I_D = 3.3^{mA} \rightarrow V_{GS} = 10 - 3.5(3.3) = -1.55 < V_{th}$$

$$\text{KVL @ B: } -10 + 2^k I_D + V_{DS} + 1.5^k I_D = 0 \xrightarrow{I_D = 2^{mA}} V_{DS} = 3^V > V_{GS} - V_{th}$$

2. In the circuits shown below, all of the transistors are the same and operate in the saturation region. Calculate the output voltage.

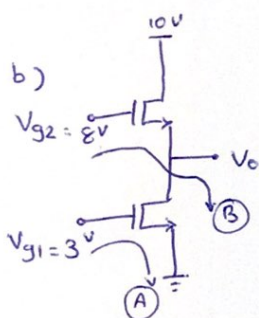


$$\text{KVL @ A: } -15 + V_{GS1} + V_{GS2} + V_{GS3} = 0 \quad \underline{V_{GS1} = V_{GS2} = V_{GS3}}$$

$$3V_{GS} = 15 \Rightarrow \boxed{V_{GS} = 5^V}$$

$$\text{KVL @ B: } -10 + V_{GS} + V_O = 0 \Rightarrow V_O = 10 - V_{GS}$$

$$\boxed{V_O = 5^V}$$

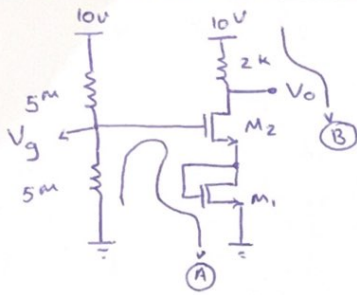


$$\begin{aligned} \text{KVL @ A: } V_{GS1} &= 3^V \rightarrow I_{D1} = I_{D2}, V_{th1} = V_{th1} \\ \Rightarrow V_{GS1} &= V_{GS2} = 3^V \end{aligned}$$

$$\text{KVL @ B: } -8 + V_{GS} + V_O = 0 \Rightarrow V_O = 8 - 3 \cdot 5^V$$

3. In the following circuit, the transistors are the same and operate in the saturation. Calculate the output voltage. Assume  $\beta = 0.5 \frac{mA}{V^2}$ ,  $V_{th} = 0.5^V$





$$V_g = \frac{5 \times 10}{10} = 5V$$

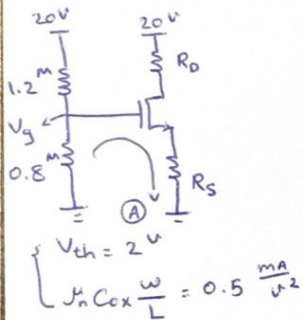
$$\text{KVL @ A: } -5V + V_{gs1} + V_{gs2} = 0 \quad \frac{V_{gs1} = V_{gs2}, V_{th1} = V_{th2}}{I_{D1} = I_{D2}}$$

$$2V_{gs} = 5 \Rightarrow V_{gs} = \frac{5}{2} = 2.5V$$

$$I_D = \frac{k'}{2} \frac{W}{L} (V_{gs} - V_{th})^2 = \frac{0.5}{2} (2.5 - 0.5)^2 = 1 \text{ mA}$$

$$\text{KVL @ B: } -10 + 2k I_D + V_O = 0 \Rightarrow V_O = 10 - 2 = 8V$$

4. specify  $R_D$  and  $R_S$  so that the transistor operates in saturation and  $I_D = 1 \text{ mA}$



$$I_D = \frac{k'}{2} \frac{W}{L} (V_{gs} - V_{th})^2 \Rightarrow V_{gs} = \sqrt{\frac{I_D}{\frac{k'}{2} \frac{W}{L}}} + V_{th}$$

$$V_{gs} = \sqrt{\frac{1 \text{ mA}}{\frac{0.5}{2}}} + 2V = 4V$$

$$V_g = \frac{0.8 \times 20}{0.8 + 1.2} = \frac{16}{2} = 8V$$

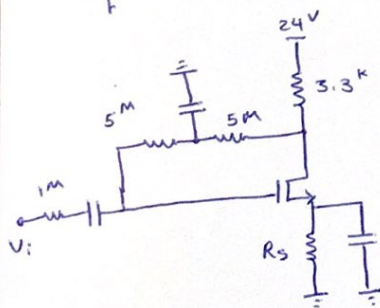
$$\text{KVL @ A: } -8 + V_{gs} + R_S I_D = 0 \quad \frac{I_D = 1 \text{ mA}, V_{gs} = 4V}{R_S = 4k}$$

برای اینکه ترانزیستور در ناحیه اشباع کار کند، باید  $V_{DS} \geq V_{GS} - V_{th}$  :  $V_D - V_S \geq 4V - 2V \Rightarrow V_D - V_S \geq 2V$  \*

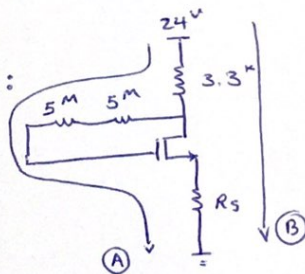
$$V_S = R_S I_D = 4k \times 1 = 4V \Rightarrow V_D \geq 6V \Rightarrow 20 - V_D = R_D I_D \quad \frac{I_D = 1 \text{ mA}}{R_D \leq 14k}$$

$$20 - 1 \text{ mA} R_D = V_D \geq 6V \Rightarrow 20 - R_D \geq 6 \Rightarrow R_D \leq 14k$$

5. specify the source resistance such that the bias point current will be equal to  $2.5 \text{ mA}$ .



DC Analysis:



$$\text{KVL @ A: } -24V + 3.3k I_D + 10M I_G + V_{gs} + R_S I_D = 0$$

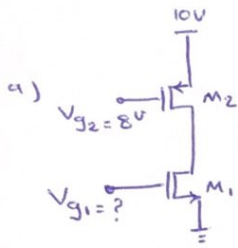
$$\Rightarrow V_{gs} = 24 - 3.3k I_D - R_S I_D = 24 - I_D (3.3k + R_S)$$

$$I_D = \frac{k'}{2} \frac{W}{L} (V_{gs} - V_{th})^2 = \frac{0.25}{2} (24 - I_D (3.3k + R_S) - 3)^2 = 2.5 \Rightarrow R_S = 2.5k \quad 7.7k$$

$$V_{gs} = 24 - 2.5 (3.3k + 2.5k) = 9.5V$$

$$\text{KVL @ B: } -24V + 3.3k I_D + V_{DS} + R_S I_D = 0 \Rightarrow V_{DS} = 9.5V > V_{gs} - V_{th}$$

6. Determine the requested parameters. Assume that the transistors are in saturation.



$$I_D = \frac{\kappa'}{2} \frac{W}{L} (V_{GS} - V_{th})^2 = \frac{19}{2} (2 - 1)^2 = \frac{1}{2} \text{ mA}$$

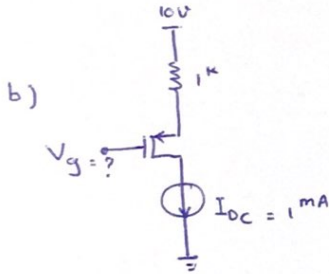
$$V_{GS1} = \sqrt{\frac{I_D}{\frac{\kappa'}{2} \frac{W}{L}}} + V_{th} = \sqrt{\frac{\frac{1}{2}}{\frac{4}{2}}} + 1 = \frac{3}{2} \text{ V}$$

$$\mu_n C_{ox} = 400 \frac{\mu A}{V^2} = 0.4 \frac{mA}{V^2}$$

$$\mu_p C_{ox} = 100 \frac{\mu A}{V^2} = 0.1 \frac{mA}{V^2}$$

$$\left(\frac{W}{L}\right)_{n,p} = \frac{1 \mu m}{0.1 \mu m}$$

$$|V_{th}| = 1 \text{ V}$$



$$I_D = \frac{\kappa'}{2} \frac{W}{L} (V_{SG} - |V_{th}|)^2$$

$$1 = \frac{8}{2} (V_S - V_G - 0.5)^2$$

$$1 = 4 (10 - 1(1) - V_G - 0.5)^2 \Rightarrow \begin{cases} V_G = 8 \text{ V} \\ V_G = 9 \text{ V} \end{cases}$$

$$\mu_p C_{ox} = 400 \frac{\mu A}{V^2}$$

$$\frac{W}{L} = \frac{2 \mu m}{0.1 \mu m}$$

$$V_{th} = 0.5 \text{ V}$$

$$V_S = 10 - 1 = 9 \text{ V}$$

$$\text{if } V_G = 8 \text{ V: } V_{SG} = 9 - 8 = 1 \text{ V} > V_{th} \checkmark$$

$$\text{if } V_G = 9 \text{ V: } V_{SG} = 9 - 9 = 0 < V_{th} \times$$