متین گلپایگانی

9114.74

آزمایش۱۰

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طراحی ساعت با کریستال **32.768KHz** و تایمر ۲

```
#include <mega32.h>
25
     #include <alcd.h>
     #include <stdio.h>
26
27
     int sec=0;
28
     int min=0;
29
     int hour=0;
30
     char time[15];
31
32
     // Timer2 overflow interrupt service routine
34 | interrupt [TIM2_OVF] void timer2_ovf_isr(void)
35 🖹 {
36
    sec++;
37 pif(sec == 60) {
         sec = 0;
38
         min++;
39
40
           sprintf(time, "%d:%d:%d", hour, min, sec);
41
42
           lcd clear();
           lcd_puts(time);
43
44
```

```
46 // External Interrupt 0 service routine
47 | interrupt [EXT_INT0] void ext_int0_isr(void)
48 □ {
49 🖨
                    if(PINB.0 == 0) {
50
51
52 🖨
                    else if(PINB.1 == 0){
                      hour++;
53
54
55
56
57
58 | void main (void)
59 日 {
60
         DDRA=(0<<DDA7) | (0<<DDA6) | (0<<DDA5) | (0<<DDA4) | (0<<DDA3) | (0<<DDA2) | (0<<DDA1) | (0<<DDA1) |
61
62
         PORTA=(0<<PORTA7) | (0<<PORTA6) | (0<<PORTA5) | (0<<PORTA4) | (0<<PORTA3) | (0<<PORTA2) | (0<<PORTA1) | (0<<PORTA1) |
63
64
        DDRB=(0<<DDB7) | (0<<DDB6) | (0<<DDB5) | (0<<DDB4) | (0<<DDB3) | (0<<DDB2) | (0<<DDB1) | (0<<DDB0);
65
        PORTB=(0<<PORTB7) | (0<<PORTB6) | (0<<PORTB5) | (0<<PORTB4) | (0<<PORTB3) | (0<<PORTB2) | (1<<PORTB1) | (1<<PORTB1) | (1<<PORTB0);
66
67
         DDRC=(0<<DDC7) | (0<<DDC6) | (0<<DDC5) | (0<<DDC4) | (0<<DDC3) | (0<<DDC2) | (0<<DDC1) | (0<<DDC0);
         FORTC=(0<<PORTC7) | (0<<PORTC6) | (0<<PORTC5) | (0<<PORTC4) | (0<<PORTC3) | (0<<PORTC2) | (0<<PORTC1) | (0<<PORTC1) |
68
69
70
         DDRD=(0<<DDD7) | (0<<DDD6) | (0<<DDD5) | (0<<DDD4) | (0<<DDD3) | (0<<DDD2) | (0<<DDD1) | (0<<DDD1) |
        PORTD=(0<<PORTD7) | (0<<PORTD6) | (0<<PORTD5) | (0<<PORTD4) | (0<<PORTD3) | (0<<PORTD2) | (0<<PORTD1) | (0<<PORTD1) | (0<<PORTD1) | (0<<PORTD2) | (0<<PORTD2) | (0<<PORTD2) | (0<<PORTD2) | (0<PORTD3) | (0<PORTD3) | (0<PORTD4) | (0<PORTD5) |
71
72
73
        TCCR0=(0<<WGM00) | (0<<COM01) | (0<<COM00) | (0<<WGM01) | (0<<CS02) | (0<<CS01) | (0<<CS00);
        TCNT0=0x00;
74
        OCR0=0x00;
75
 77
          TCCR1A=(0<<COM1A1) | (0<<COM1A0) | (0<<COM1B1) | (0<<COM1B0) | (0<<WGM11) | (0<<WGM10);
  78
           TCCR1B=(0<<ICNC1) | (0<<ICES1) | (0<<WGM13) | (0<<WGM12) | (0<<CS12) | (0<<CS11) | (0<<CS10);
  79
           TCNT1H=0x00;
           TCNT1L=0x00;
  80
           ICR1H=0x00;
  81
           ICR1L=0x00;
  82
           OCRIAH=0x00;
  83
  84
           OCRIAL=0x00:
  85
           OCR1BH=0x00;
           OCR1BL=0x00;
            // Timer/Counter 2 initialization
  88
  89
           // Clock source: TOSC1 pin
  90
            // Clock value: PCK2/128
           // Mode: Normal top=0xFF
  91
            // OC2 output: Disconnected
  92
           ASSR=1<<AS2;
  93
           TCCR2=(0<<PWM2) | (0<<COM21) | (0<<COM20) | (0<<CTC2) | (1<<CS22) | (0<<CS21) | (1<<CS20);
  95
           TCNT2=0x00;
           OCR2=0x00;
  96
  97
            // Timer(s)/Counter(s) Interrupt(s) initialization
  98
  99
           TIMSK=(0<<OCIE2) | (1<<TOIE2) | (0<<TICIE1) | (0<<OCIE1A) | (0<<OCIE1B) | (0<TOIE1) | (0<<OCIE0) | (0<<TOIE0);
 100
101
           GICR = (0<<INT1) | (1<<INT0) | (0<<INT2);
           MCUCR=(0<<ISC11) | (0<<ISC10) | (1<<ISC01) | (0<<ISC00);
102
103
          MCUCSR=(0<<ISC2);
          GIFR=(0<<INTF1) | (1<<INTF0) | (0<<INTF2);
104
105
          // Alphanumeric LCD initialization
```

```
105
106
     // Alphanumeric LCD initialization
107
     // Connections are specified in the
     // Project|Configure|C Compiler|Libraries|Alphanumeric LCD menu:
108
     // RS - PORTA Bit 0
109
     // RD - PORTA Bit 1
110
111
     // EN - PORTA Bit 2
     // D4 - PORTA Bit 4
112
     // D5 - PORTA Bit 5
113
     // D6 - PORTA Bit 6
114
115
     // D7 - PORTA Bit 7
116
     // Characters/line: 16
     lcd init(16);
117
118
     // Global enable interrupts
119
     #asm("sei")
120
121
     while (1)
122
123 🖨
124 🖨
            if(min == 60) {
125
              min = 0;
              hour++;
126
127
            if(hour == 24)
128
              hour = 0;
129
130
    }
131
132
```



