

Report Generated by Test Manager

Title: State chart test
Author: Moein-Erfan
Date: 06-Jan-2025 18:21:31

Test Environment

Platform: PCWIN64
MATLAB: (R2024a)

Summary

Name

Outcome

Duration
(Seconds)

[Results: 2025-Jan-06 18:06:47](#)

5 ✓

9.7

 [model_test](#)

5 ✓

8.99

 [Test Suite 1](#)

5 ✓

8.991

 [test_start_on](#)

✓

1.811

 [test_start_on off shut](#)

✓

1.812

 [test_start dec inc shut](#)

✓

1.681

 [test_on](#)

✓

1.613

 [test_start_on inc shut](#)


✓

1.771

Results: 2025-Jan-06 18:06:47

Result Type: Result Set
Parent: None
Start Time: 06-Jan-2025 18:06:48
End Time: 06-Jan-2025 18:06:57
Outcome: Total: 5, Passed: 5

Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision
 ex4 statecart/Chart1	Normal	13	100%

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model_test

Test Result Information

Result Type: Test File Result
Parent: [Results: 2025-Jan-06 18:06:47](#)
Start Time: 06-Jan-2025 18:06:48
End Time: 06-Jan-2025 18:06:57
Outcome: Total: 5, Passed: 5

Test Suite Information

Name: model_test

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Test Suite 1

Test Result Information

Result Type: Test Suite Result
Parent: [model_test](#)
Start Time: 06-Jan-2025 18:06:48
End Time: 06-Jan-2025 18:06:57
Outcome: Total: 5, Passed: 5

Test Suite Information

Name: Test Suite 1

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test_start_on

Test Result Information

Result Type: Test Case Result

Parent: [Test Suite 1](#)

Start Time: 06-Jan-2025 18:06:48

End Time: 06-Jan-2025 18:06:50

Outcome: Passed

Test Case Information

Name: test_start_on

Type: Baseline Test

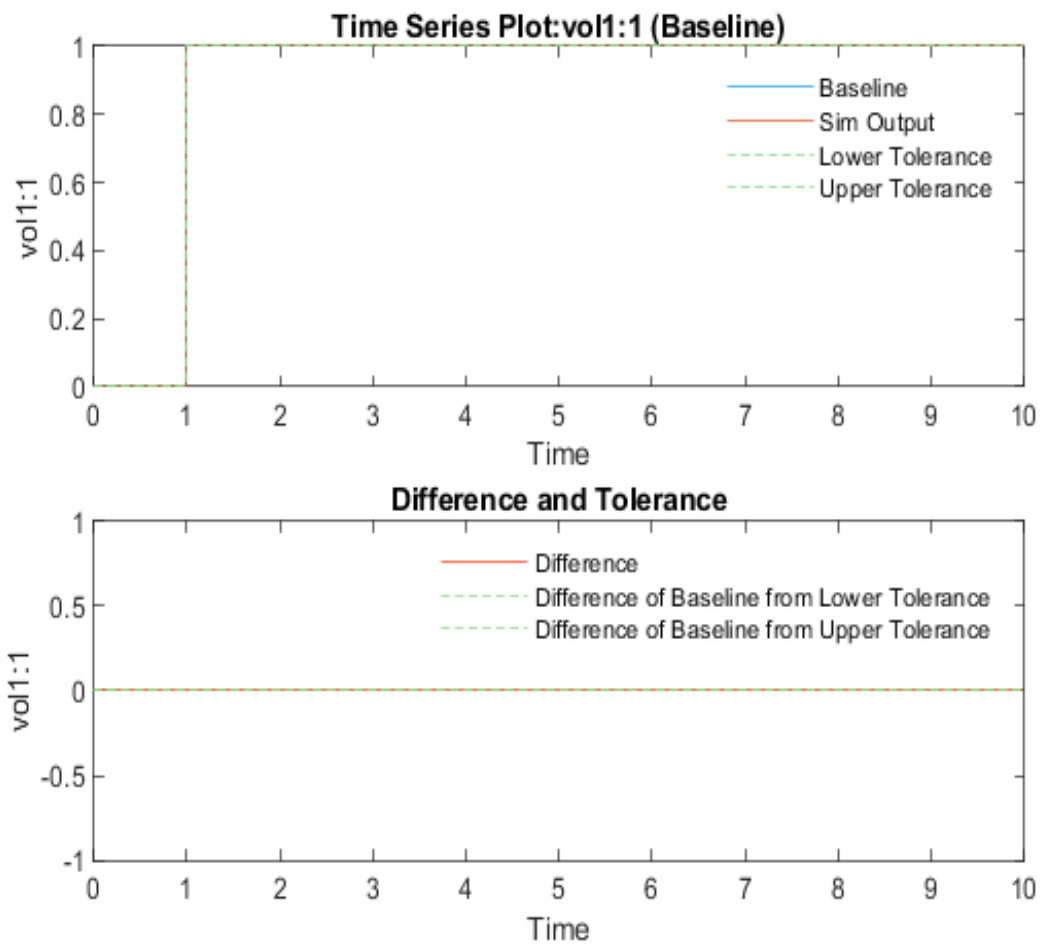
Baseline Name: baseline_test_on.mat

Baseline File: C:\Users\asus\Downloads\Simulink\test_start_on\b
aseline_test_on.mat

Baseline Comparison

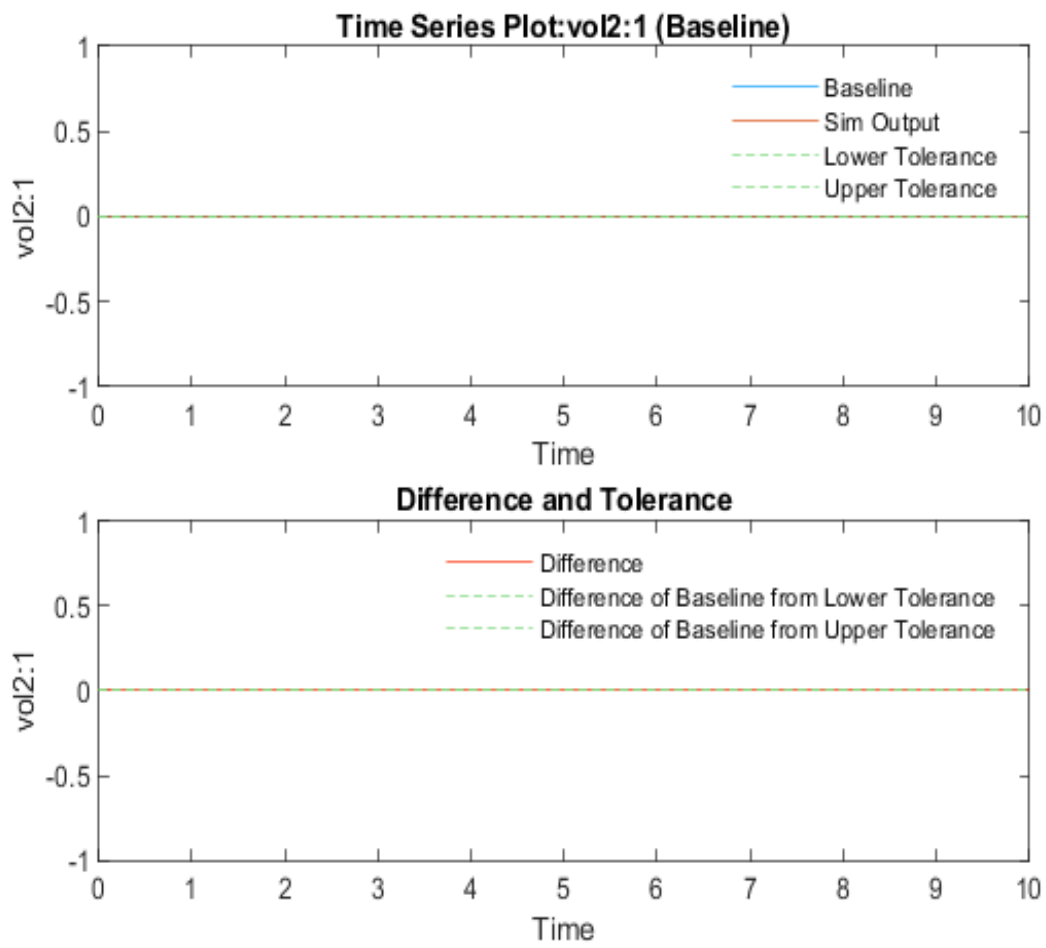
Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
✓ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✓ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✓ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✓ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✓ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



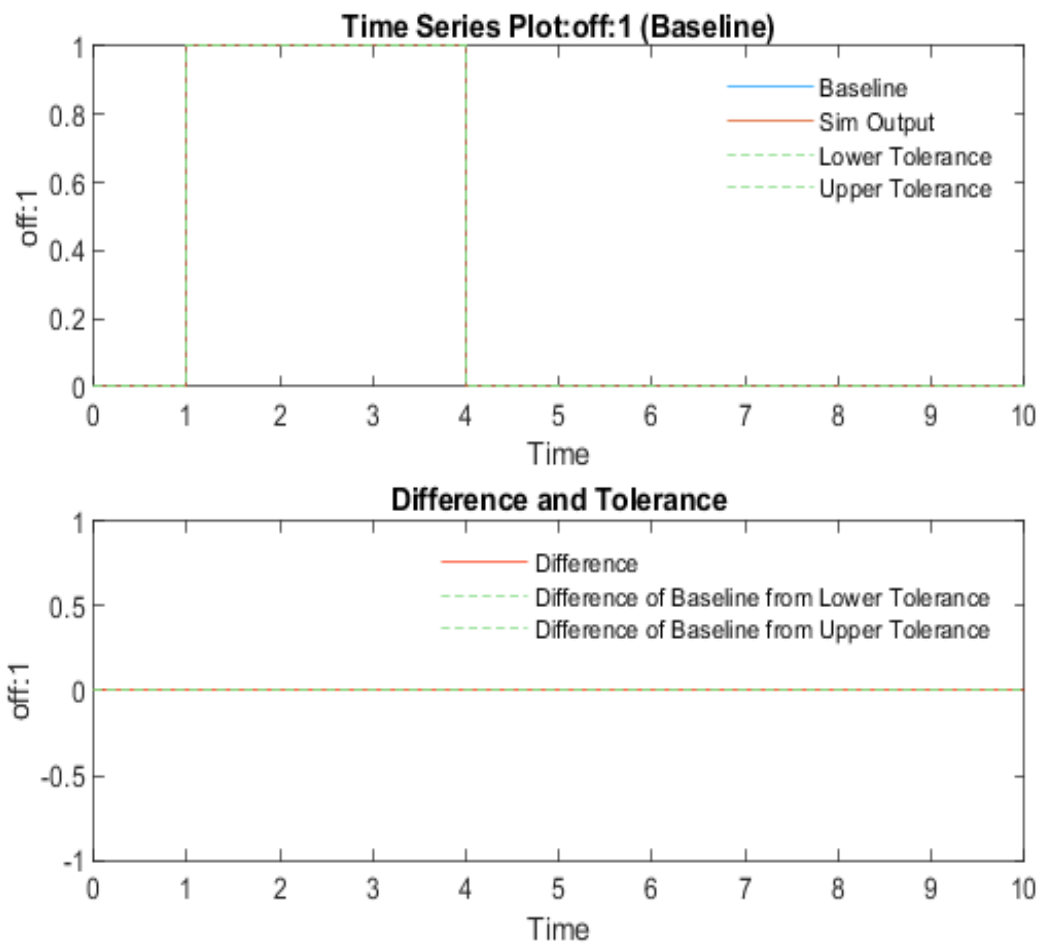
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



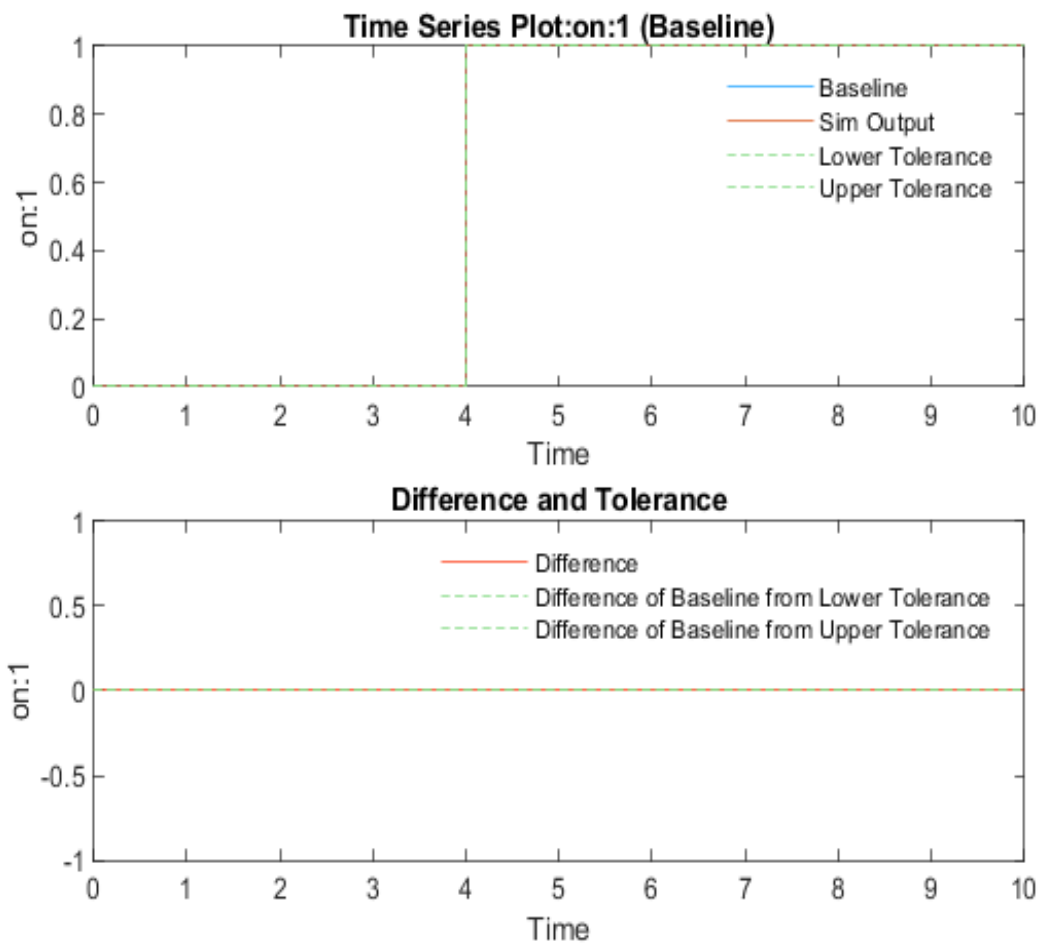
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



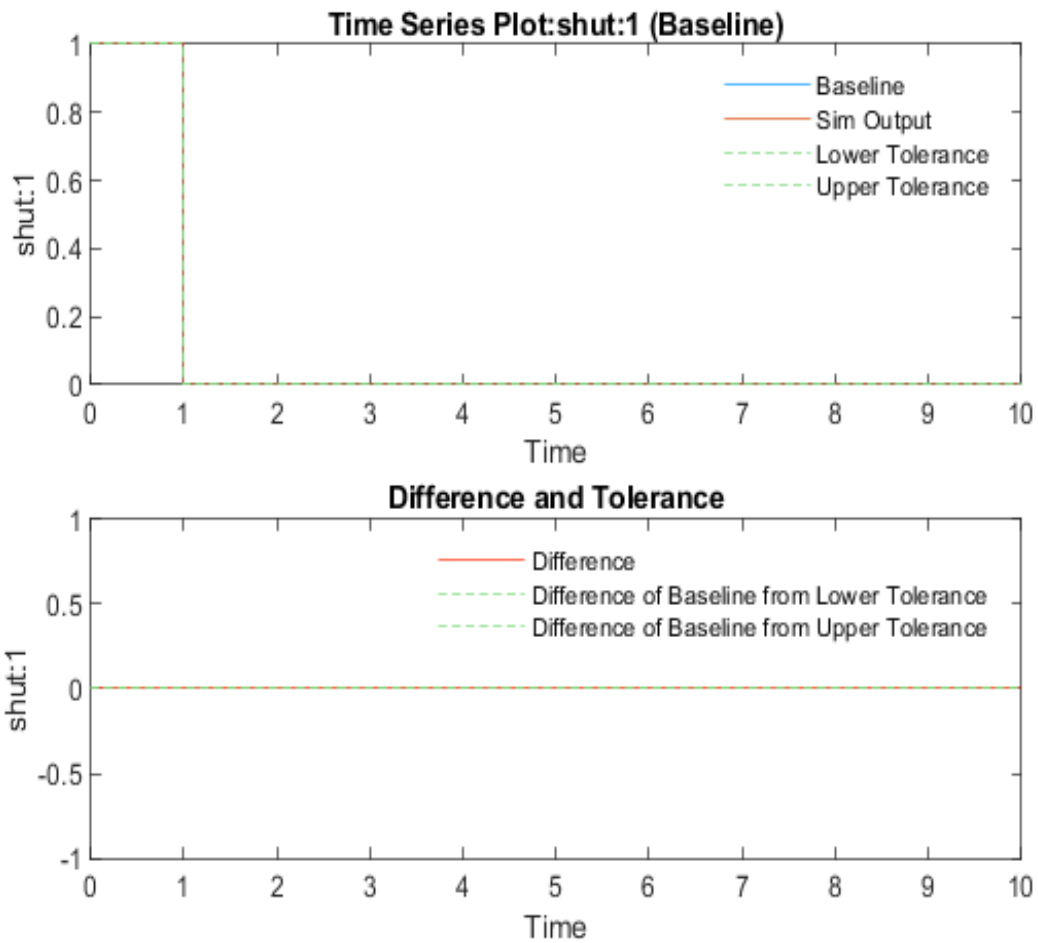
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Simulation

System Under Test Information

Model:	ex4_statecart
Harness:	ex4_statecart_Harness1
Harness Owner:	ex4_statecart/Chart1
Release:	Current
Simulation Mode:	normal

Override SIL or PIL Mode:	0
Configuration Set:	Configuration1
External Input Name:	test_on.mat
External Input File:	C:\Users\asus\Downloads\Simulink\test_start_on\test_on.mat
Start Time:	0
Stop Time:	10
Checksum:	1585101186 1071409664 3040648266 564266473
Simulink Version:	24.1
Model Version:	1.1
Model Author:	Mohammad Moein
Date:	Mon Jan 06 17:11:19 2025
User ID:	Mohammad Moein
Model Path:	C:\Users\asus\Downloads\Simulink\ex4_statecart.slx
Machine Name:	DESKTOP-7GPGEMD
Solver Name:	FixedStepDiscrete
Solver Type:	Fixed-Step
Fixed Step Size:	0.10000000000000001
Simulation Start Time:	2025-01-06 18:06:48
Simulation Stop Time:	2025-01-06 18:06:48
Platform:	PCWIN64

test_start_on_off_shut

Test Result Information






Result Type:	Test Case Result
Parent:	Test Suite 1
Start Time:	06-Jan-2025 18:06:50
End Time:	06-Jan-2025 18:06:51
Outcome:	Passed


Test Case Information

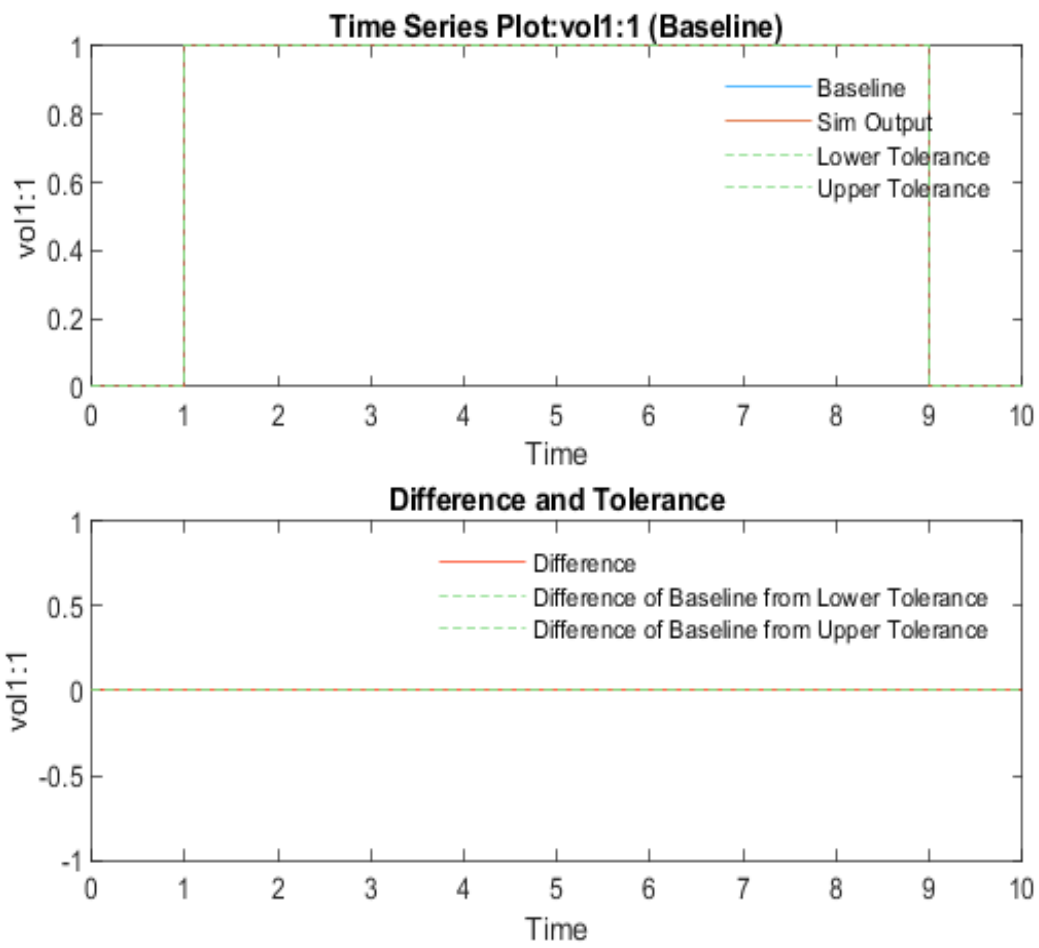
Name:	test_start_on_off_shut
Type:	Baseline Test

Baseline Name: baseline_test_on_off.mat
Baseline File: C:\Users\asus\Downloads\Simulink\test_on_off\baseline_test_on_off.mat

Baseline Comparison

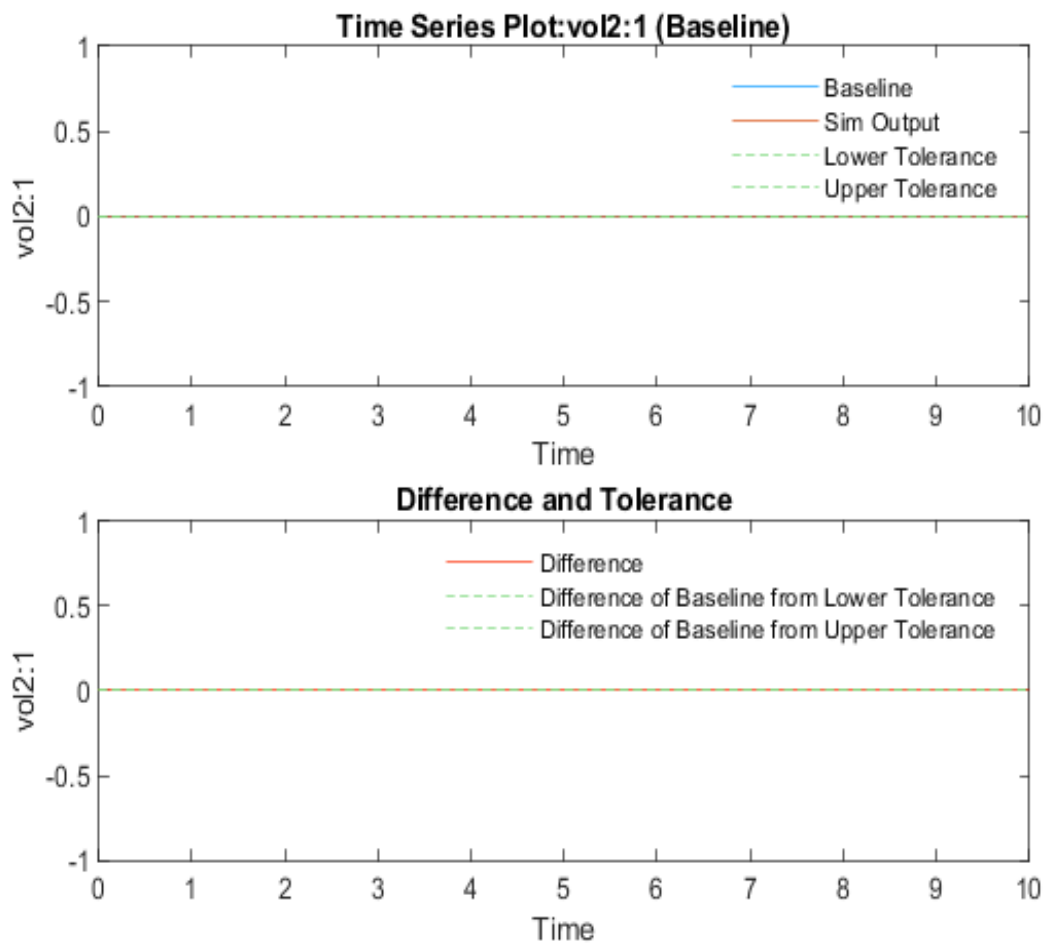
Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
 vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
 vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
 off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
 on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
 shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
 vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



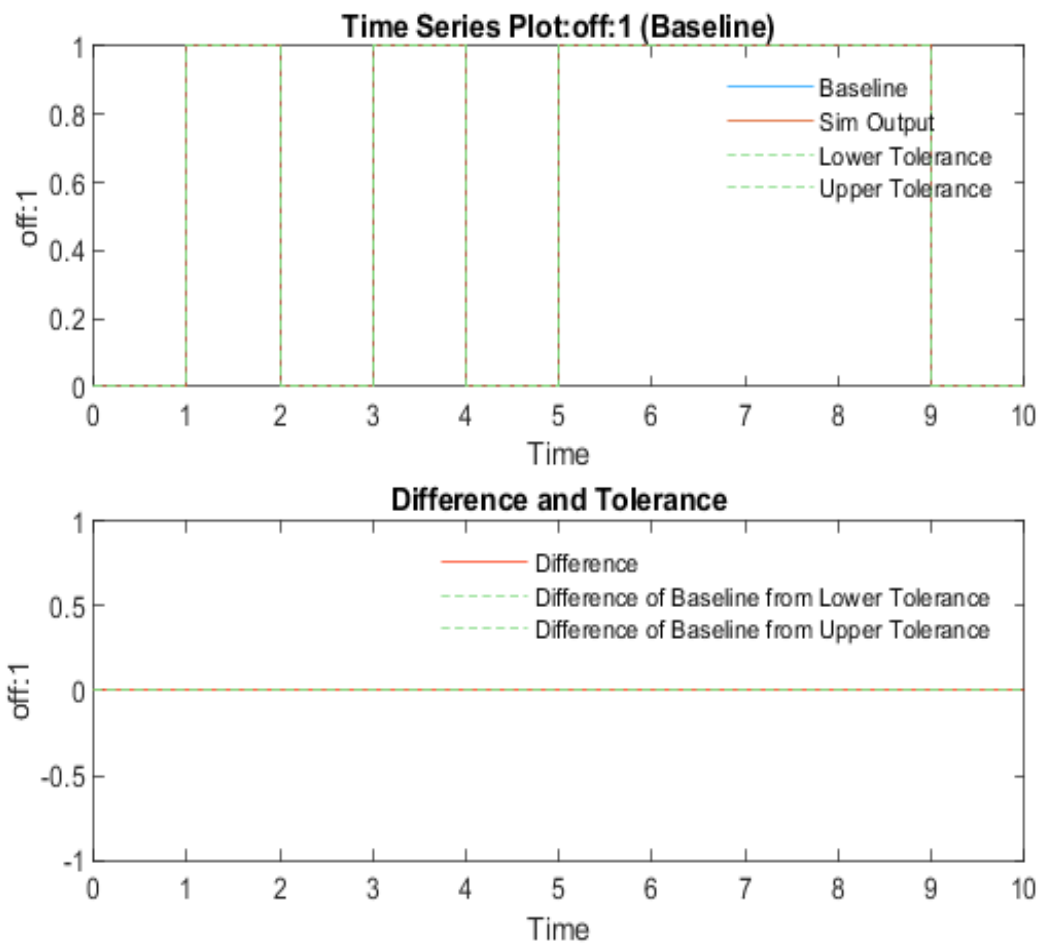
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



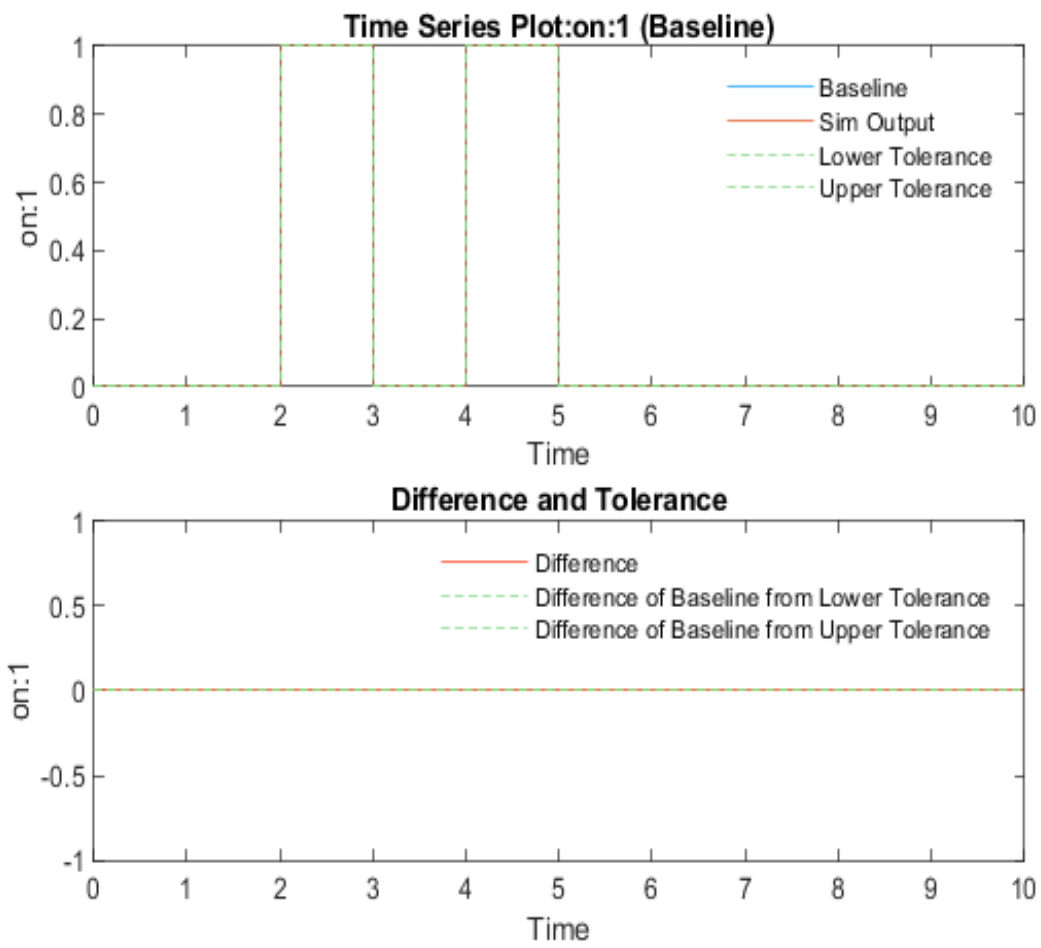
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



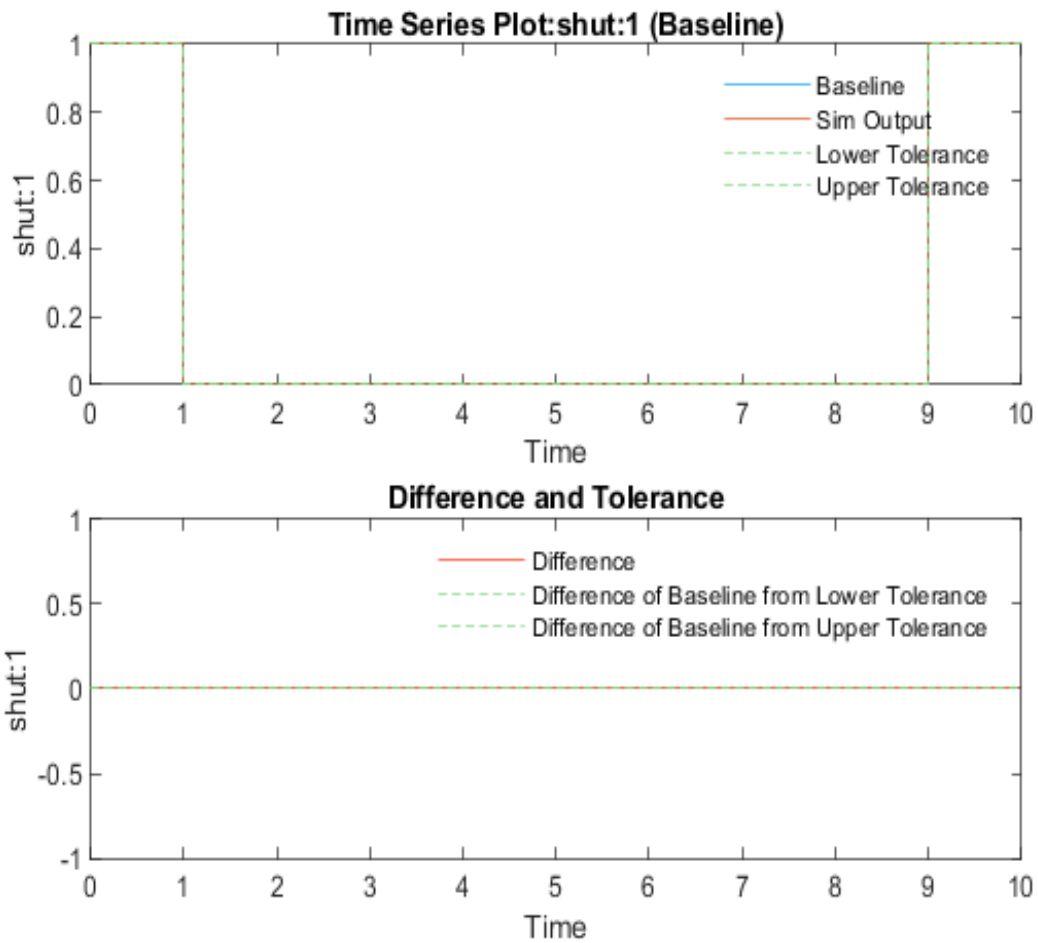
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Simulation

System Under Test Information

Model:	ex4_statecart
Harness:	ex4_statecart_Harness1
Harness Owner:	ex4_statecart/Chart1
Release:	Current
Simulation Mode:	normal

Override SIL or PIL Mode:	0
Configuration Set:	Configuration1
External Input Name:	test_on_off.mat
External Input File:	C:\Users\asus\Downloads\Simulink\test_on_off\test_on_off.mat
Start Time:	0
Stop Time:	10
Checksum:	1585101186 1071409664 3040648266 564266473
Simulink Version:	24.1
Model Version:	1.1
Model Author:	Mohammad Moein
Date:	Mon Jan 06 17:11:19 2025
User ID:	Mohammad Moein
Model Path:	C:\Users\asus\Downloads\Simulink\ex4_statecart.slx
Machine Name:	DESKTOP-7GPGEMD
Solver Name:	FixedStepDiscrete
Solver Type:	Fixed-Step
Fixed Step Size:	0.10000000000000001
Simulation Start Time:	2025-01-06 18:06:50
Simulation Stop Time:	2025-01-06 18:06:50
Platform:	PCWIN64

test_start_dec_inc_shut

Test Result Information

Result Type:	Test Case Result
Parent:	Test Suite 1
Start Time:	06-Jan-2025 18:06:52
End Time:	06-Jan-2025 18:06:53
Outcome:	Passed

Test Case Information

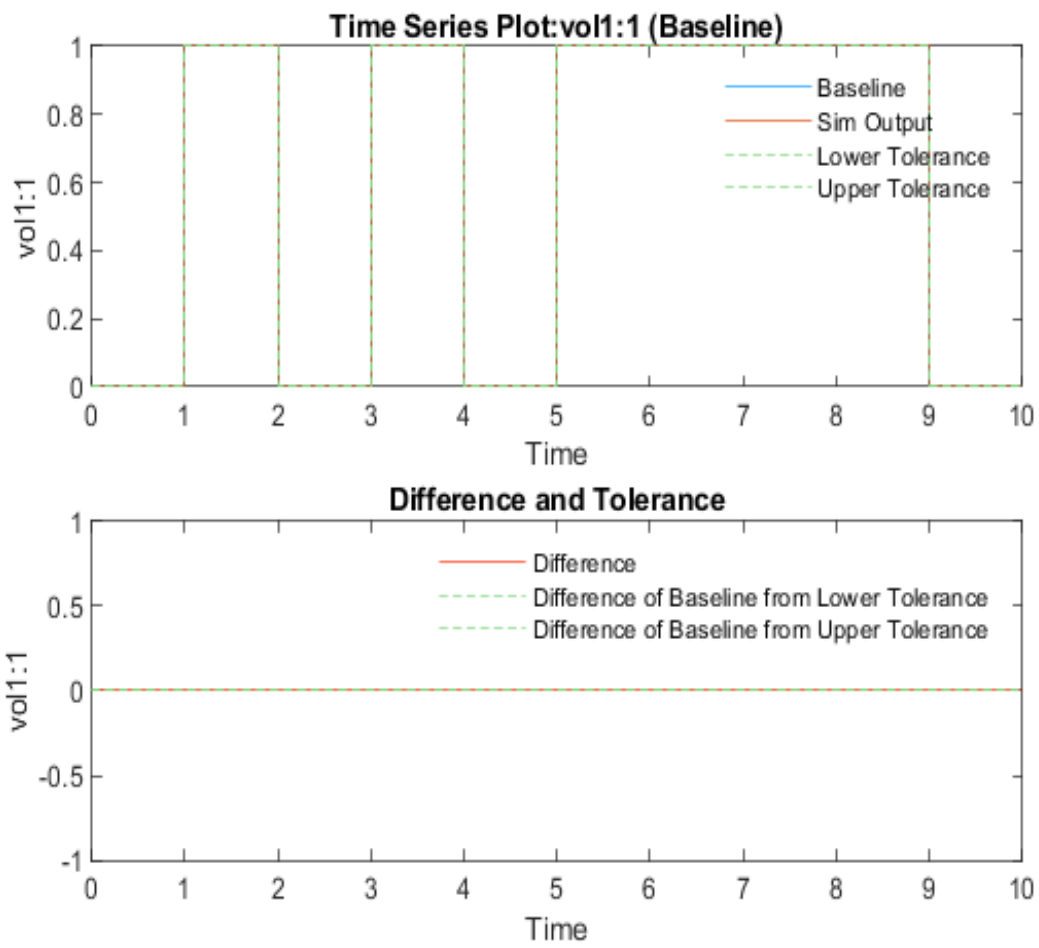
Name:	test_start_dec_inc_shut
Type:	Baseline Test

Baseline Name: baseline_test_inc_dec.mat
Baseline File: C:\Users\asus\Downloads\Simulink\test_inc_dec\b
aseline_test_inc_dec.mat

Baseline Comparison

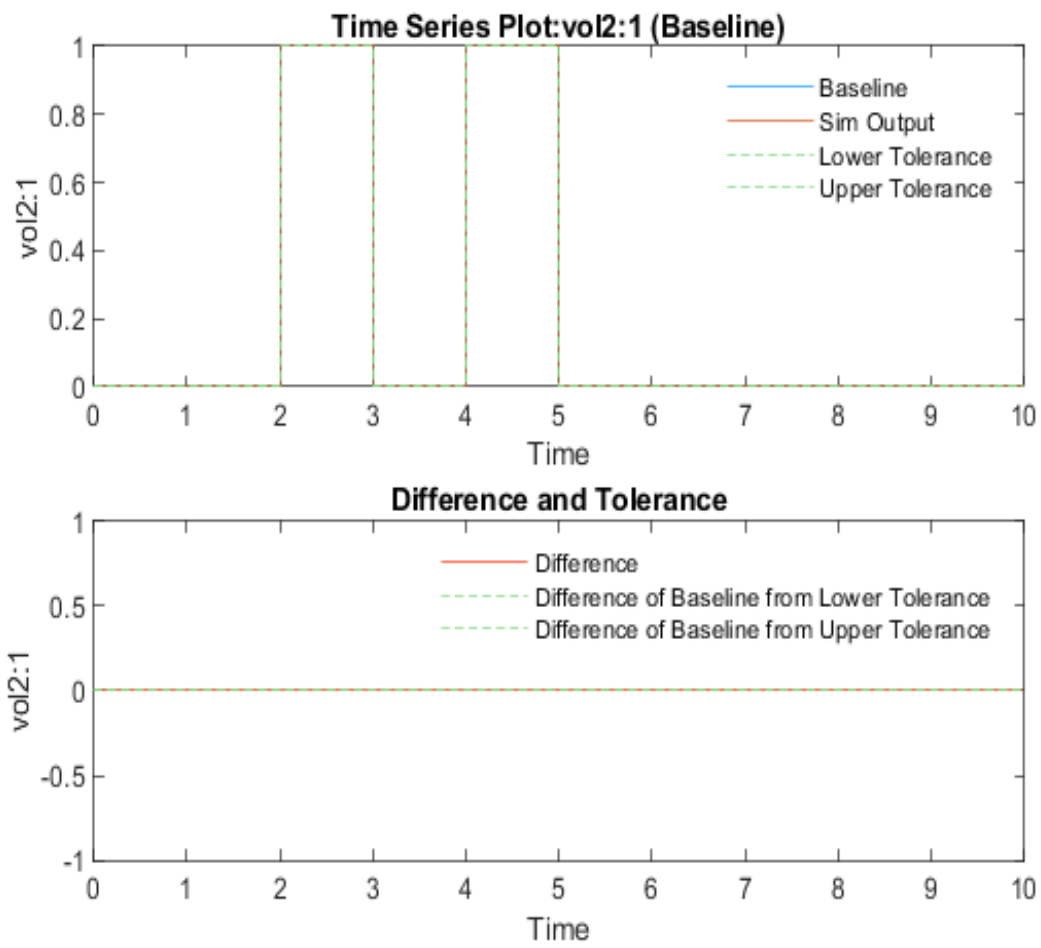
Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
✔ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



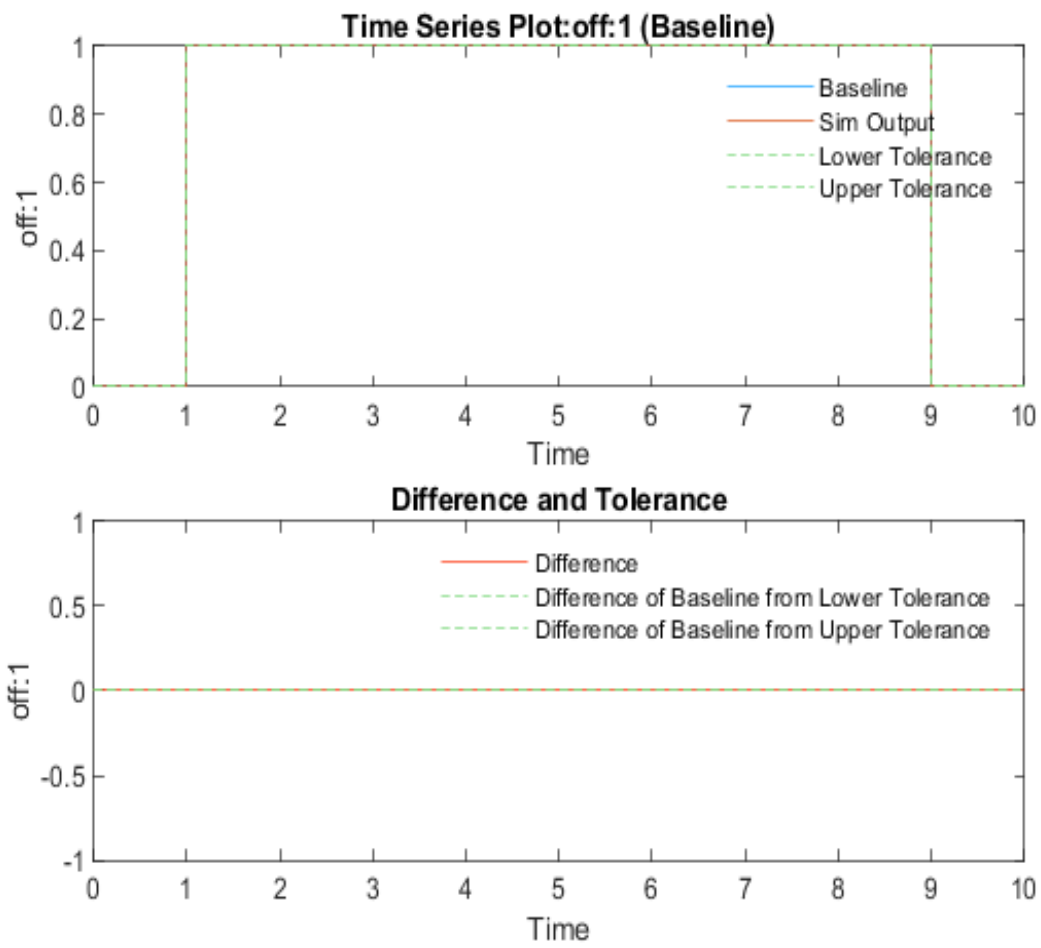
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



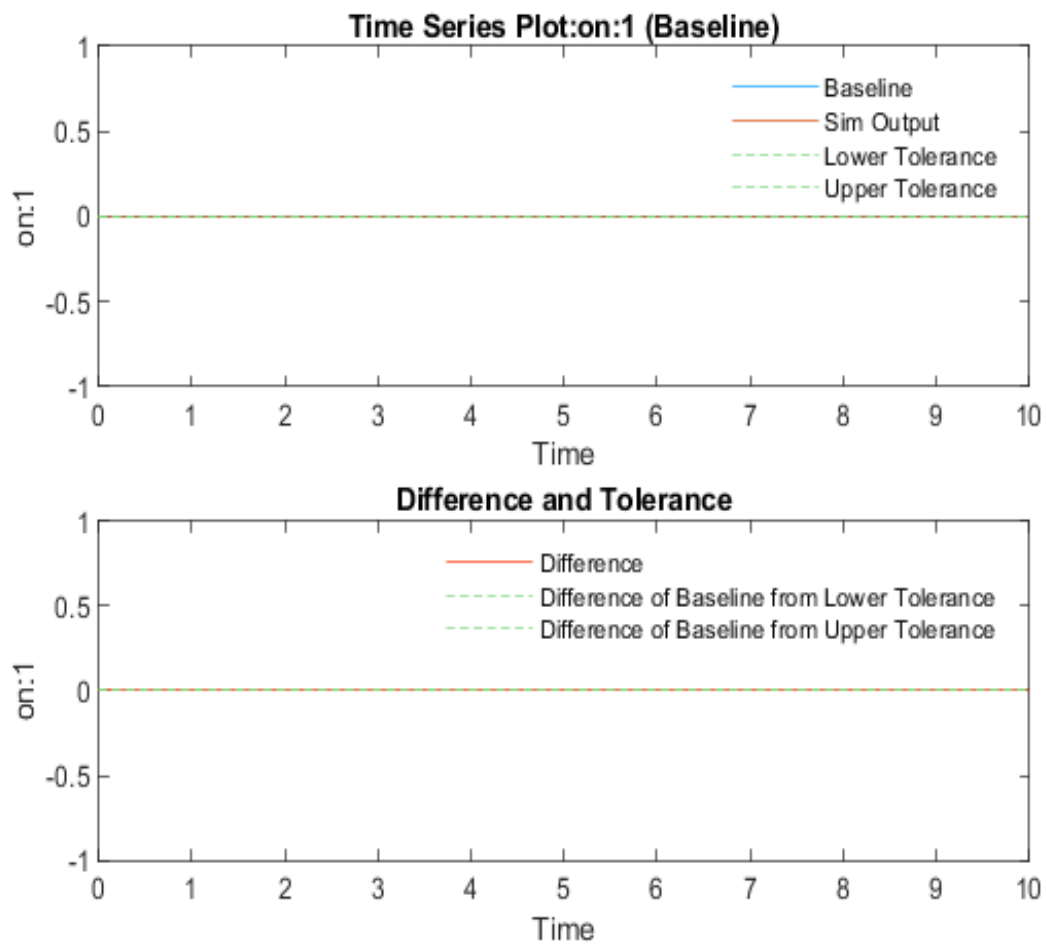
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



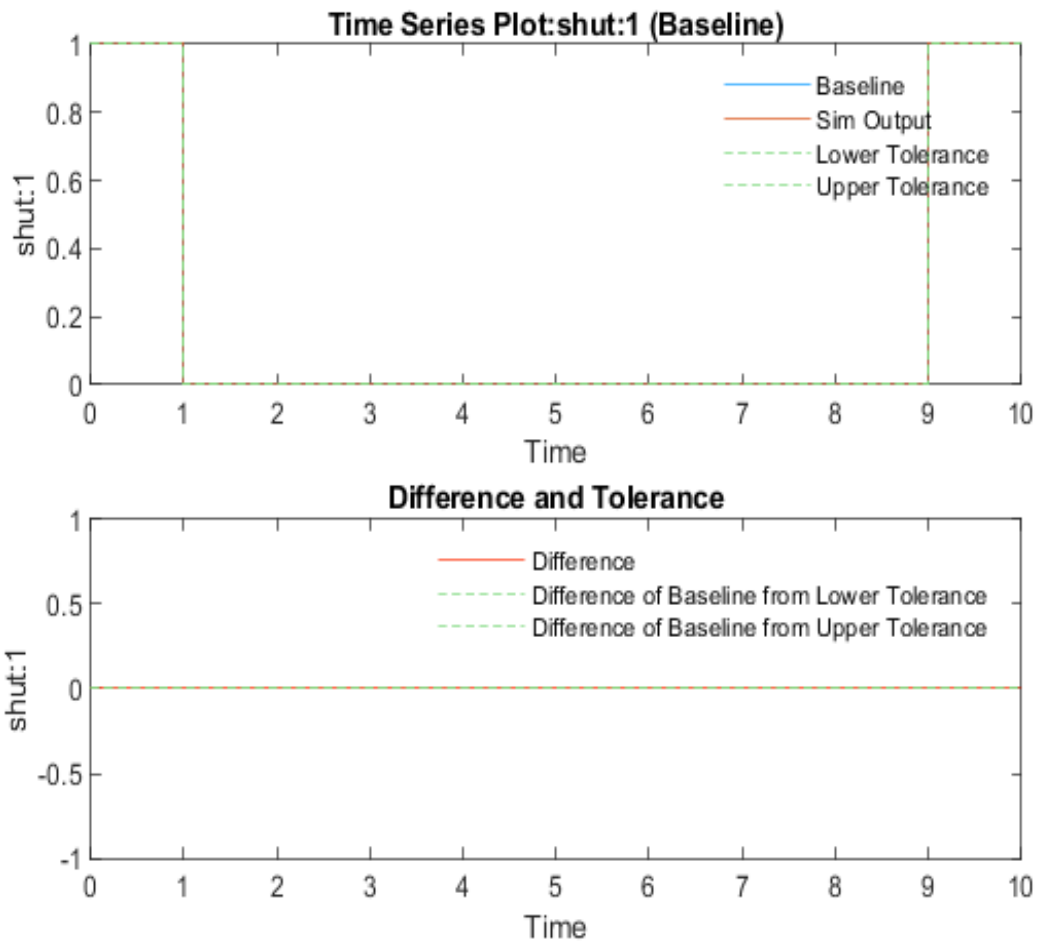
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Simulation

System Under Test Information

Model:	ex4_statecart
Harness:	ex4_statecart_Harness1
Harness Owner:	ex4_statecart/Chart1
Release:	Current
Simulation Mode:	normal

Override SIL or PIL Mode:	0
Configuration Set:	Configuration1
External Input Name:	test_inc_dec.mat
External Input File:	C:\Users\asus\Downloads\Simulink\test_inc_dec\test_inc_dec.mat
Start Time:	0
Stop Time:	10
Checksum:	1585101186 1071409664 3040648266 564266473
Simulink Version:	24.1
Model Version:	1.1
Model Author:	Mohammad Moein
Date:	Mon Jan 06 17:11:19 2025
User ID:	Mohammad Moein
Model Path:	C:\Users\asus\Downloads\Simulink\ex4_statecart.slx
Machine Name:	DESKTOP-7GPGEMD
Solver Name:	FixedStepDiscrete
Solver Type:	Fixed-Step
Fixed Step Size:	0.10000000000000001
Simulation Start Time:	2025-01-06 18:06:52
Simulation Stop Time:	2025-01-06 18:06:52
Platform:	PCWIN64

test_on

Test Result Information

Result Type:	Test Case Result
Parent:	Test Suite 1
Start Time:	06-Jan-2025 18:06:53
End Time:	06-Jan-2025 18:06:55
Outcome:	Passed

Test Case Information

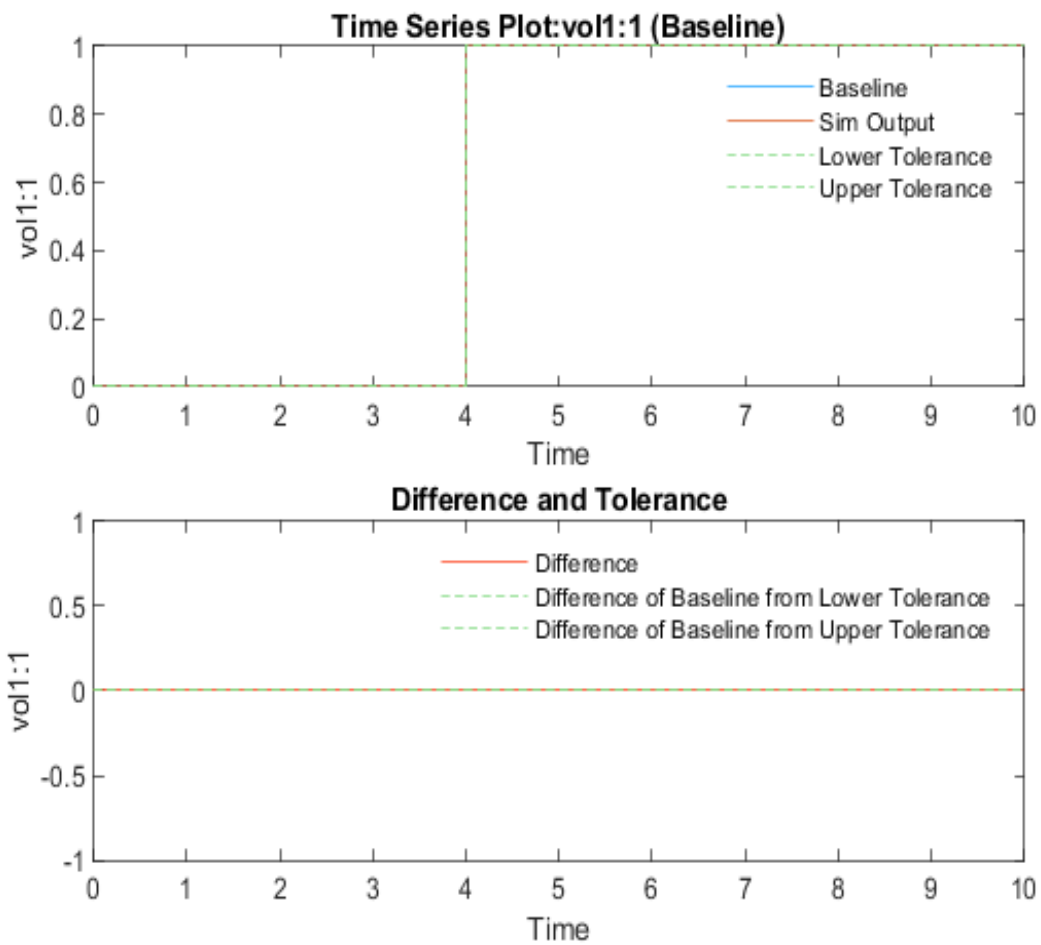
Name:	test_on
Type:	Baseline Test

Baseline Name: baseline_test_on.mat
Baseline File: C:\Users\asus\Downloads\Simulink\test_on\baseline_test_on.mat

Baseline Comparison

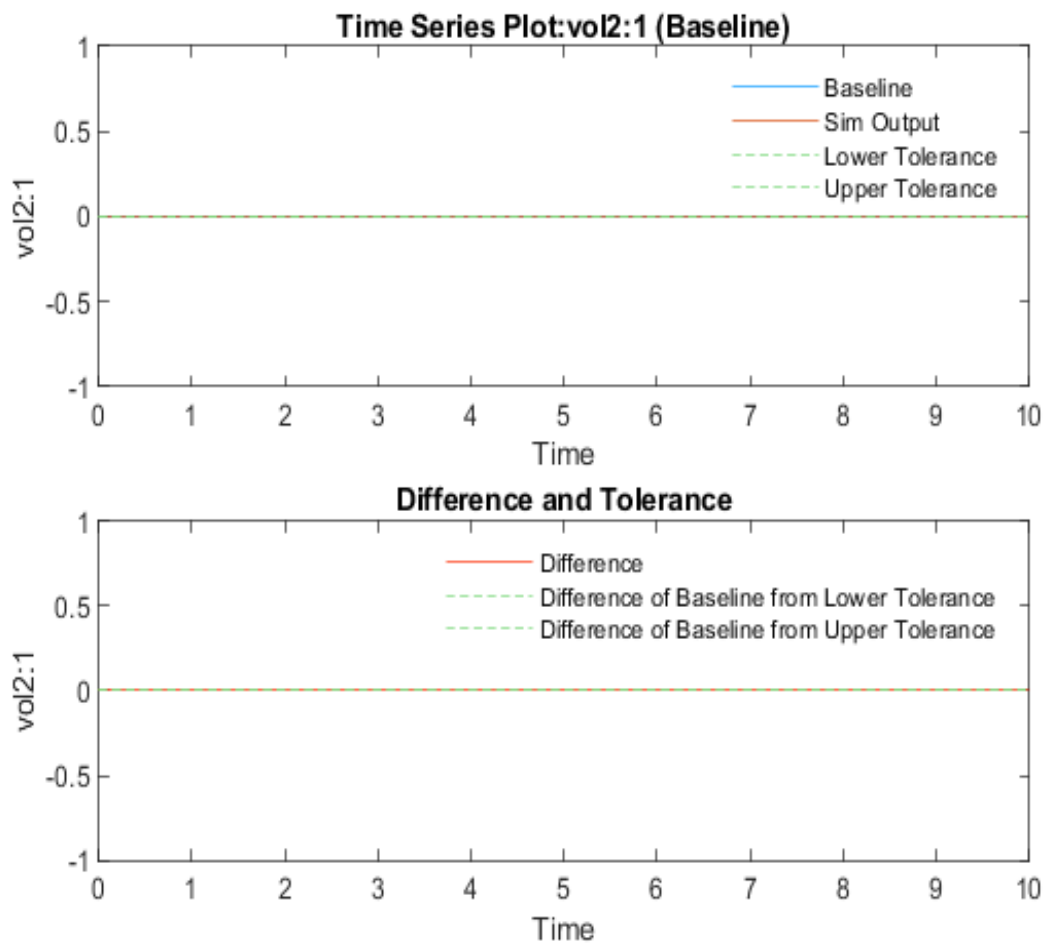
Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
✔ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



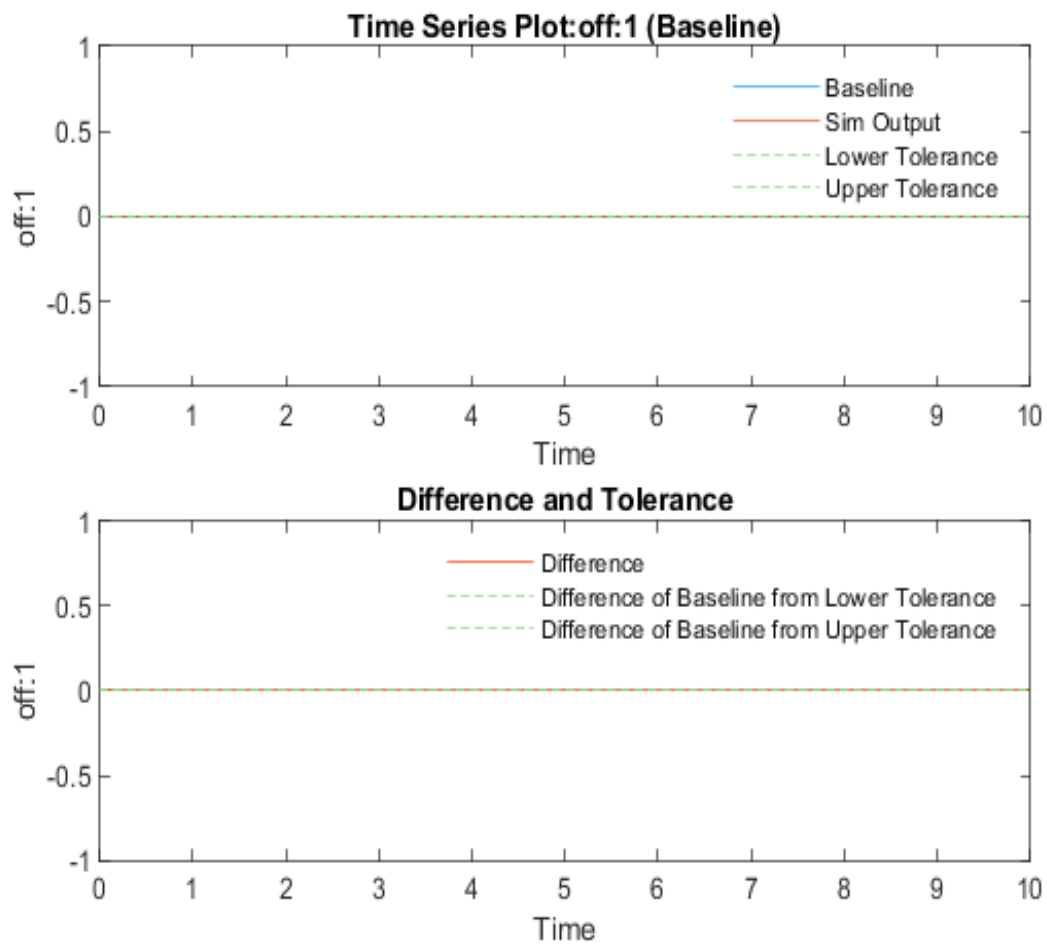
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



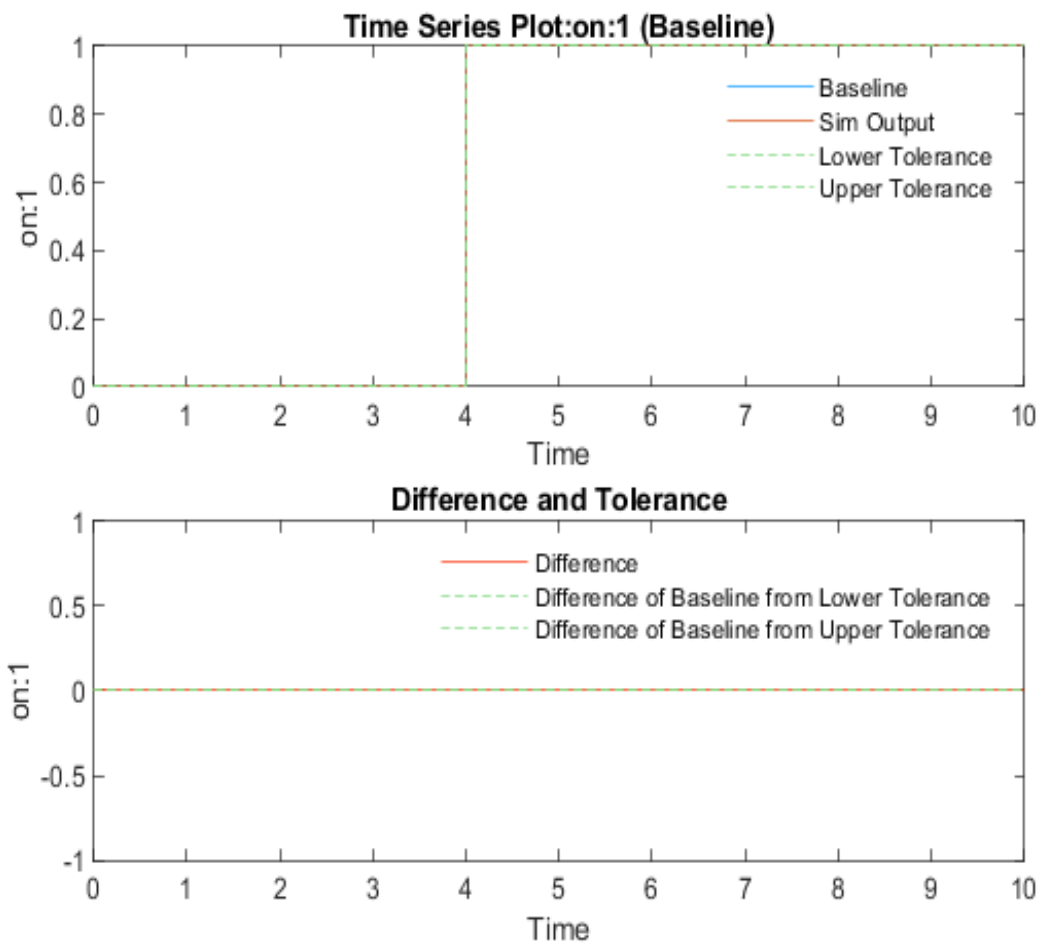
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



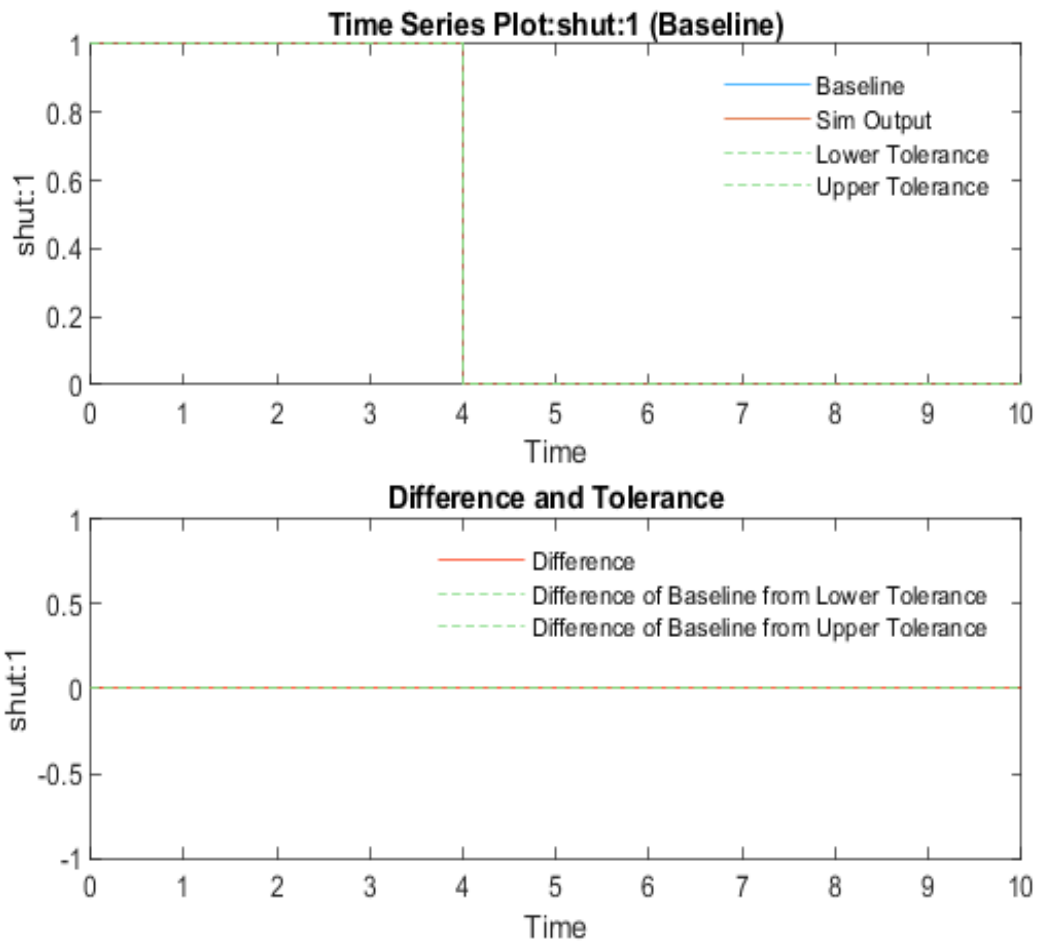
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Simulation

System Under Test Information

Model:	ex4_statecart
Harness:	ex4_statecart_Harness1
Harness Owner:	ex4_statecart/Chart1
Release:	Current
Simulation Mode:	normal

Override SIL or PIL Mode:	0
Configuration Set:	Configuration1
External Input Name:	test_on.mat
External Input File:	C:\Users\asus\Downloads\Simulink\test_on\test_on.mat
Start Time:	0
Stop Time:	10
Checksum:	1585101186 1071409664 3040648266 564266473
Simulink Version:	24.1
Model Version:	1.1
Model Author:	Mohammad Moein
Date:	Mon Jan 06 17:11:19 2025
User ID:	Mohammad Moein
Model Path:	C:\Users\asus\Downloads\Simulink\ex4_statecart.slx
Machine Name:	DESKTOP-7GPGEMD
Solver Name:	FixedStepDiscrete
Solver Type:	Fixed-Step
Fixed Step Size:	0.10000000000000001
Simulation Start Time:	2025-01-06 18:06:53
Simulation Stop Time:	2025-01-06 18:06:54
Platform:	PCWIN64

test_start_on_inc_shut

Test Result Information

Result Type:	Test Case Result
Parent:	Test Suite 1
Start Time:	06-Jan-2025 18:06:55
End Time:	06-Jan-2025 18:06:57
Outcome:	Passed

Test Case Information

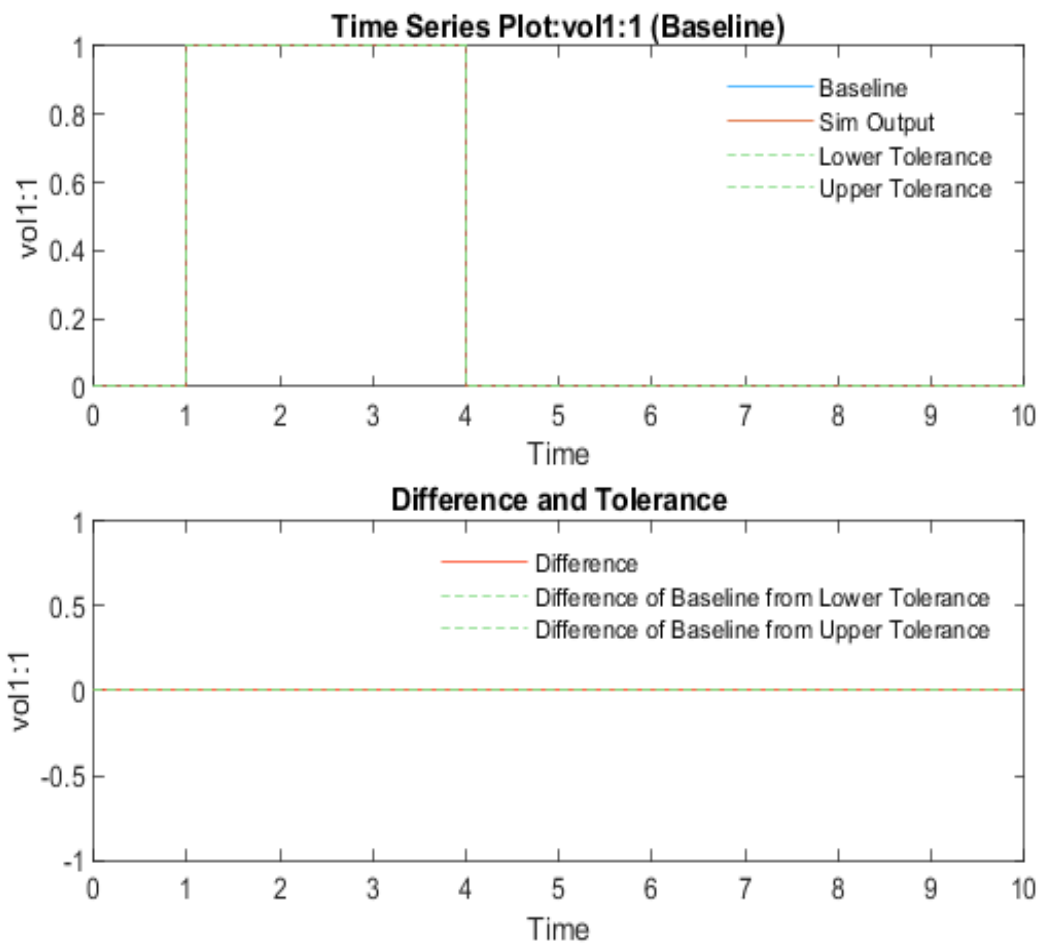
Name:	test_start_on_inc_shut
Type:	Baseline Test

Baseline Name: baseline_test_start_on_dec.mat
Baseline File: C:\Users\asus\Downloads\Simulink\test_start_on_in\baseline_test_start_on_dec.mat

Baseline Comparison

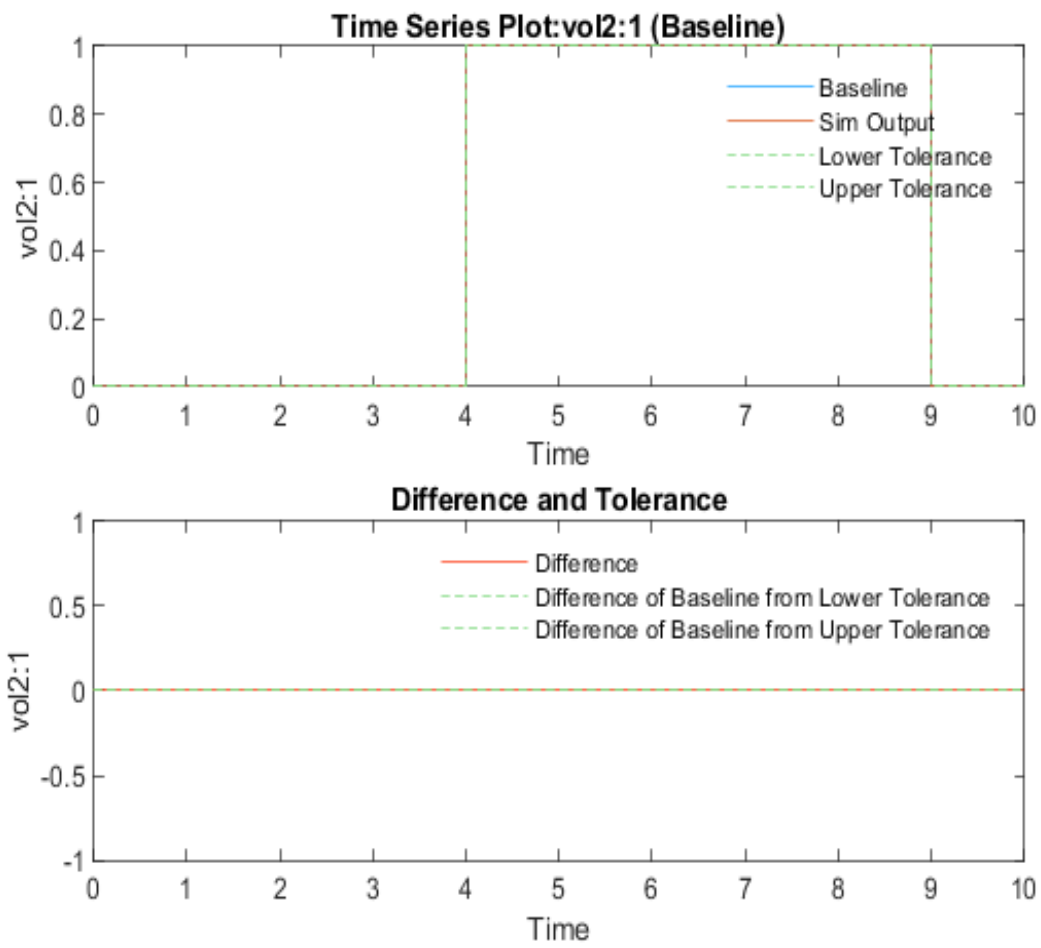
Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
✔ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link
✔ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✔ vol1:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



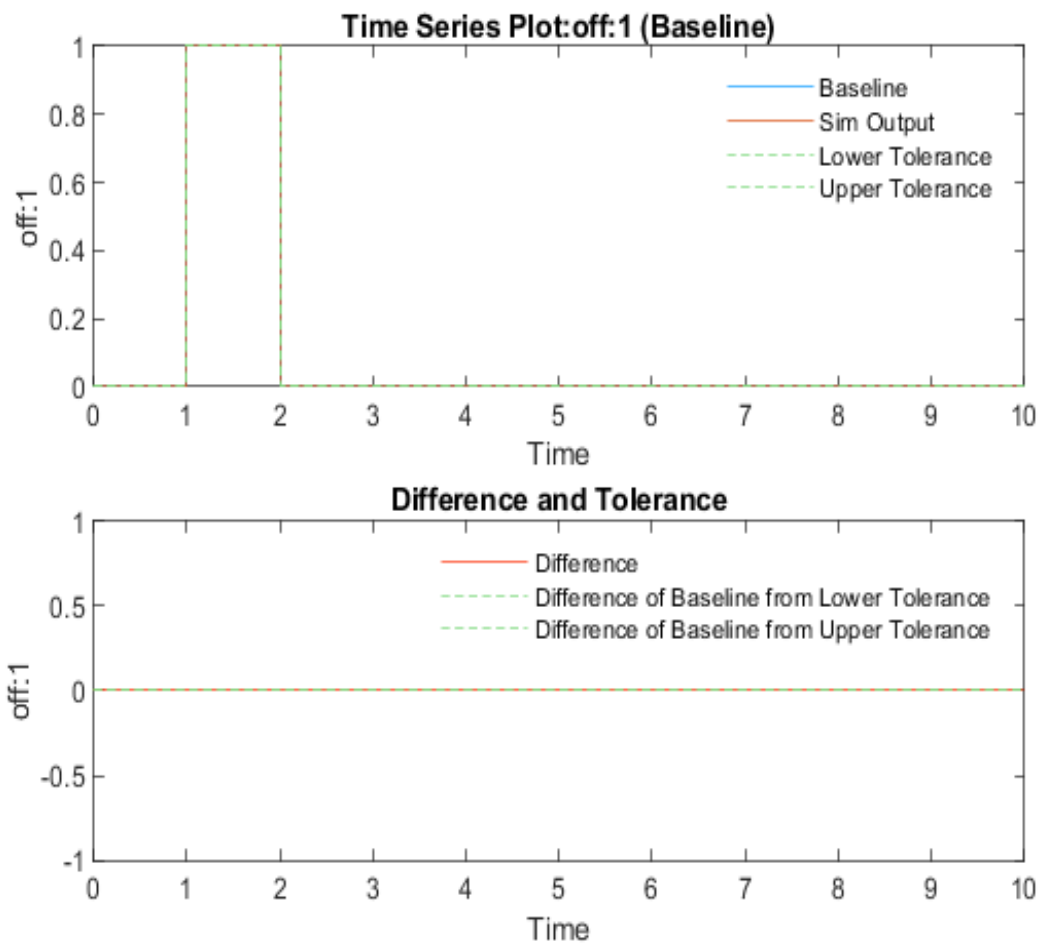
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ vol2:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



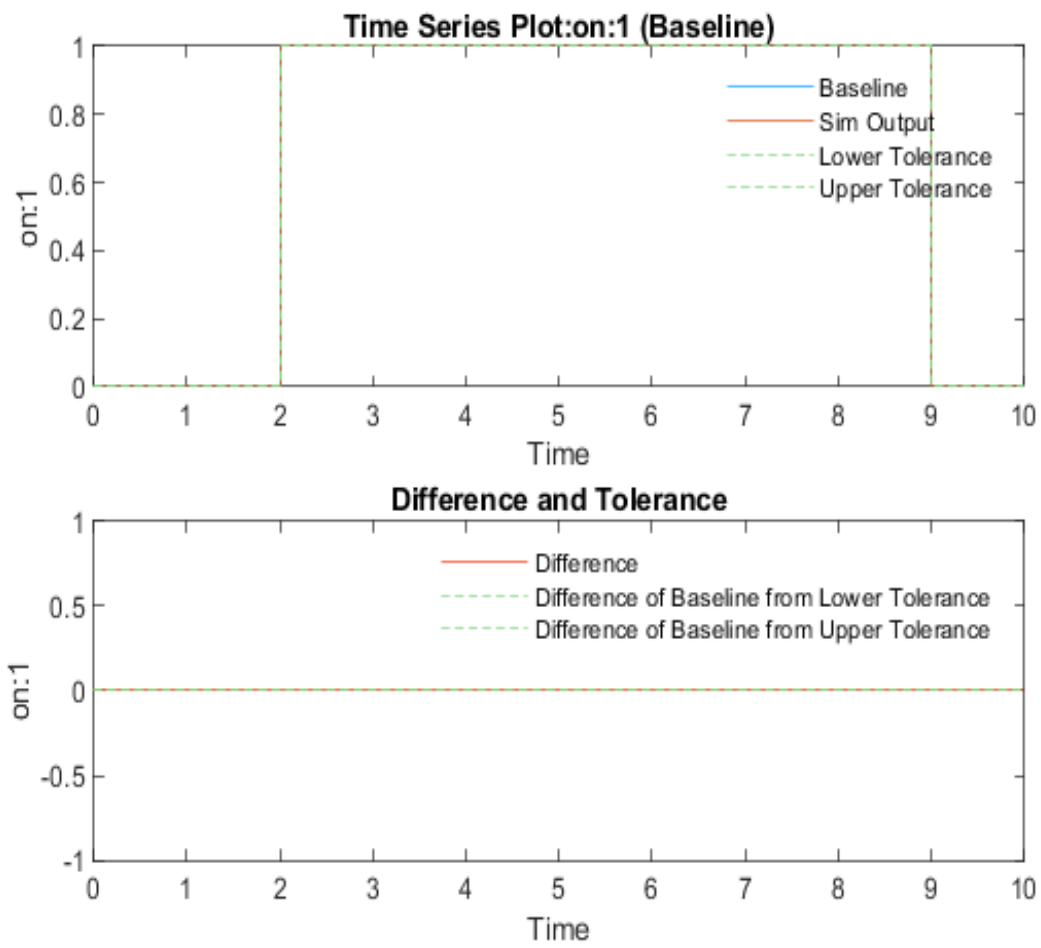
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ off:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



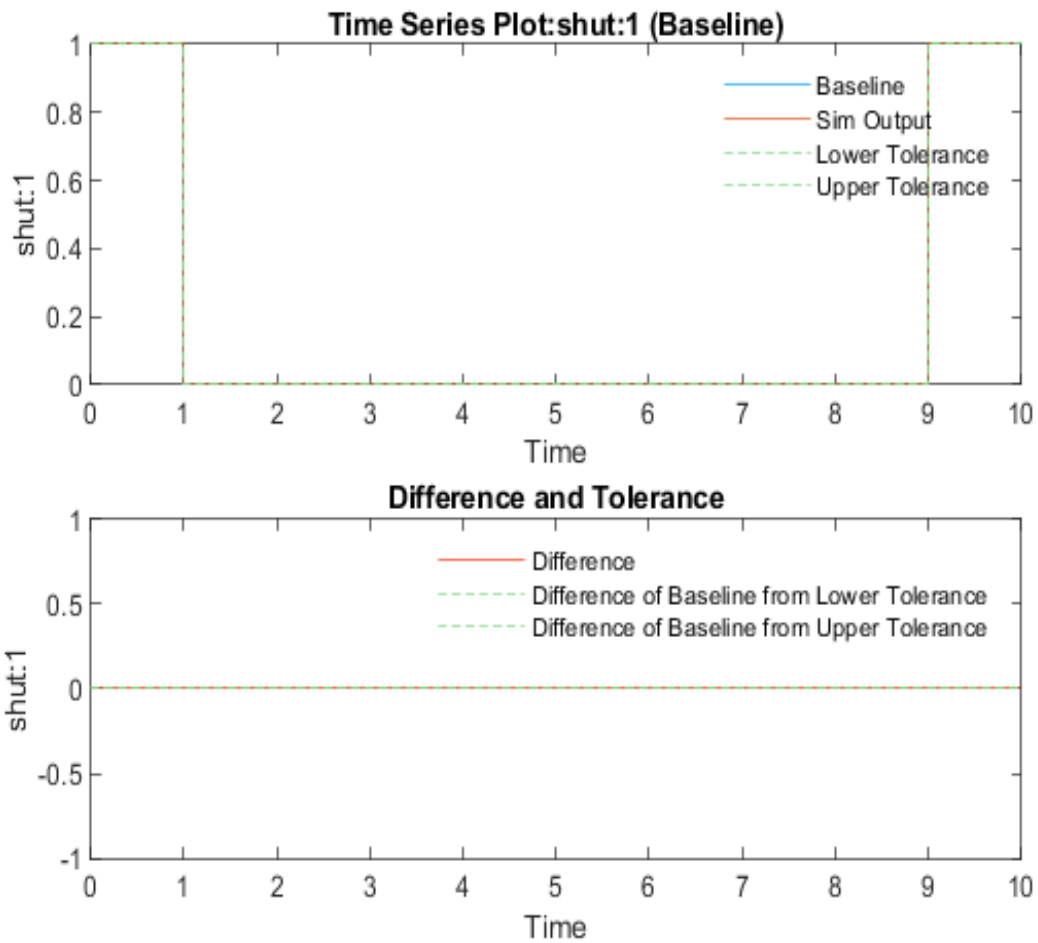
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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ on:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ shut:1	0	0	0	0	0	double		0.1	double		0.1	zoh	union



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Simulation

System Under Test Information

Model:	ex4_statecart
Harness:	ex4_statecart_Harness1
Harness Owner:	ex4_statecart/Chart1
Release:	Current
Simulation Mode:	normal

Override SIL or PIL Mode:	0
Configuration Set:	Configuration1
External Input Name:	test_start_on_inc.mat
External Input File:	C:\Users\asus\Downloads\Simulink\test_start_on_i nc\test_start_on_inc.mat
Start Time:	0
Stop Time:	10
Checksum:	1585101186 1071409664 3040648266 564266473
Simulink Version:	24.1
Model Version:	1.1
Model Author:	Mohammad Moein
Date:	Mon Jan 06 17:11:19 2025
User ID:	Mohammad Moein
Model Path:	C:\Users\asus\Downloads\Simulink\ex4_statecart. slx
Machine Name:	DESKTOP-7GPGEMD
Solver Name:	FixedStepDiscrete
Solver Type:	Fixed-Step
Fixed Step Size:	0.100000000000000001
Simulation Start Time:	2025-01-06 18:06:55
Simulation Stop Time:	2025-01-06 18:06:56
Platform:	PCWIN64