

Overview and Analysis of GPU Acceleration for Regular Expressions *

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25. september 2023

Abstract

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1 Introduction

Pattern matching is widely used in a variety of different domains. Regular expressions have become a prevalent tool for text processing and sanitation due to their flexibility, conciseness, and vast support in most programming languages [3]. They appear in approximately a third of open-source projects [4]. They are employed in technical fields, ranging from database querying [8], texts editors¹, and web scraping (the process of extracting data or information from

*Semestrálny projekt v predmete Metódy inžinierskej práce, ak. rok 2023/24, vedenie: MSc. Mirwais Ahmadzai

¹<https://neovim.io/doc/user/change.html#%3Asubstitute>

internet sites) [7] to network security, such as deep packet inspection [2], and bioinformatics [13], among others.

Regular expressions are implemented using finite automata, in either deterministic (DFA) or non-deterministic (NFA) form, each with their respective advantages and drawbacks. Each of them has their own advantages and disadvantages [12, 19, 20].

In many applications, regular expressions are applied to large amounts of input data, or require a fast response, or both. It stands to reason that efficiency in both memory and speed is the key to optimal use [16]. Here, the question is how to achieve the greatest possible efficiency for a given problem that is addressed by the regular expressions.

The processor's capacity to execute multiple expressions simultaneously is notably restricted, even in the current era of multicore processors [9]. However, its frequency and cache memory speed prove excellent for handling small datasets. For tasks that require more extensive parallelism, FPGAs (Field-Programmable Gate Arrays) or ASICs (Application-Specific Integrated Circuits) have been used. The problem is that they are slow to configure [17] and inflexible to change [6, 11].

In recent years, GPUs with their extensive parallelism, computational capabilities, and high memory bandwidth have become prevalent in numerous computing system. They have scaled at a faster rate than CPUs, providing significant computing power [11, 14]. APIs were created to allow General Purpose Graphics Processing Units (GPGPU) to accelerate processing in supported applications, replacing shading languages and simplifying their use for programmers. Two popular APIs are Compute Unified Device Architecture (CUDA) and Open Computing Language (OpenCL) [5].

In this paper, we investigate a variety of GPU-based regular expression execution methods and conduct a comparative analysis of their strengths and weaknesses. Our research begins with a thorough examination of regular expressions in 2. This is followed by a comparison of their representations of finite state automata forms in 2.1. We then move into parallel computing platforms, such as CUDA and OpenCL in 2.2.1.

By combining these findings, our investigation aims to provide a comprehensive overview of GPU-accelerated regular expressions, utilizing previous studies to provide an in-depth comparative analysis in section 3 and 5. Result from this was evaluated in the 6 and 7 sections. Finally, we conclude with a summary of our findings in section 8.

2 Background

A regular expression, or regex for short, represents a set of exactly matching strings of characters and special symbols. This set can be infinite. The string of characters is then matched against the pattern to see if it matches. Regular expressions can be constructed in several ways [15]. The most common is to use a formal language, such as the one in POSIX standard². Basic syntax is described as follows: characters of alphabet are matched literally, special symbols are used to match a single/multiple character matches, optional character matches,

²https://pubs.opengroup.org/onlinepubs/9699919799/basedefs/V1_chap09.html

alternation, any character, line start/end and the empty string. As described in table 1.

Symbols	Meaning
.	Any character
*	Zero or more matches
+	One or more matches
?	Zero or one match
	Alternation
-	Range
\	Escape character
^	Line start
\$	Line end
:	Grouping
[]	Start and end of character class
()	Start and end of group
{ }	Start and end of quantifier

Table 1: Regular expression special symbols, author's own work

pozriet ci uz neni dakde

2.1 Finite Automata

Regular expression matching is performed by using finite automata, a mathematical model of computation that abstracts computations into a finite number of states and transitions [19]. A finite automaton comprises a directed graph in which each node symbolises a state while each edge reflects a state transition. Two widely used representations of finite automata are deterministic finite automata (DFAs) and non-deterministic finite automata (NFAs). While NFAs and DFAs achieve the same outcome, there are some practical differences in terms of resource requirements and traversal behaviour [12]. Although deterministic finite automata (DFAs) have a simpler transition system, their execution is serial and the size of transitions in DFAs may be significantly larger than their equivalent NFAs [10, 11].

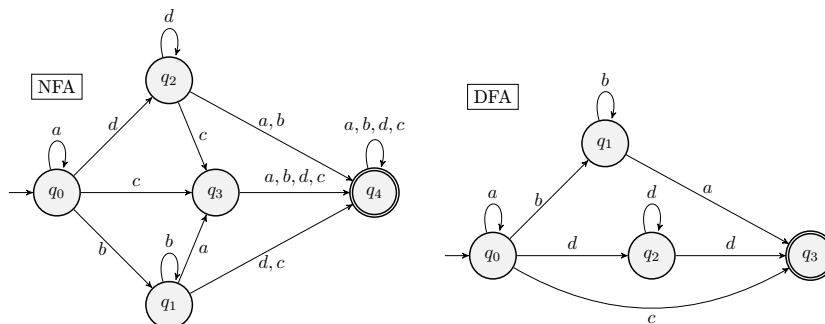


Figure 1: NFA and DFA for regular expression $a^*(b+a|d^*c)$, autor's own work

2.1.1 Automata processing

The regular expression's matching process is equivalent to a finite state machine traversal of the input stream. The process of matching begins with the activation of the initial states. Symbols from the input stream are sequentially utilized by the finite automaton. The process concludes once all the symbols of the input stream are processed. The incoming symbol is matched with the active states, and if it falls within the matchset of an active state, the active state transforms into a matched state [11].

We can guarantee worst-case performance by restricting the processing of each input character. Techniques to limit per-character processing involve enlarging the finite automaton. Therefore, the search space is determined by balancing the size of the automaton and the upper limit of per-character processing [12].

2.2 Graphics Processing Unit

The GPU, or Graphics Processing Unit, offers significantly higher instruction throughput and memory bandwidth than the CPU at a comparable price and power consumption. While the CPU is optimized for rapid execution of a sequence of operations referred to as a "thread" and can execute dozens of these threads concurrently, the GPU is optimized for thousands of parallel executions (offsetting the slower single-thread performance for increased throughput). This variation in capabilities is a result of differing design objectives for the GPU and CPU.³

All threads within a compute unit share a common instruction counter. Execution of a single compute unit occurs in lock-step, whereby each thread executes the same instruction when directed to do so. When control flow divergence occurs between threads within the same work group, divergent instructions are serialised, which can negatively impact performance. Similarly, an unbalanced workload across threads in a work-group will result in idle threads, which reduces performance [18].

The memory hierarchy of GPUs comprises: Global memory, a larger and slower form of memory accessible by all threads in any compute units. Constant memory, a read-only section of the global memory with a specific cache for faster memory access. Local memory, connected to compute units and shared among threads in a single unit and private memory, exclusively reserved for individual threads [18].

2.2.1 Parallel computing platforms

Due to the significant performance potential of GPUs, their utilization has evolved from high-level shading languages to modern programming languages, reducing time and complexity involved in creating GPU-enabled applications [1]. Two of the most popular APIs for GPU programming are CUDA⁴ (Compute Unified Device Architecture) and OpenCL⁵ (Open Computing Language). CUDA is a proprietary API developed by NVIDIA, while OpenCL is an open

³From <https://docs.nvidia.com/cuda/cuda-c-programming-guide/index.html>

⁴<https://developer.nvidia.com/cuda-toolkit>

⁵<https://www.khronos.org/opencl/>

royalty-free standard developed by the Khronos Group (an open, member-driven consortium, publishing and maintaining open standards)⁶.

add comparison of CUDA and OpenCL, maybe?

3 Related Work

4 Objective and methodology

5 Analysis

6 Results

7 Discussion

8 Conclusions

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