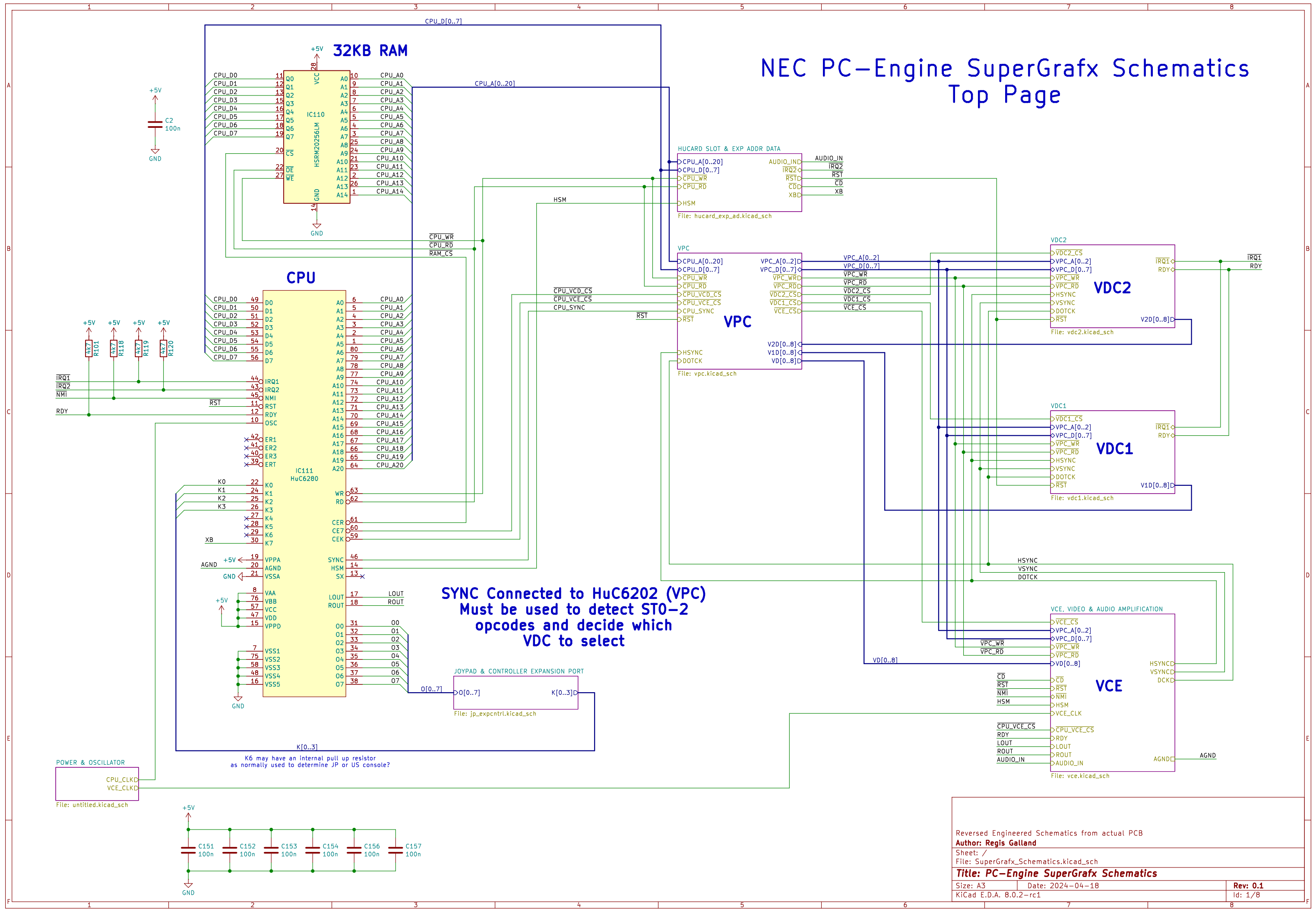


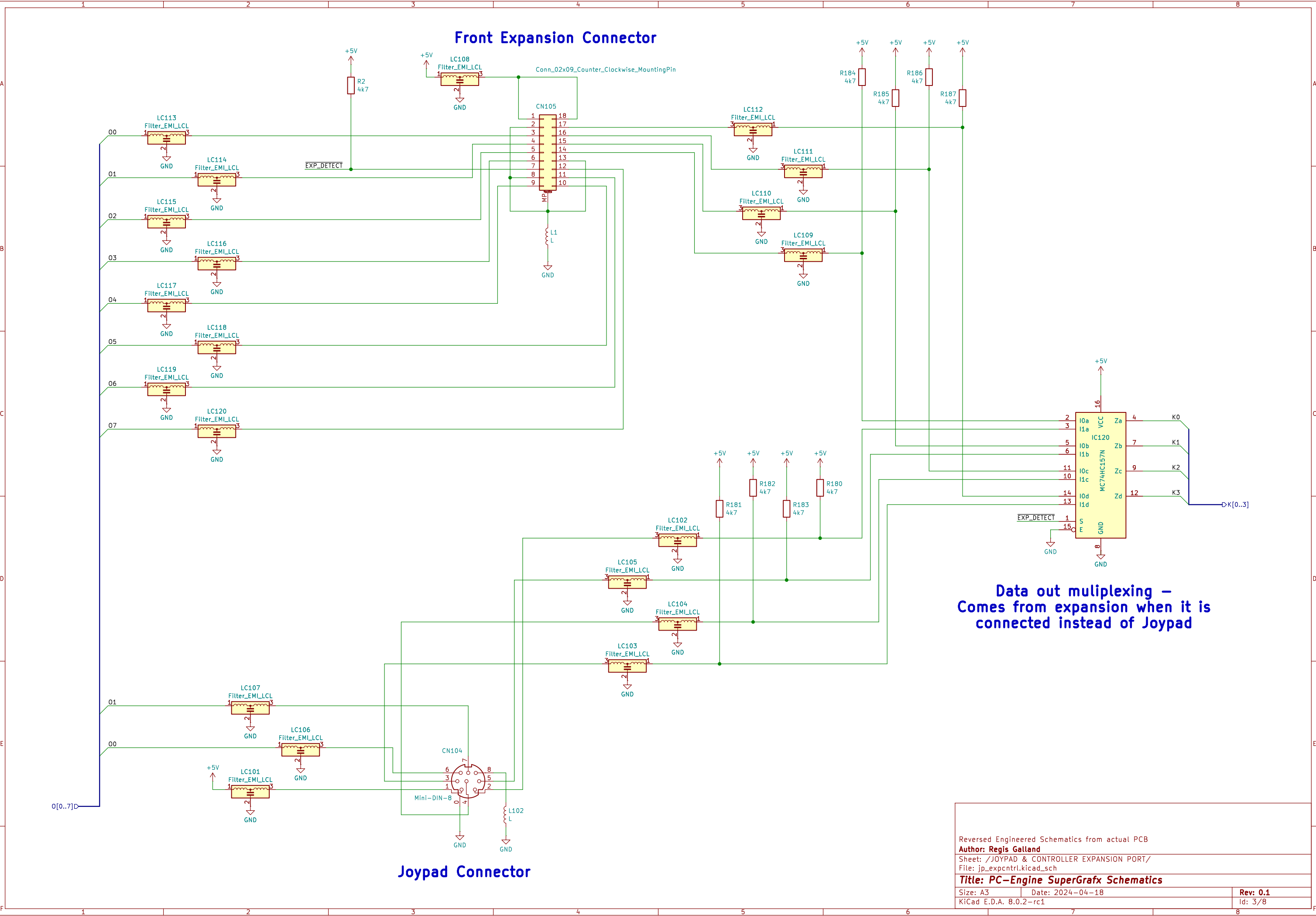
NEC PC-Engine SuperGrafx Schematics

Top Page

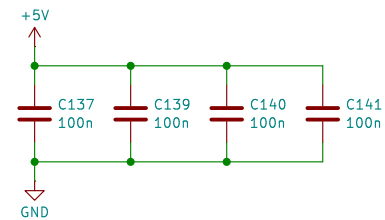


SYNC Connected to HuC6202 (VPC)
Must be used to detect ST0-2
opcodes and decide which
VDC to select

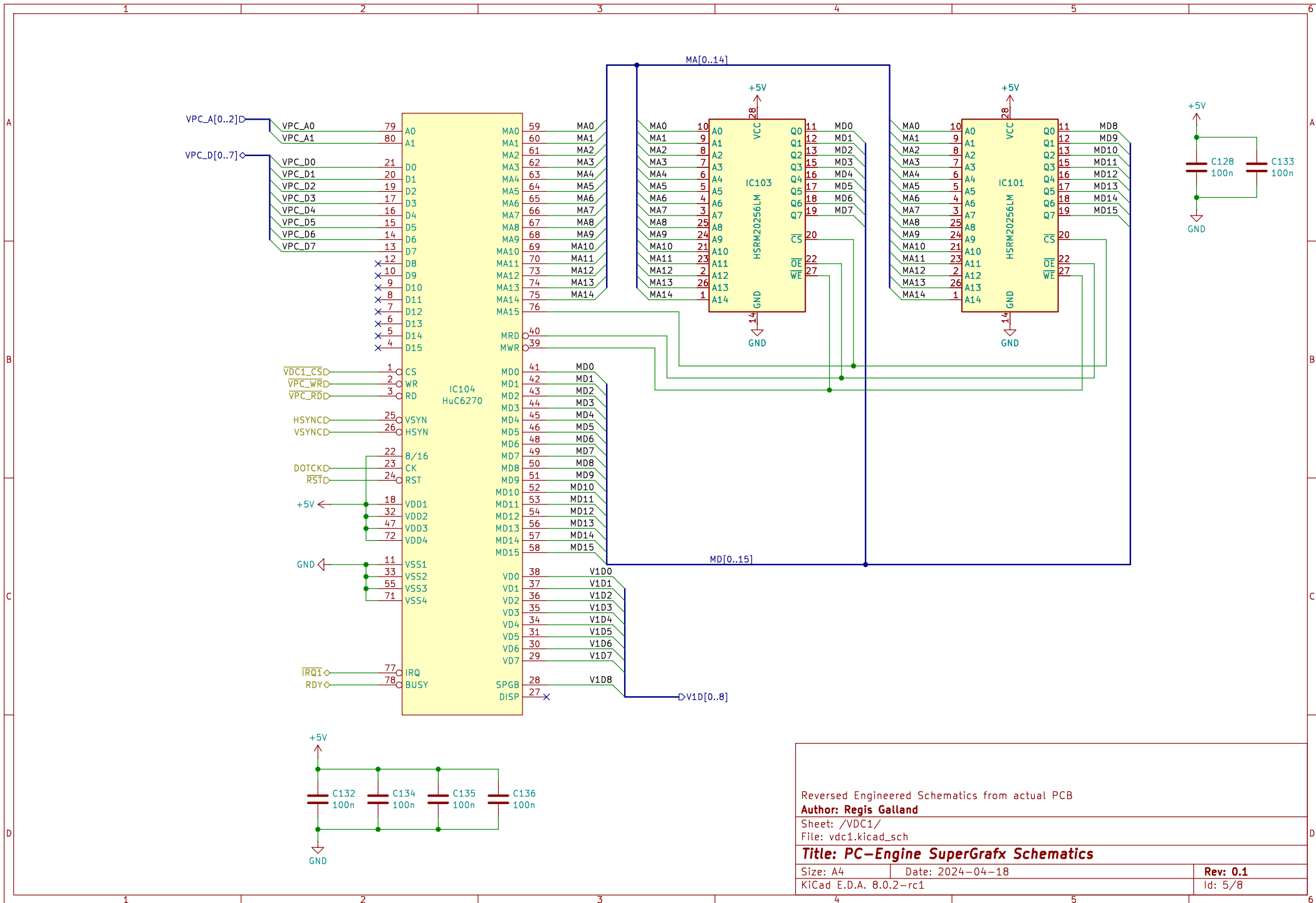
Front Expansion Connector



Data out multiplexing –
Comes from expansion when it is
connected instead of Joypad



Id: 4/8



Reversed Engineered Schematics from actual PCB

Author: Regis Galland

Sheet: /VDC1/

File: vdc1.kicad_sch

Title: PC-Engine SuperGrafx Schematics

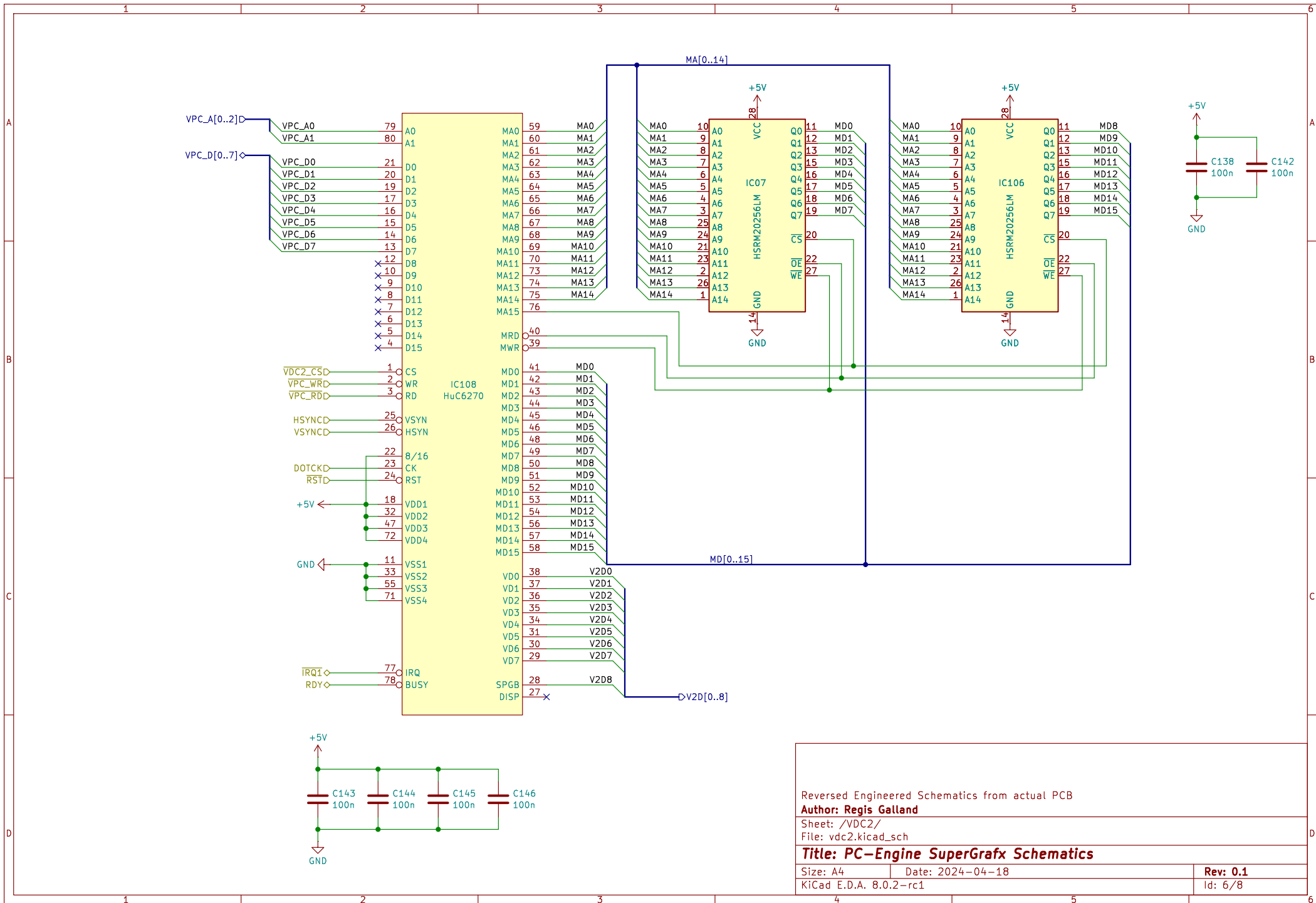
Size: A4

Date: 2024-04-18

KiCad E.D.A. 8.0.2-rc1

Rev: 0.1

Id: 5/8



Reversed Engineered Schematics from actual PCB

Author: Regis Galland

Sheet: /VDC2/

File: vdc2.kicad_sch

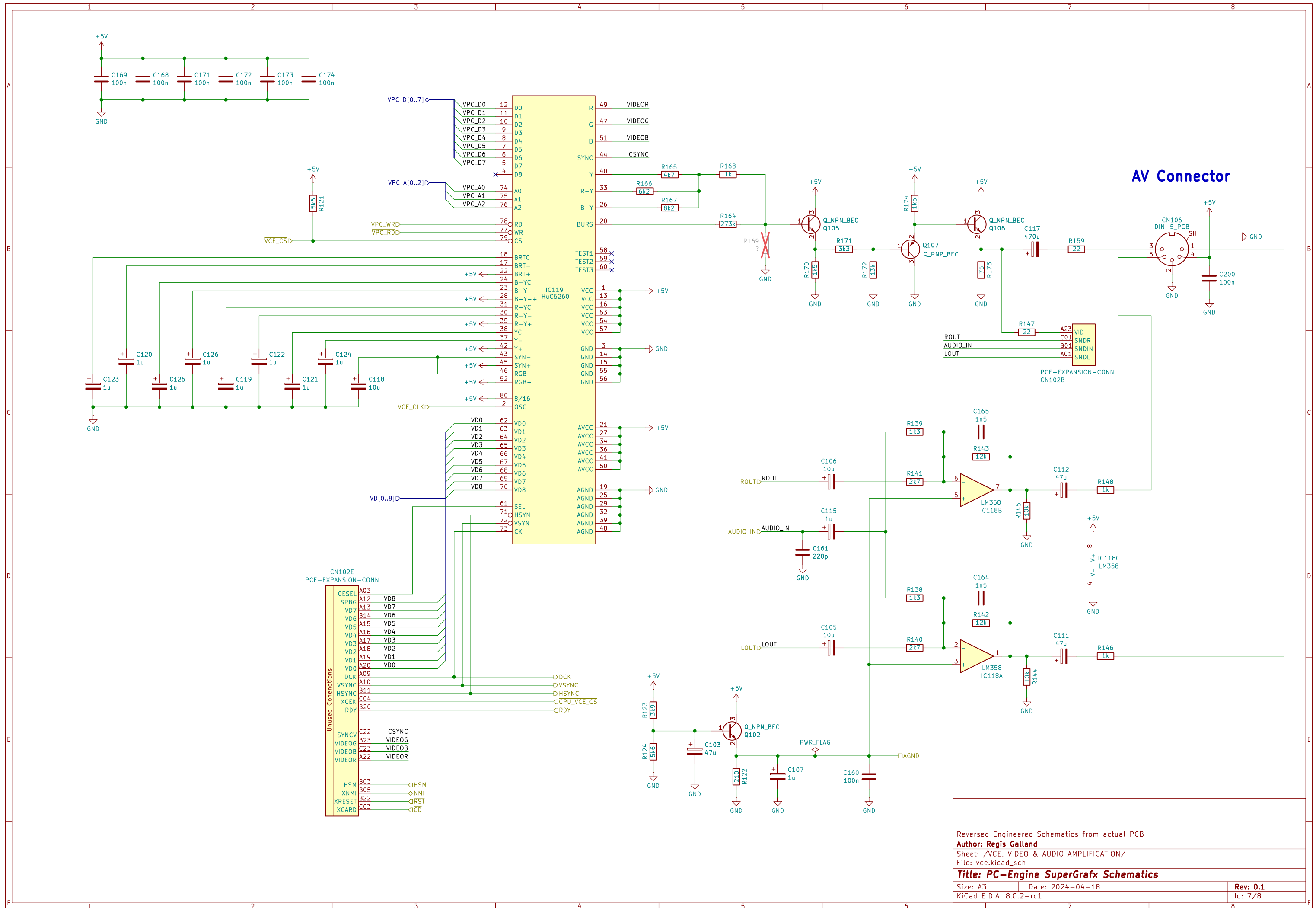
Title: PC-Engine SuperGrafx Schematics

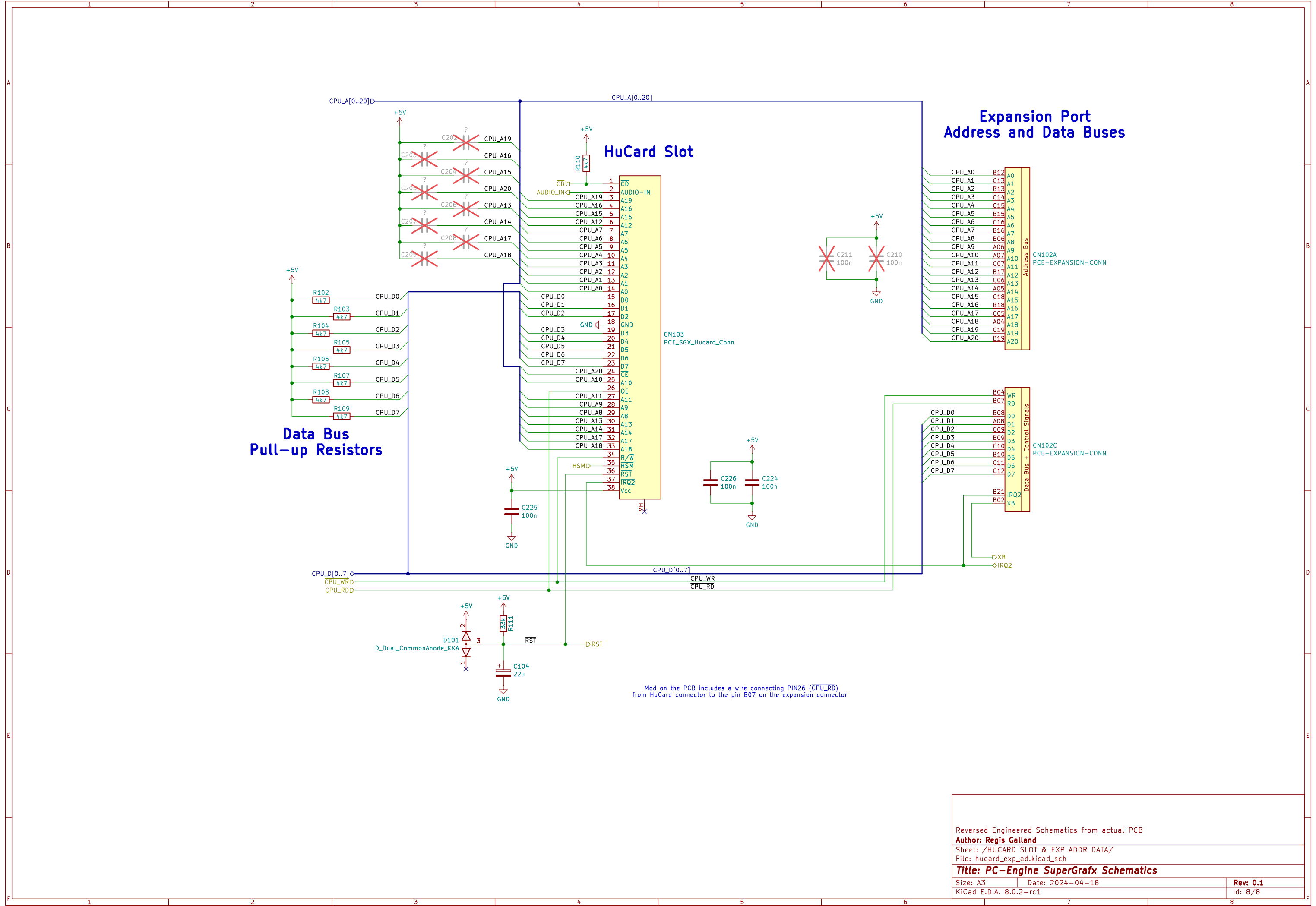
Size: A4 Date: 2024-04-18

KiCad E.D.A. 8.0.2-rc1

Rev: 0.1

Id: 6/8





Reversed Engineered Schematics from actual PCB

Author: Regis Galland

Sheet: /HUCARD SLOT & EXP ADDR DATA/

File: hucard_exp_ad.kicad_sch

Title: PC-Engine SuperGrafx Schematics

Size: A3

Date: 2024-04-18

Rev: 0.1

KiCad E.D.A. 8.0.2-rc1

Id: 8/8