

D

C

B

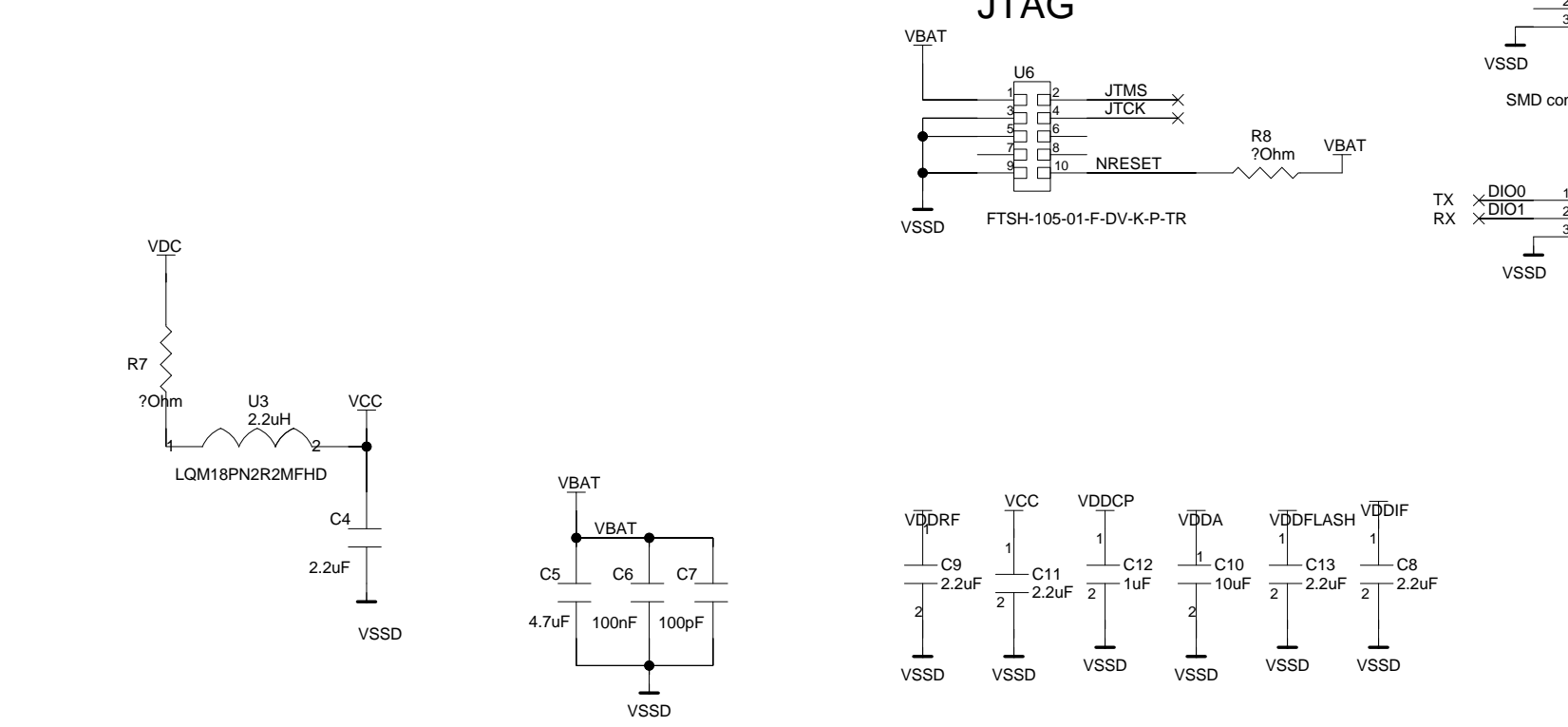
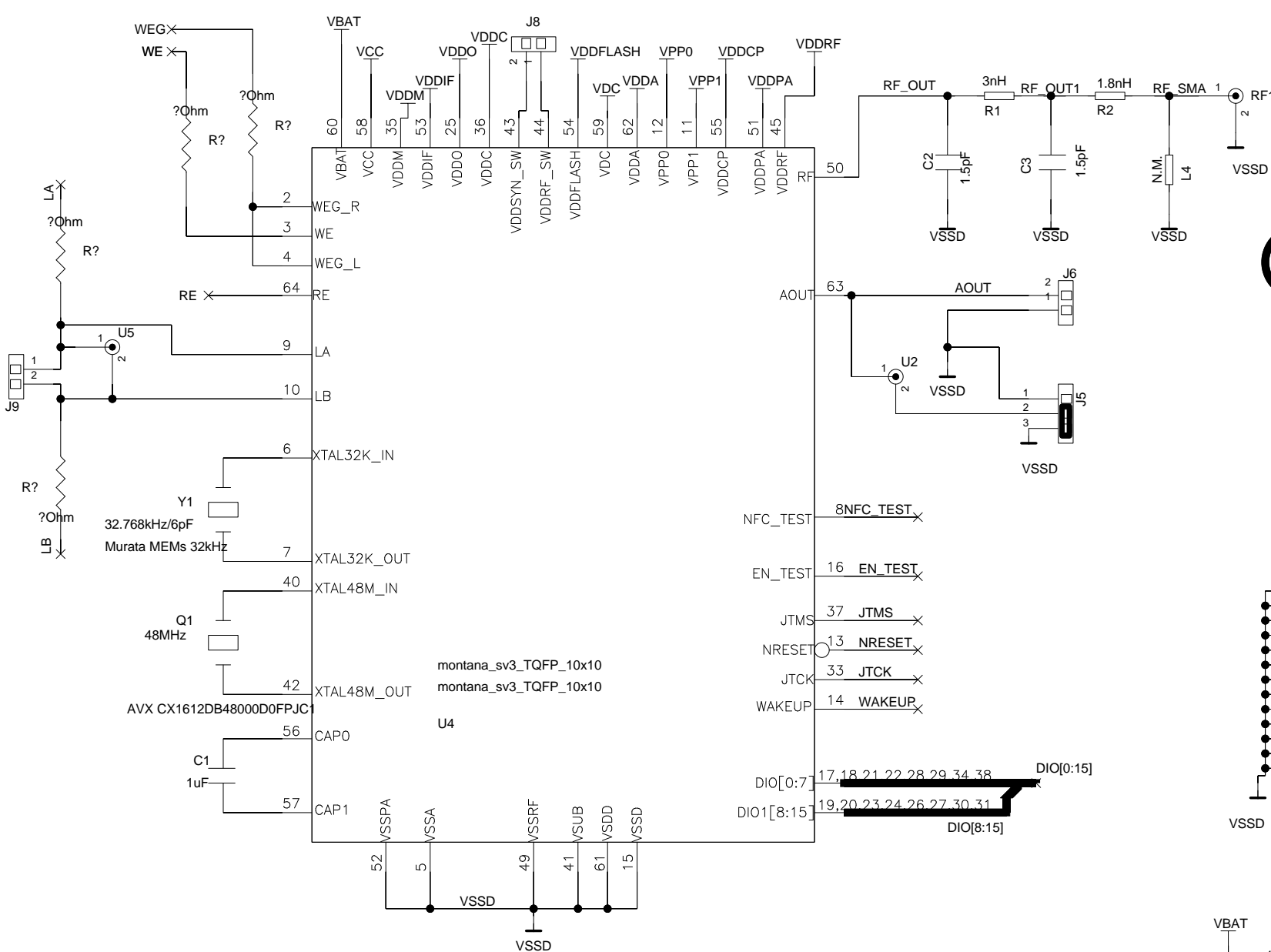
A

D

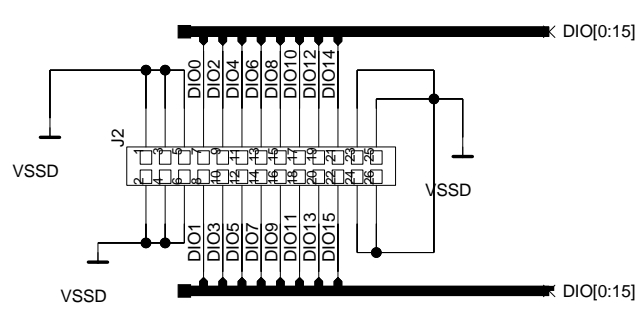
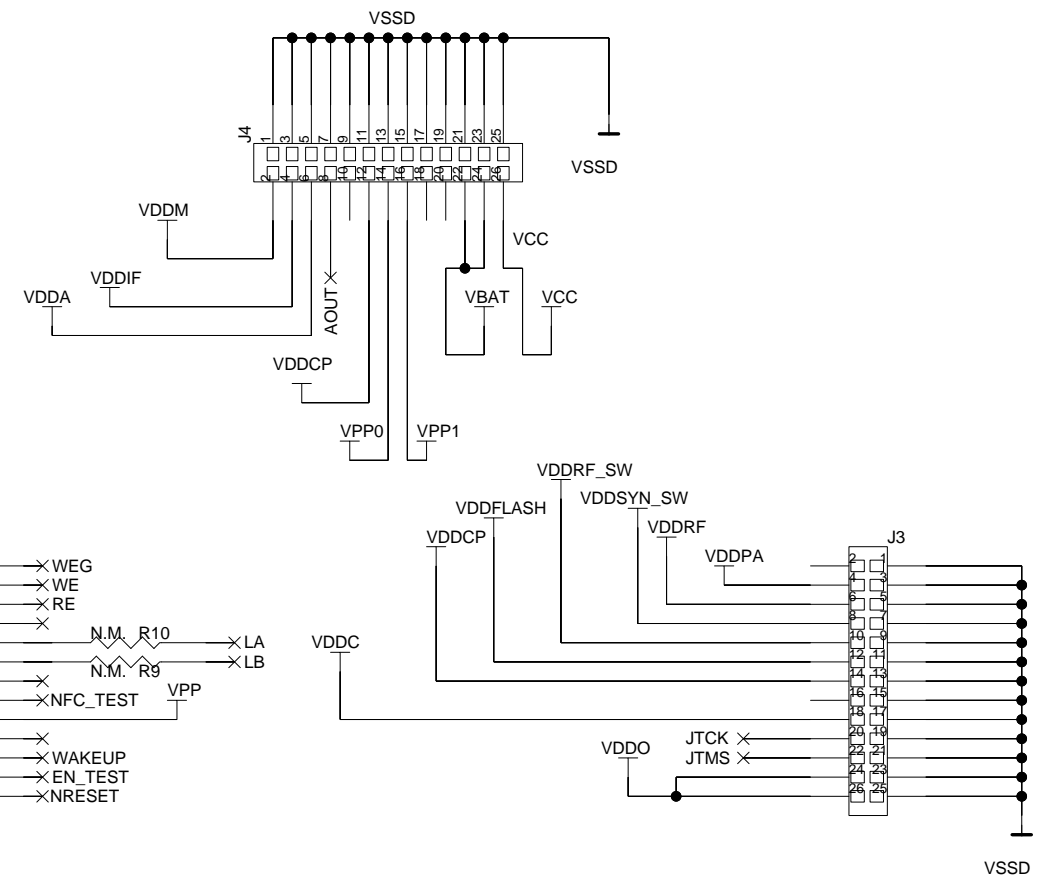
C

B


A



To daughter board (dfs_c096v01a)



Version TQFP 64pins

REVISION HISTORY V1.0 Initial Design		<div><div>ON Semiconductor Switzerland SA Champs-Montants 12a CH-2074 Marin, Switzerland</div></div> <div>Montana SV3</div>	
TITLE SCHEMATIC			
top			
SIZE A3	TITLE PROJECT dfs_c097v01a		REV 1.0
AUTHOR NLI		DATE 26/09/2019:16:15	SHEET 1 of 1