
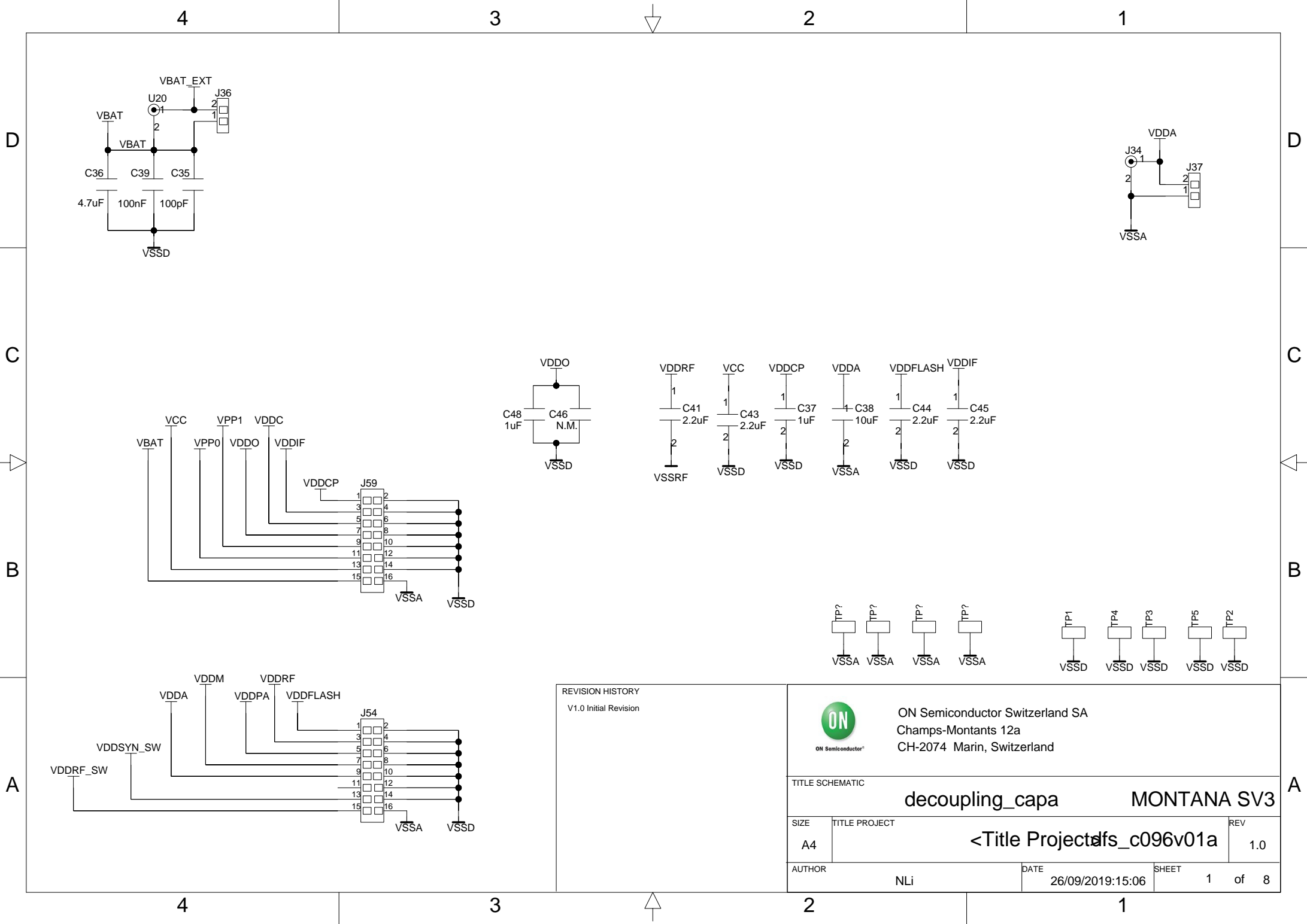
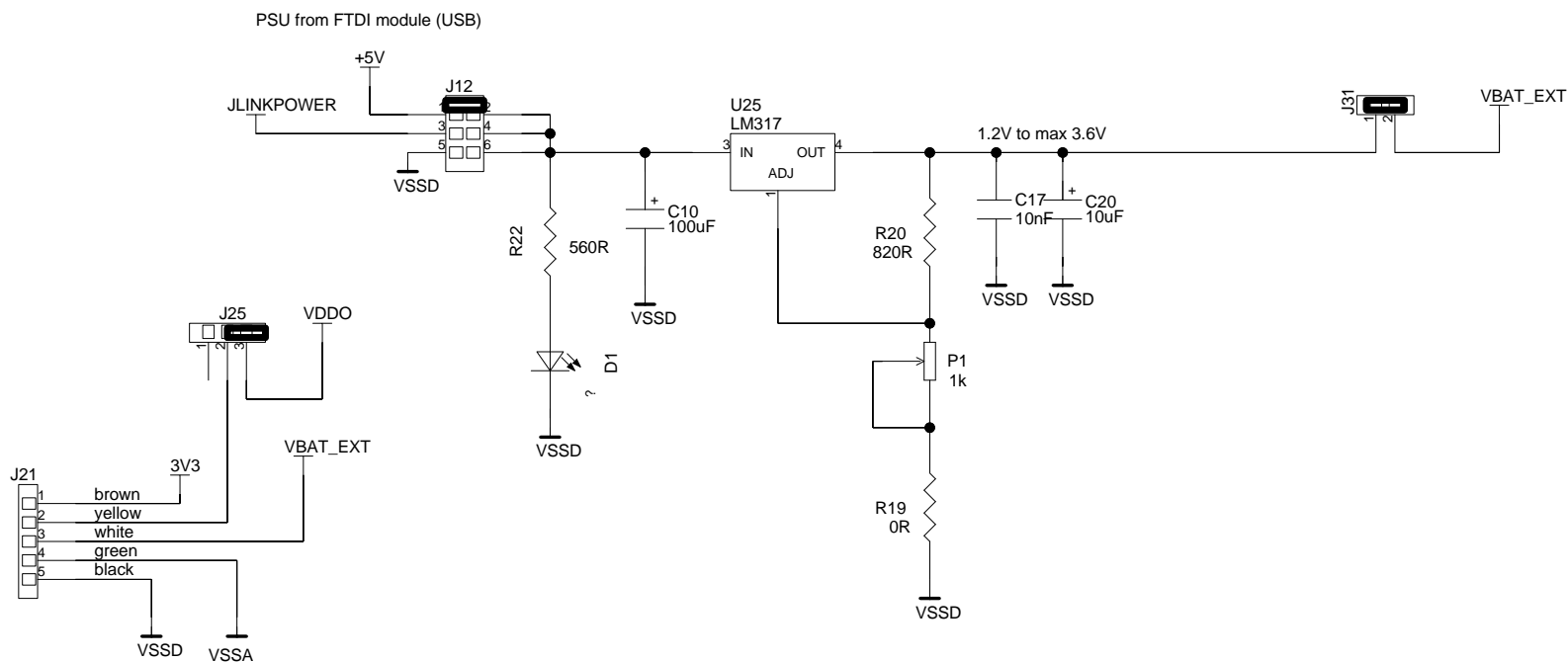


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AUTHOR	DATE		SHEET		
NLi	26/09/2019:16:11		1 of 8		





REVISION HISTORY
V1.0 Initial Revision



ON Semiconductor Switzerland SA
Champs-Montants 12a
CH-2074 Marin, Switzerland

TITLE SCHEMATIC

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TITLE PROJECT

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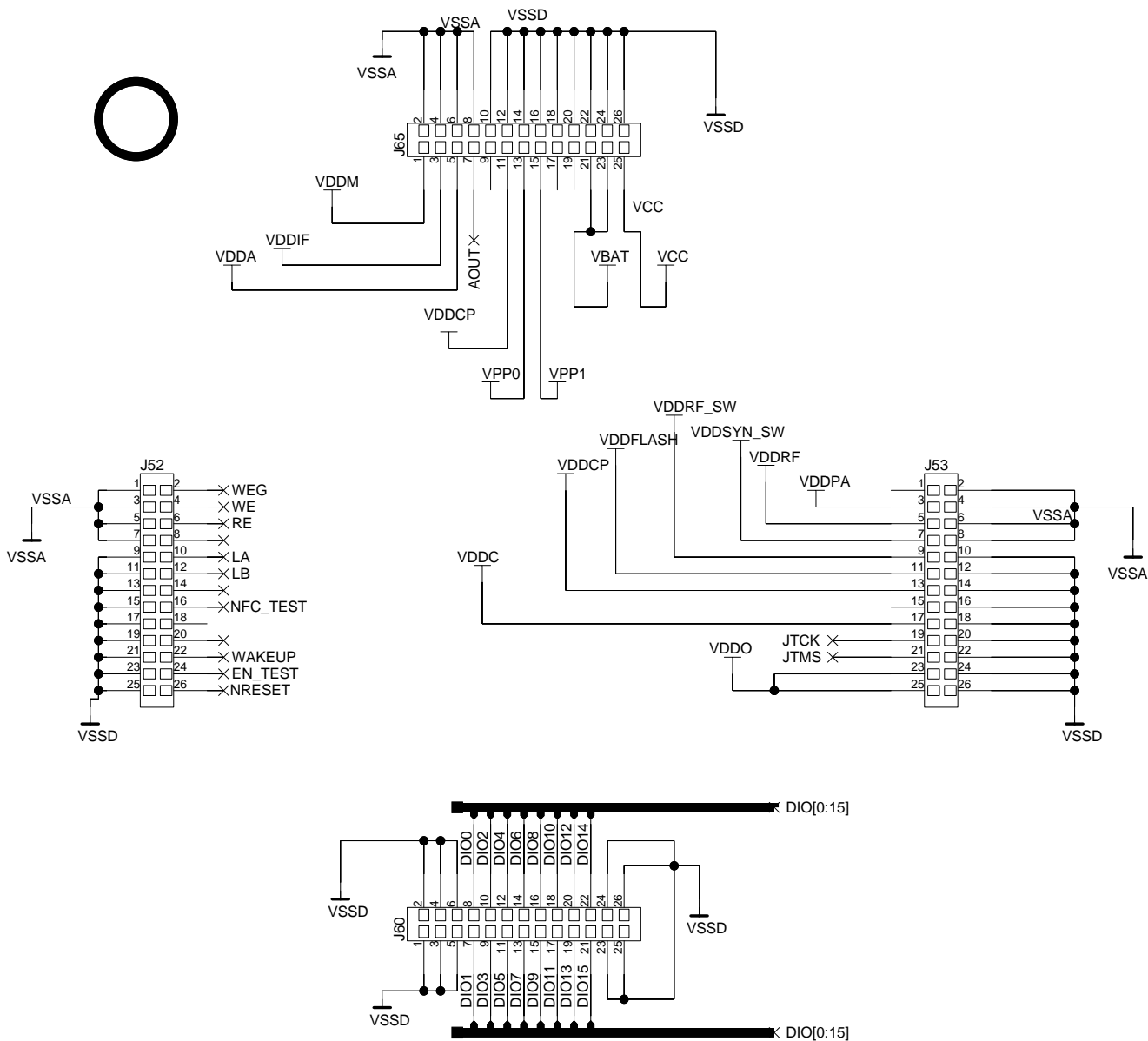
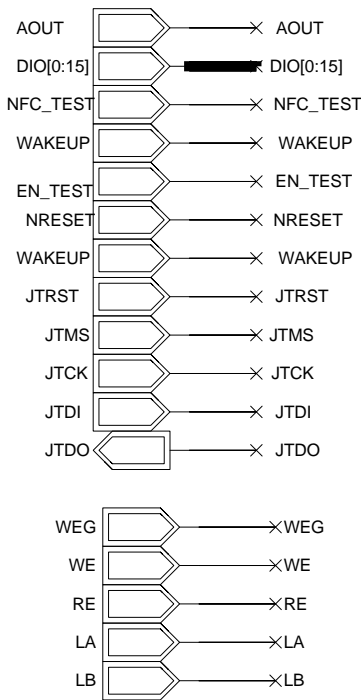
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To daughter board (dfs_c097v01a)



REVISION HISTORY
V1.0 Initial Design



ON Semiconductor Switzerland SA
Champs-Montants 12a
CH-2074 Marin, Switzerland

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SIZE	A3	TITLE PROJECT	dfs_c096v01a
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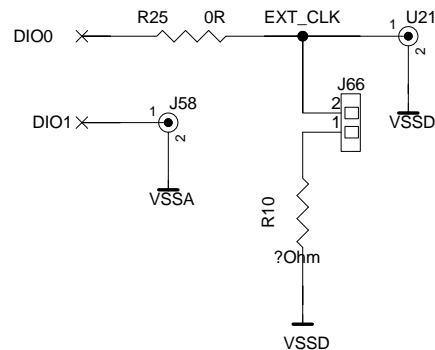
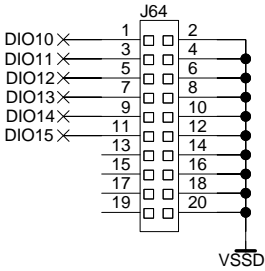
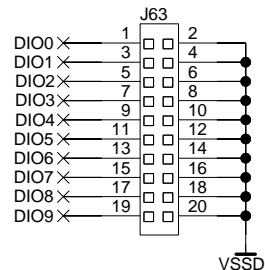
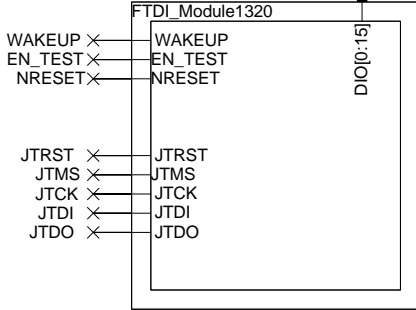
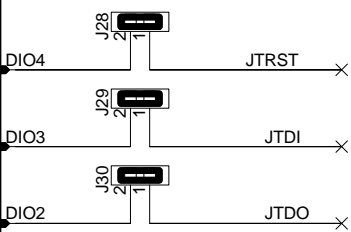
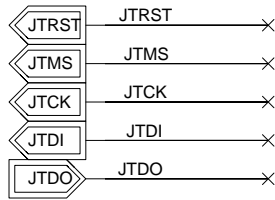
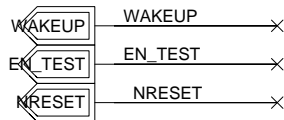
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REVISION HISTORY
V1.0 Initial Revision



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CH-2074 Marin, Switzerland

TITLE SCHEMATIC

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TITLE PROJECT
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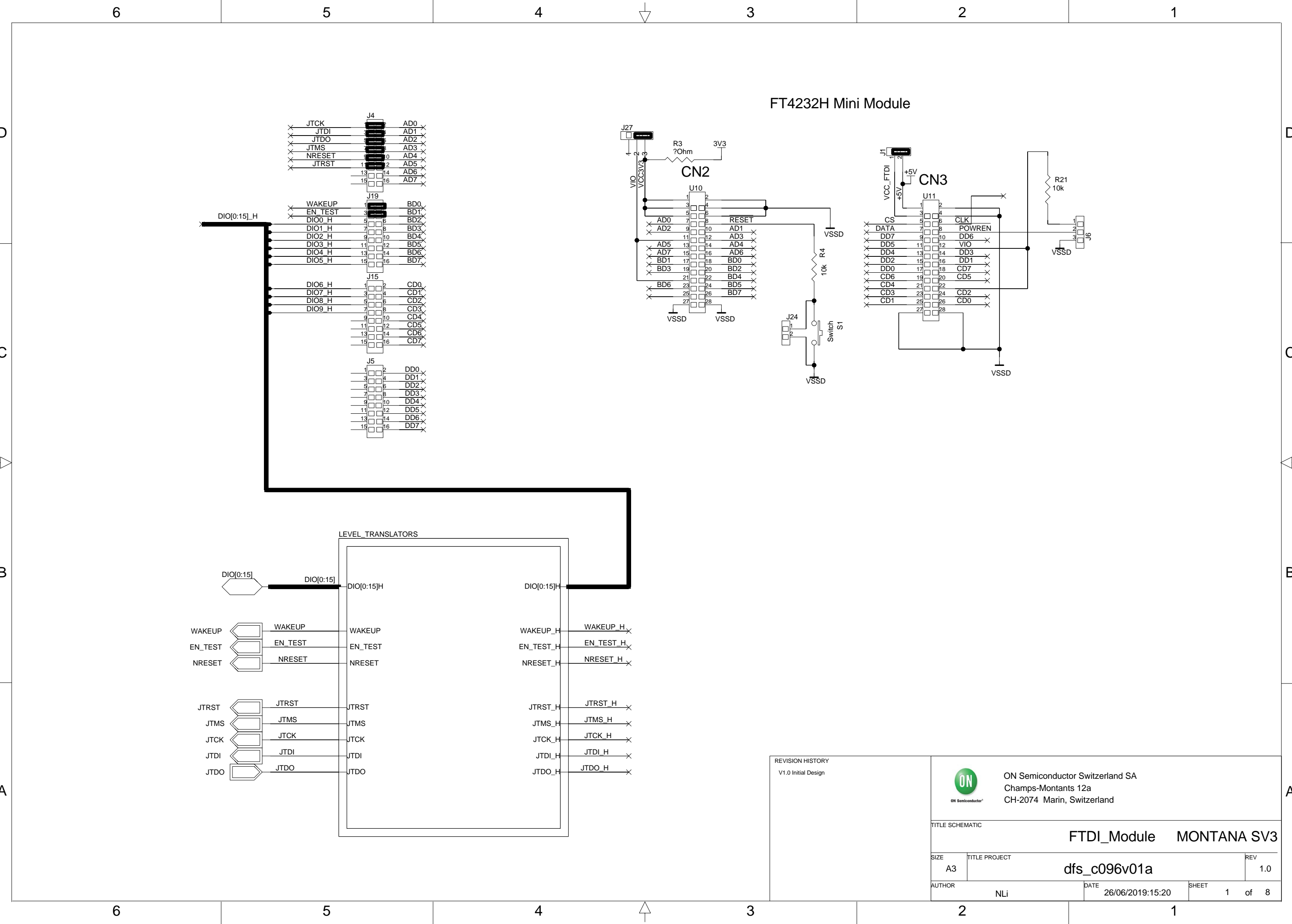
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FT4232H Mini Module

REVISION HISTORY
V1.0 Initial Design

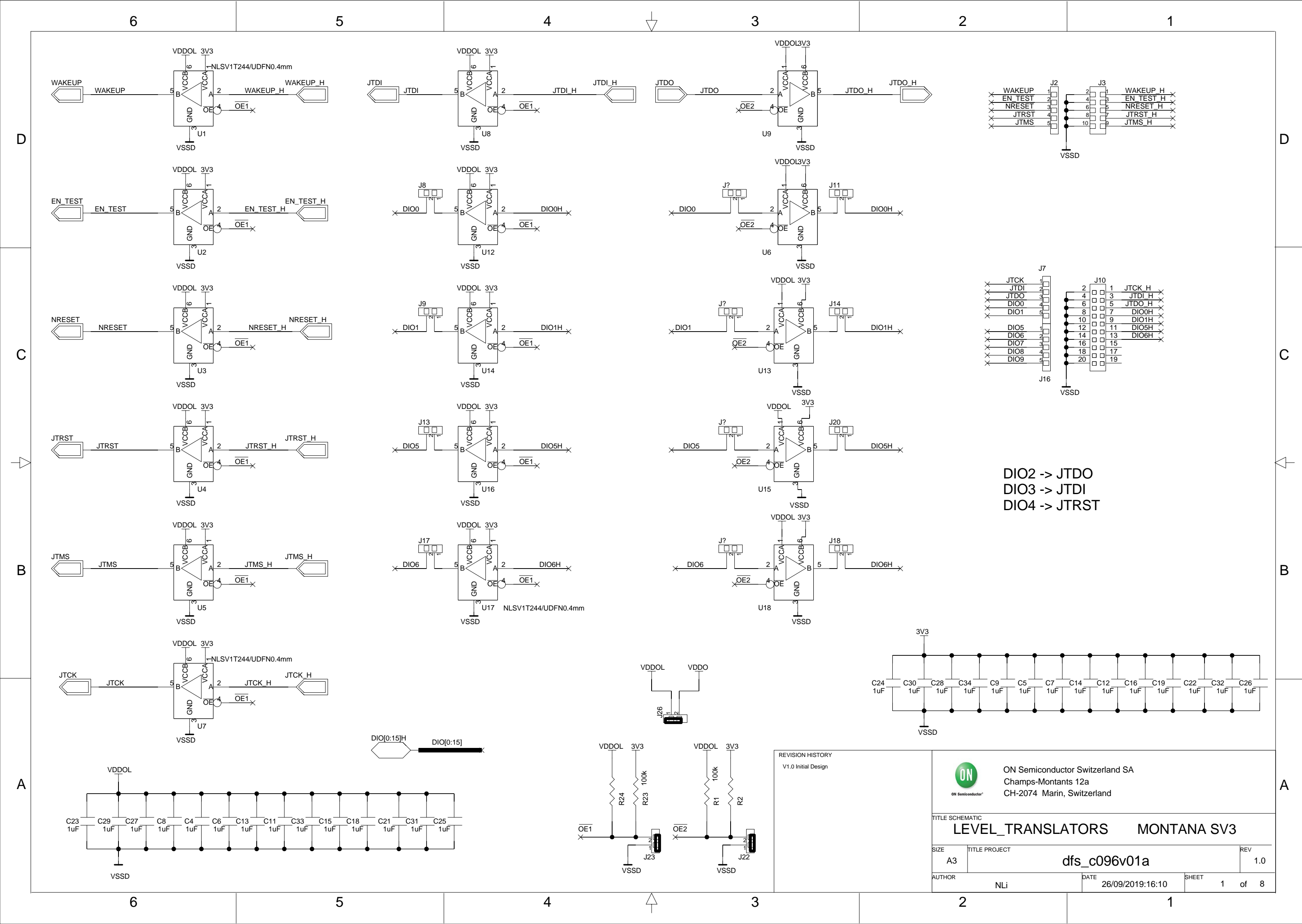


ON Semiconductor Switzerland SA
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TITLE SCHEMATIC
FTDI_Module MONTANA SV3

SIZE A3	TITLE PROJECT dfs_c096v01a	REV 1.0
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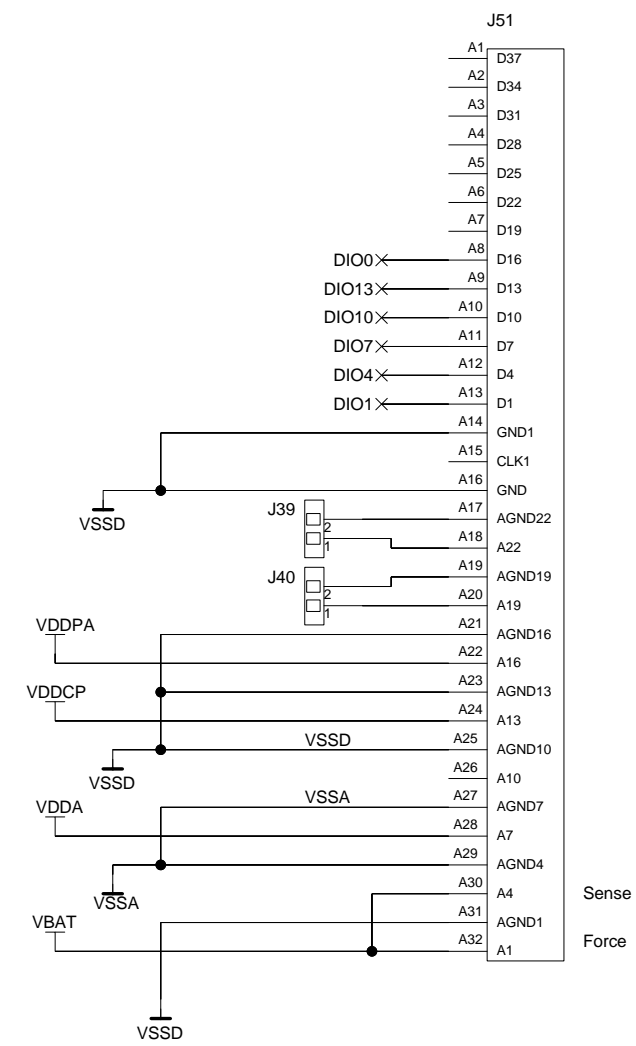
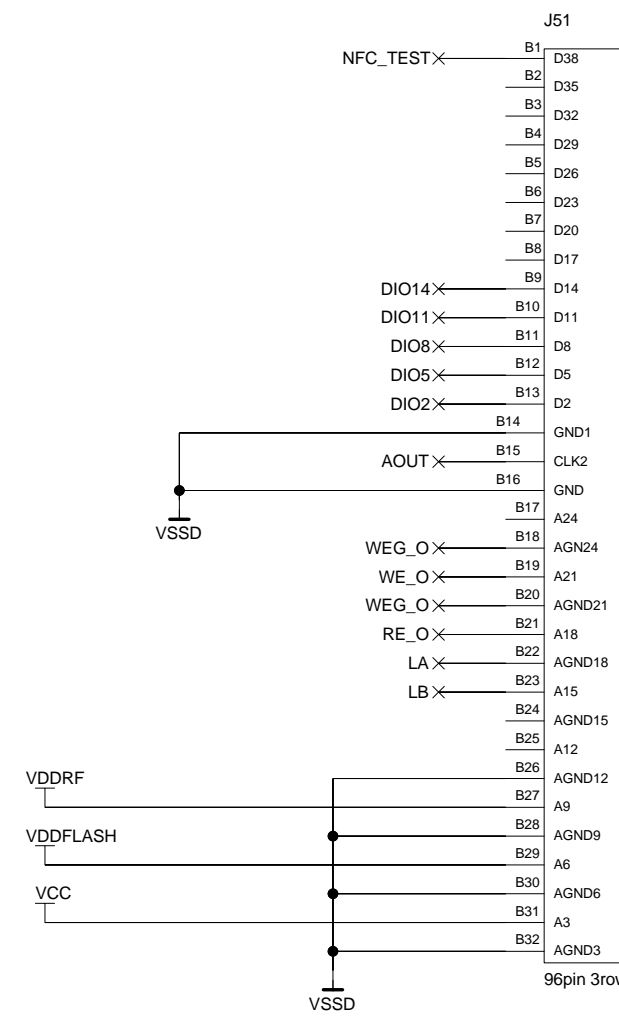
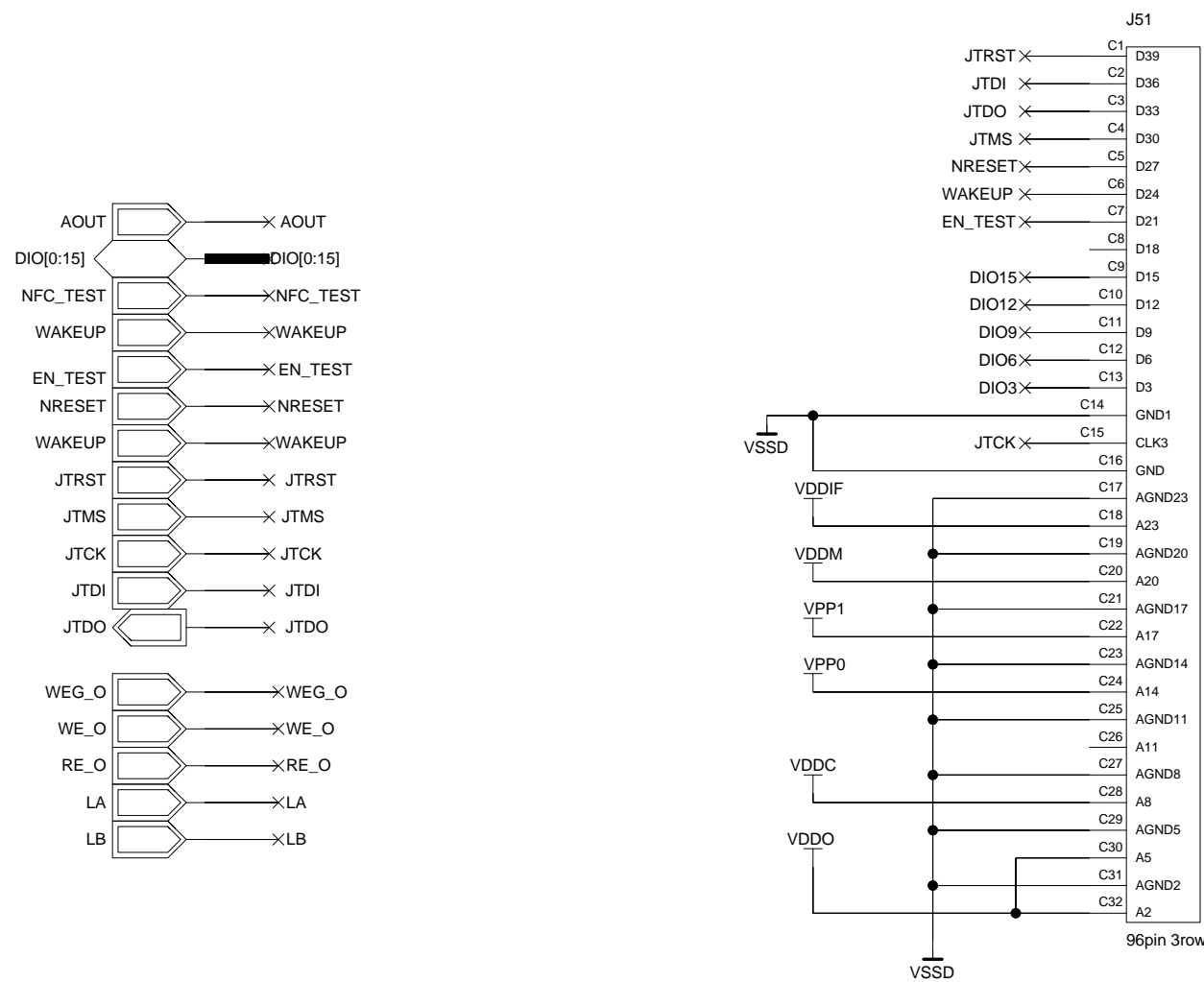
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