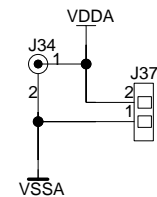
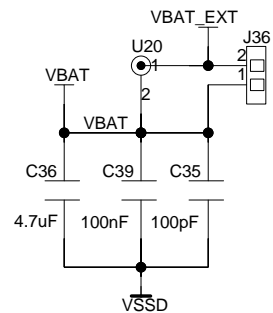
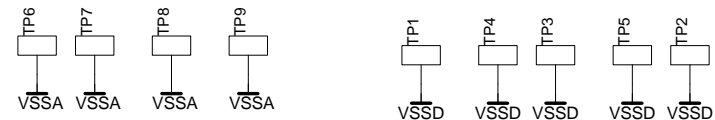
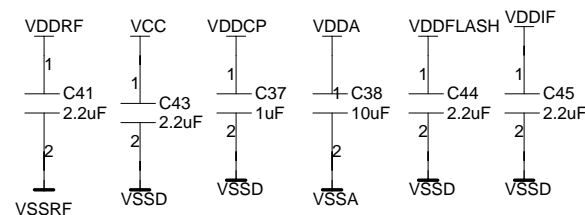
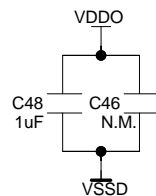
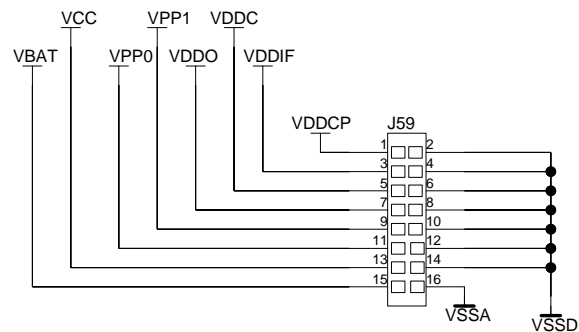


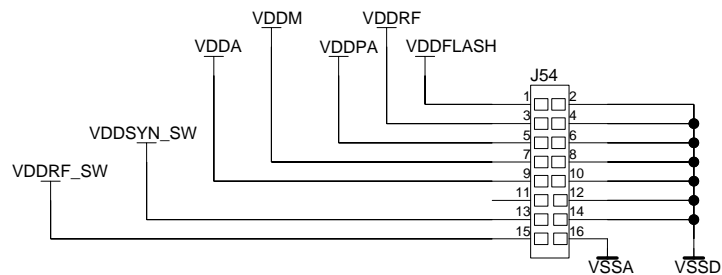
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REVISION HISTORY  
V1.0 Initial Revision



ON Semiconductor Switzerland SA  
Champs-Montants 12a  
CH-2074 Marin, Switzerland

TITLE SCHEMATIC

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SIZE  
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TITLE PROJECT  
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REV  
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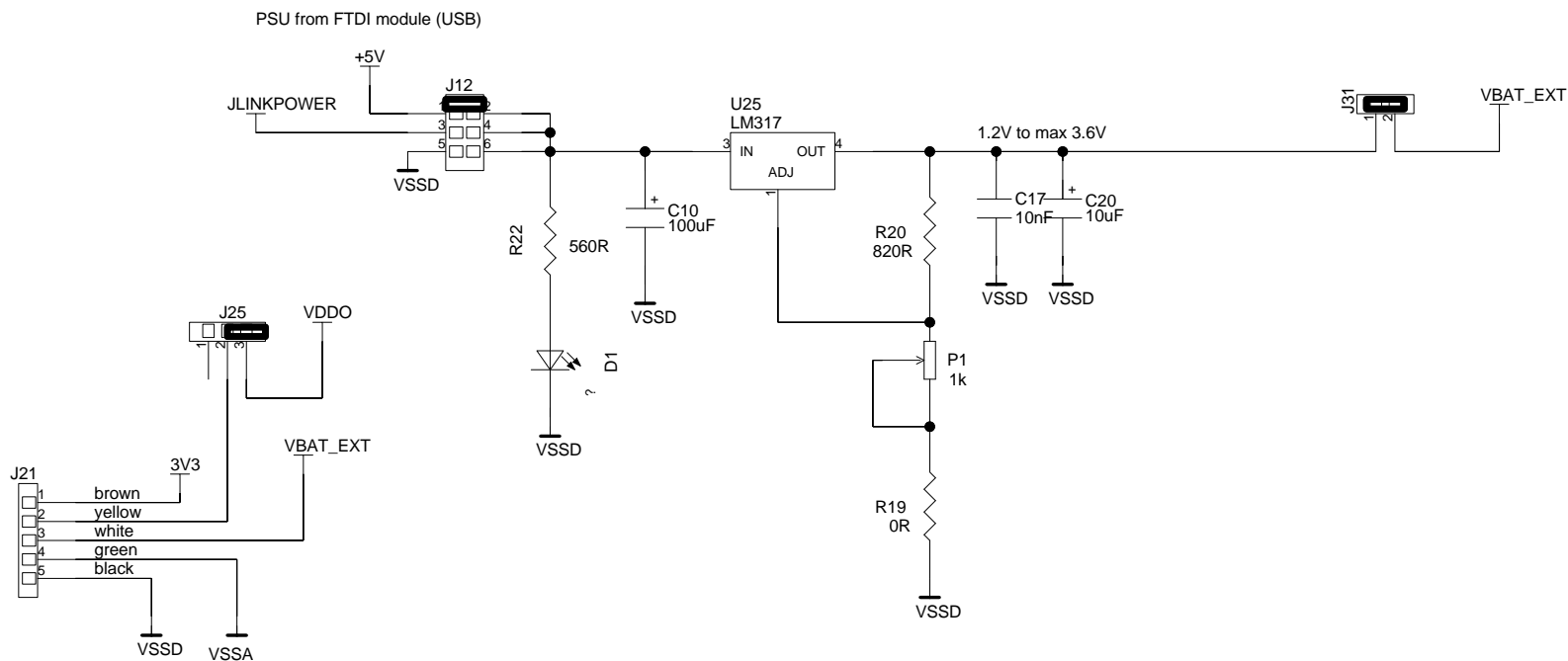
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REVISION HISTORY  
V1.0 Initial Revision



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TITLE SCHEMATIC

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TITLE PROJECT

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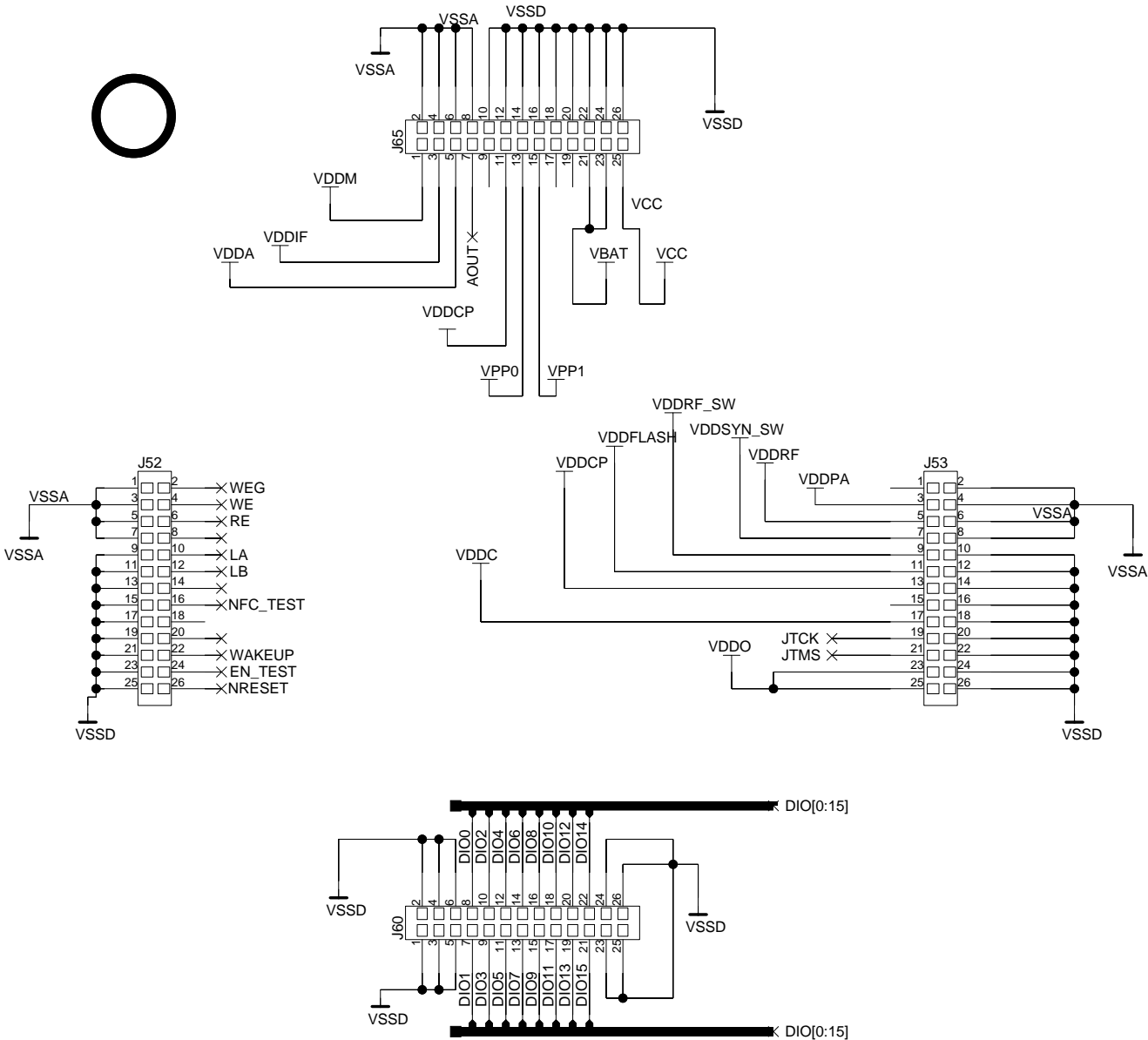
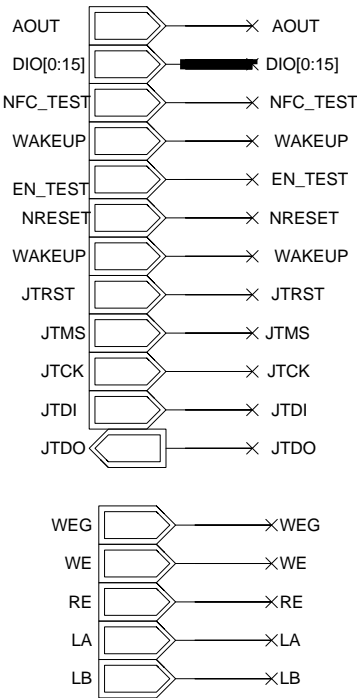
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To daughter board (dfs\_c097v01a)

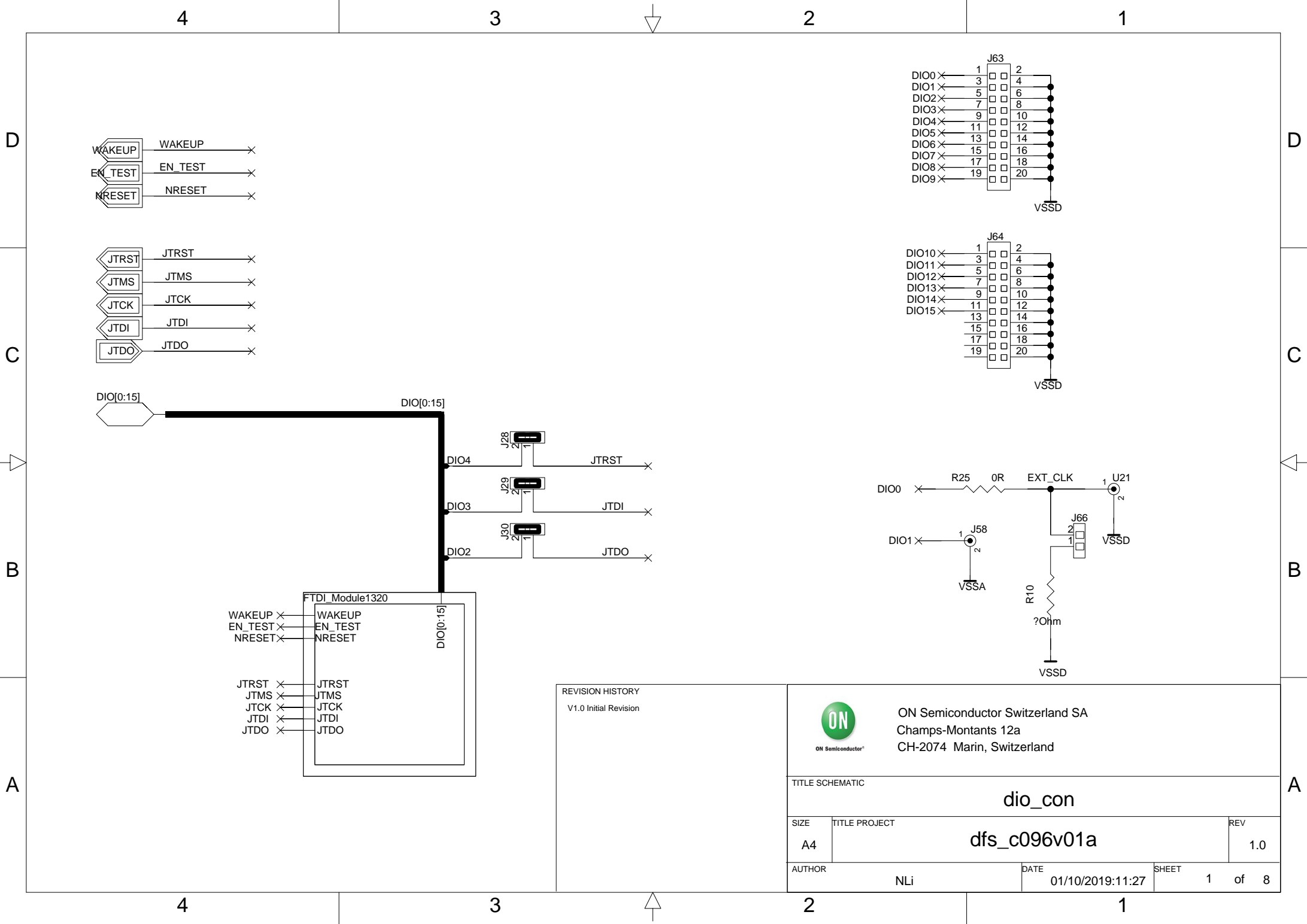


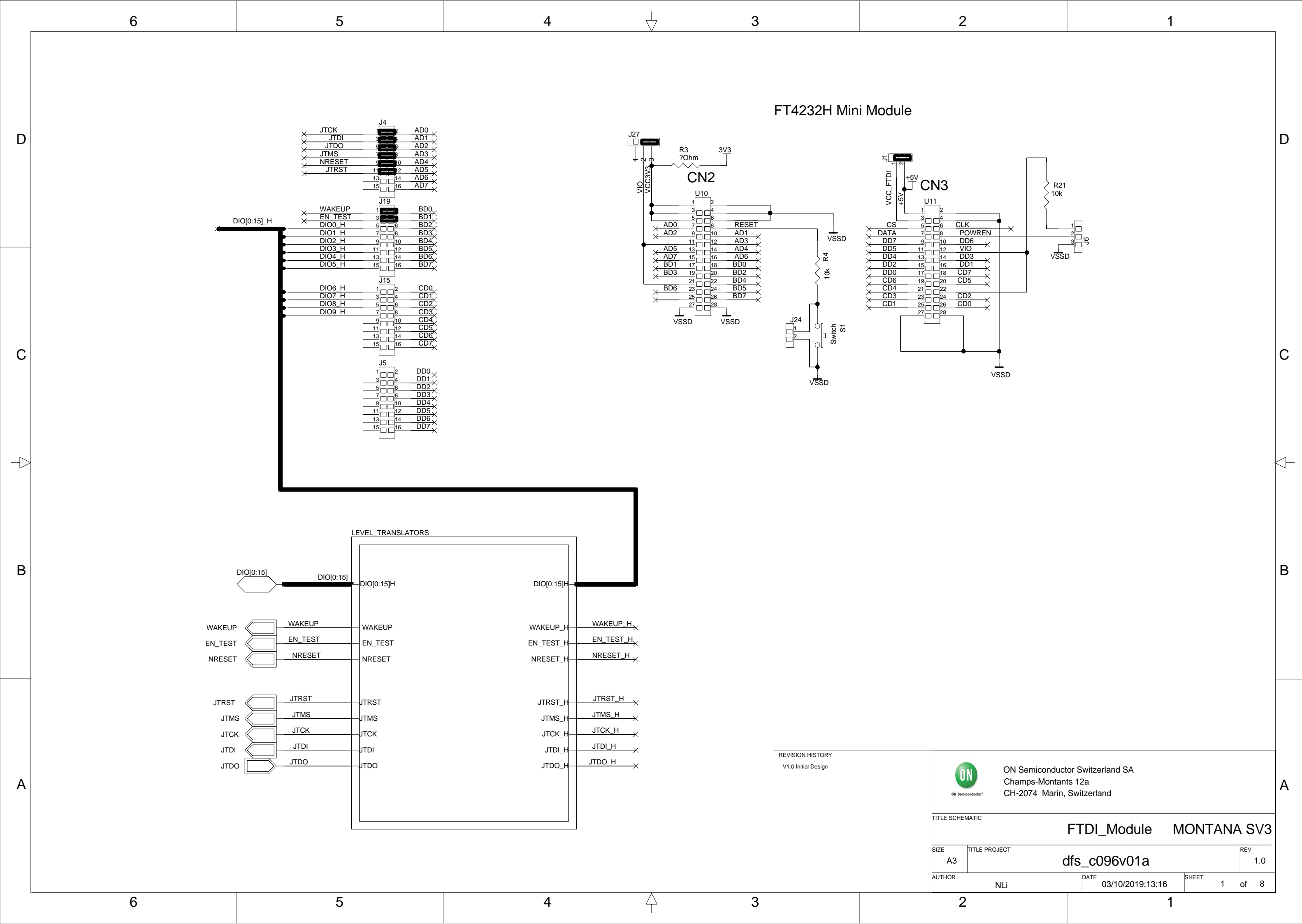
REVISION HISTORY  
V1.0 Initial Design

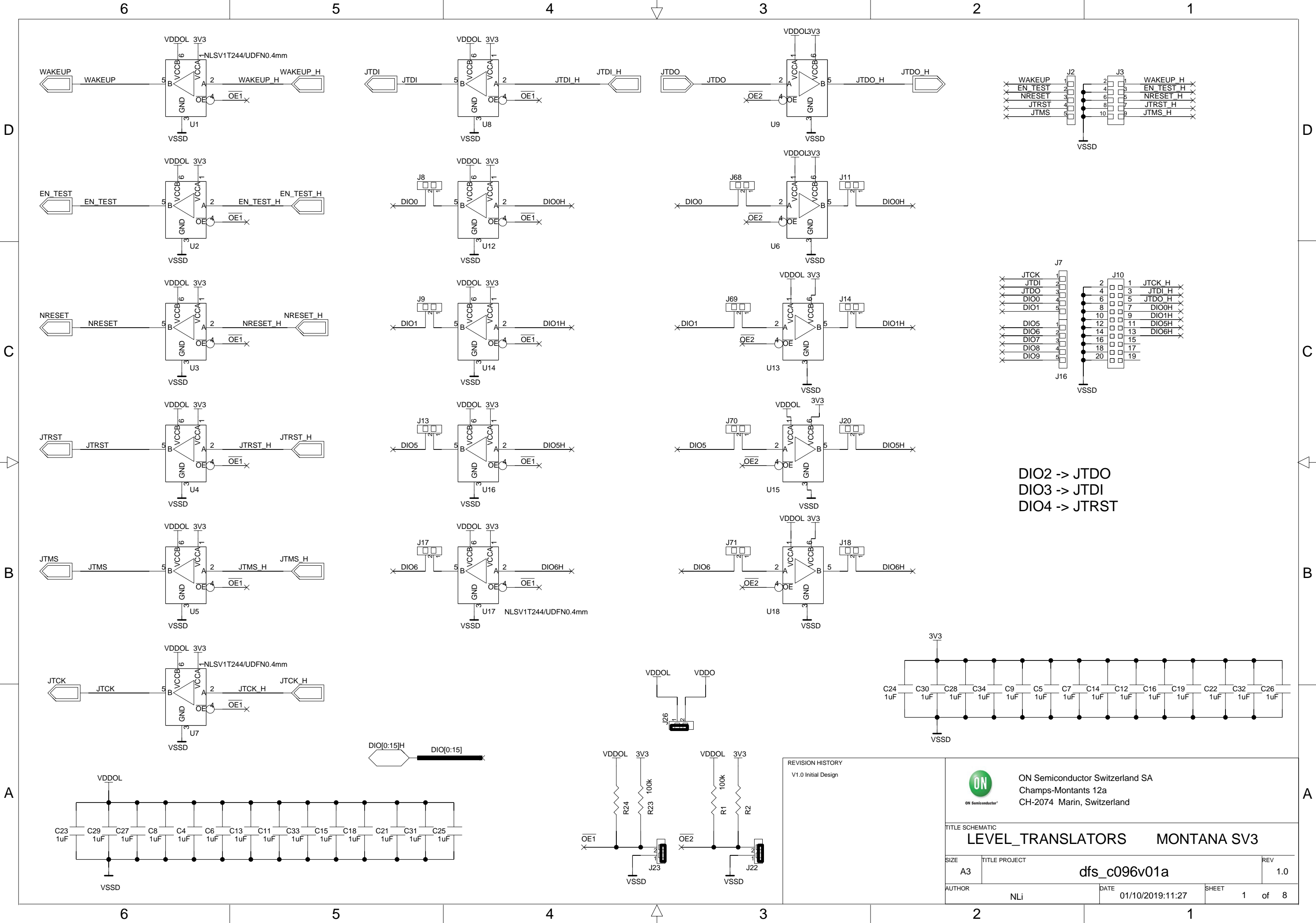


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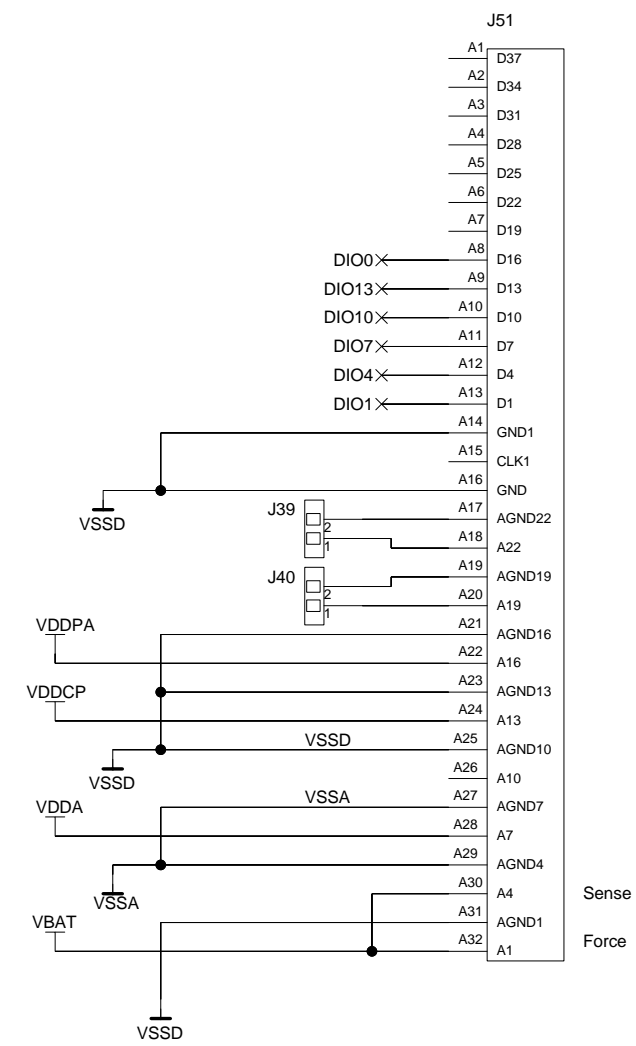
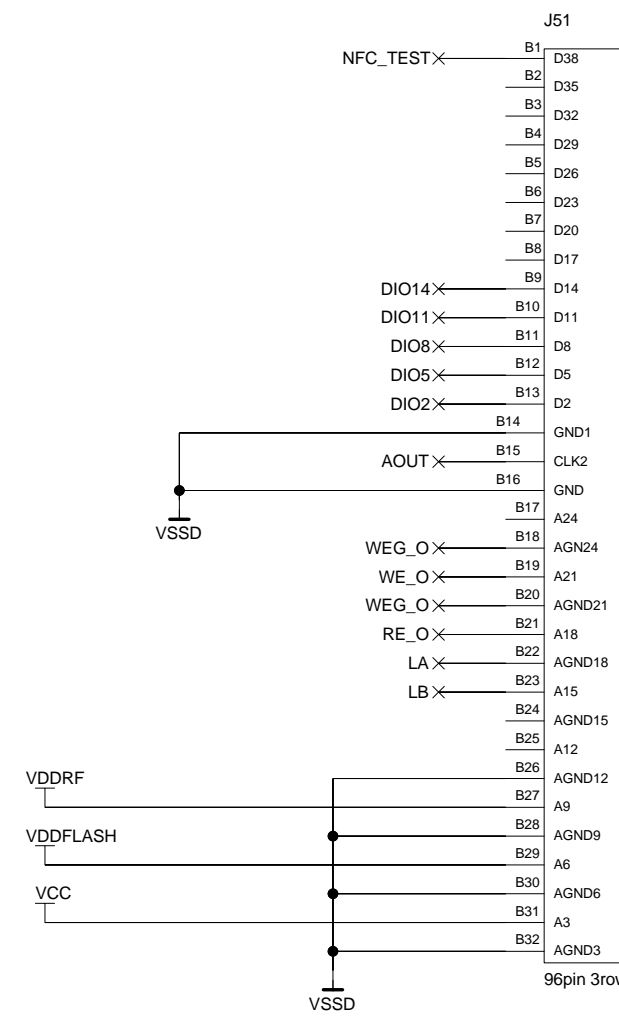
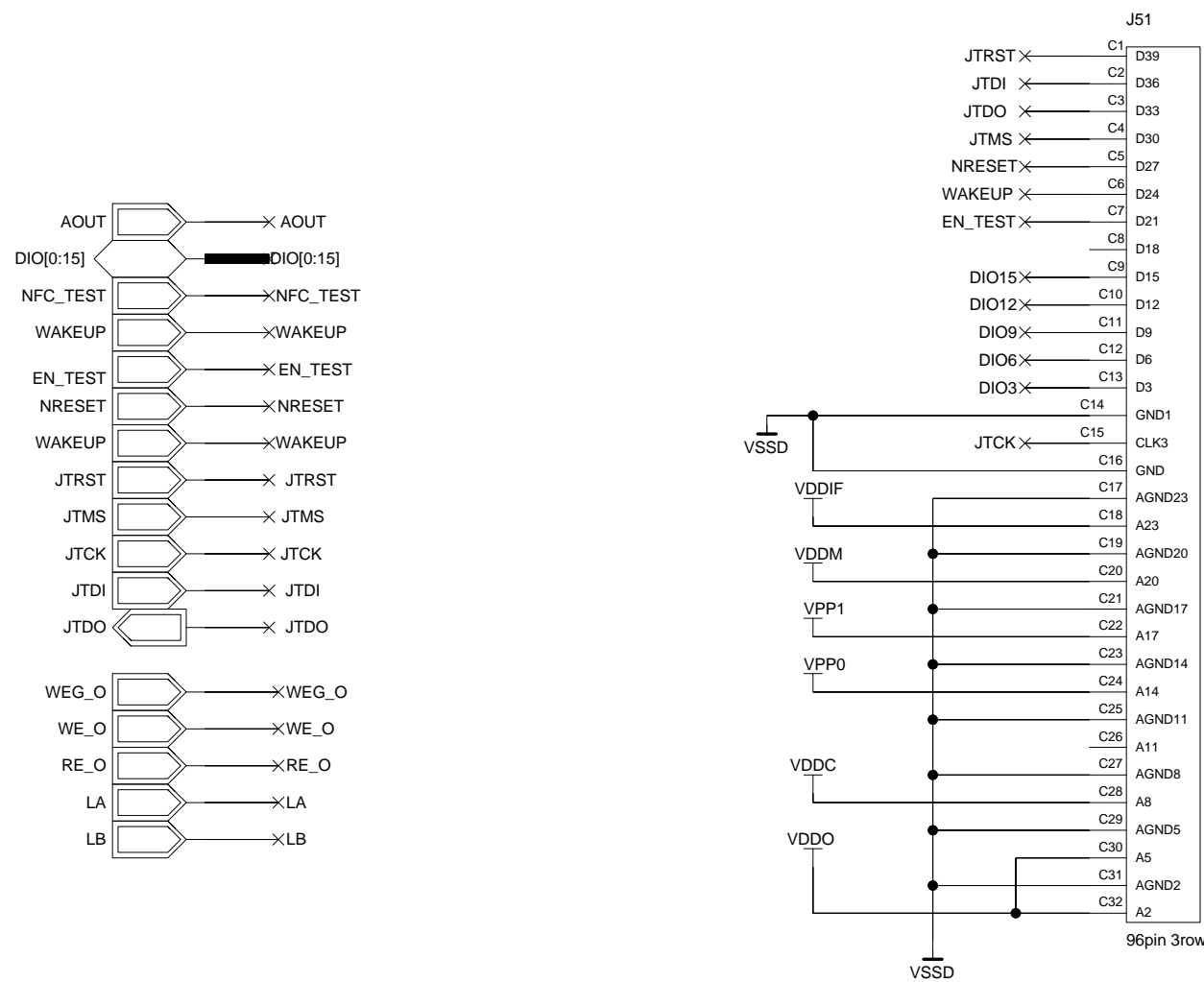
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REVISION HISTORY

V1.0 Initial Design



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TITLE SCHEMATIC

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1 of 8