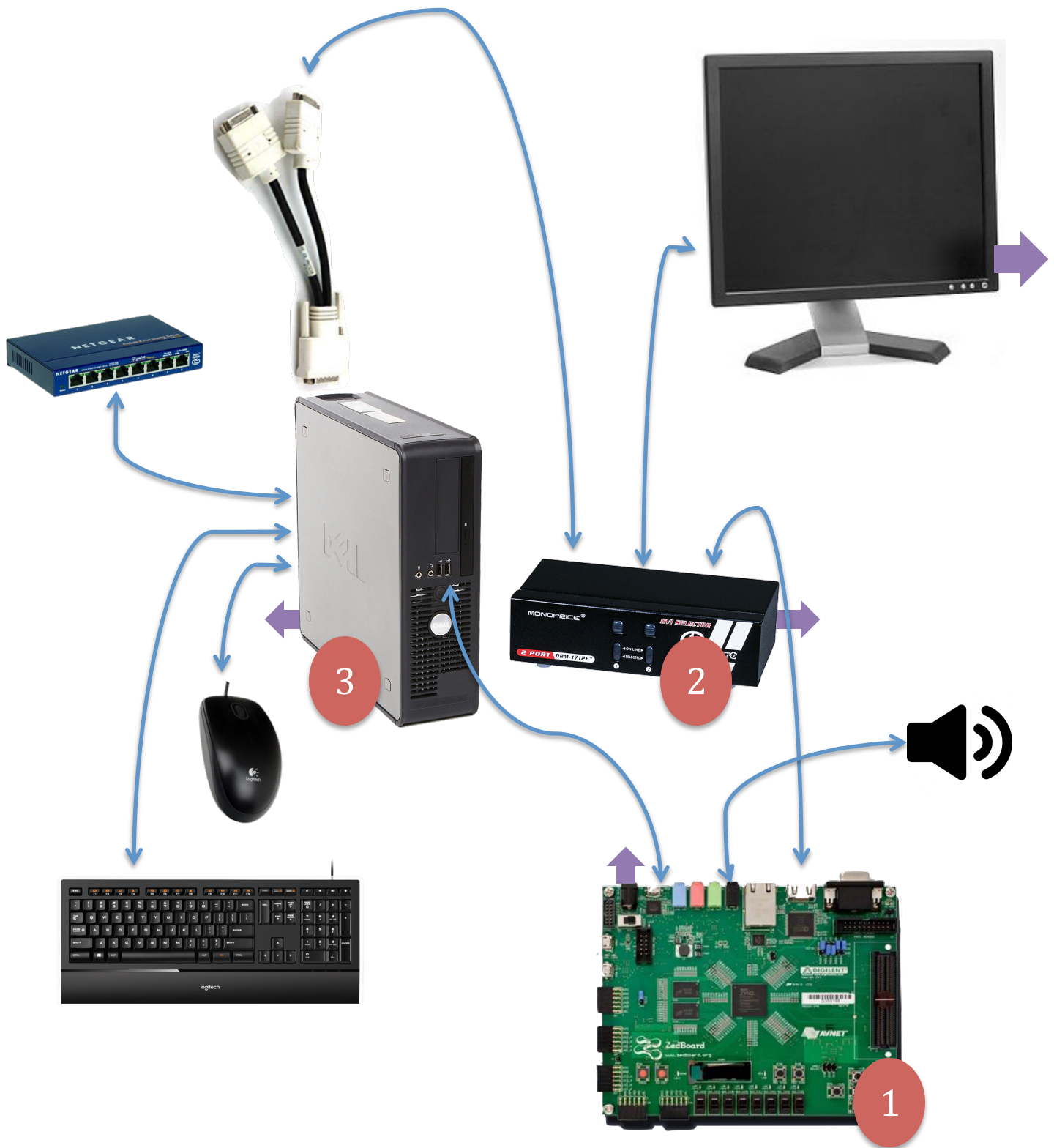
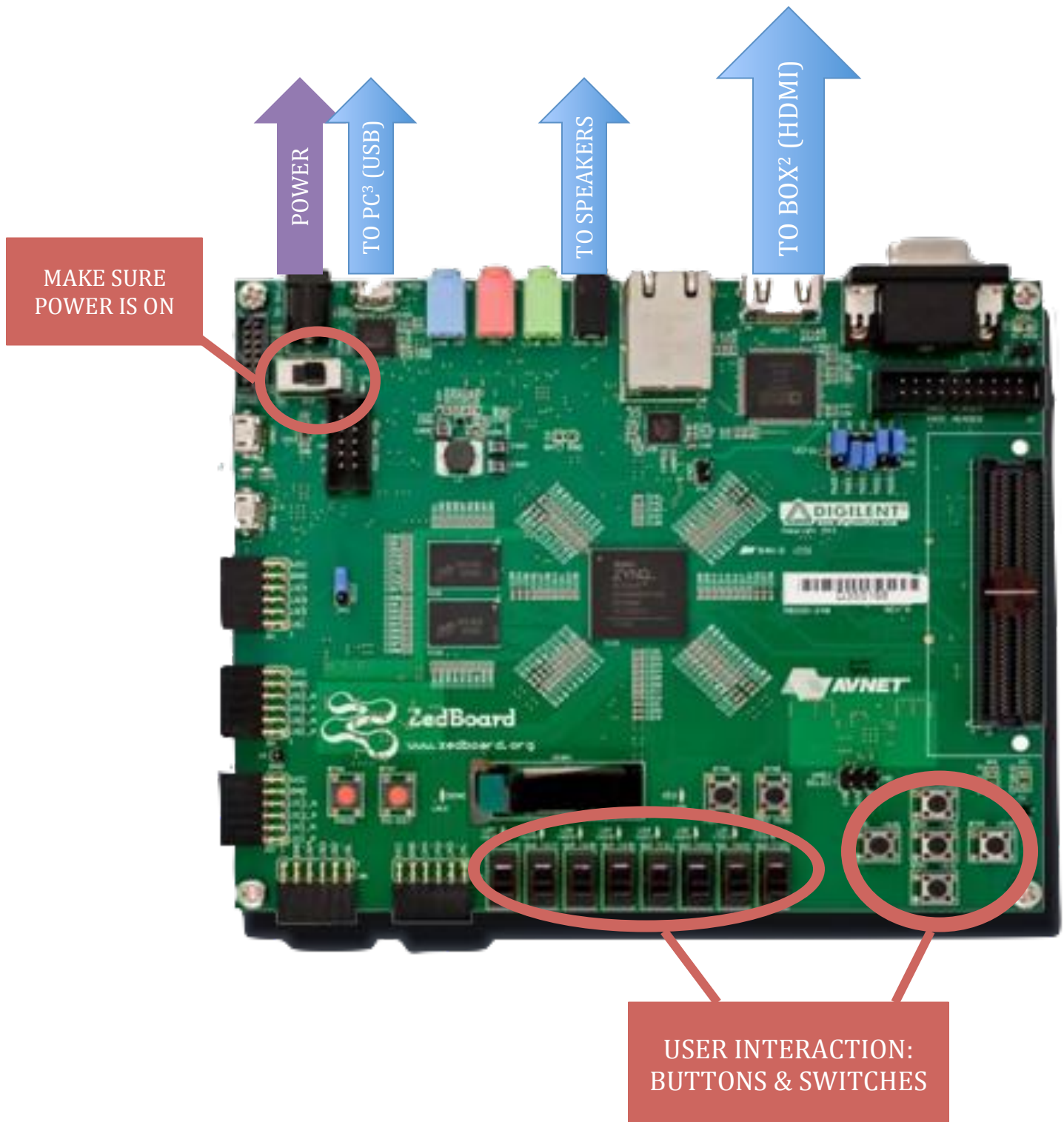


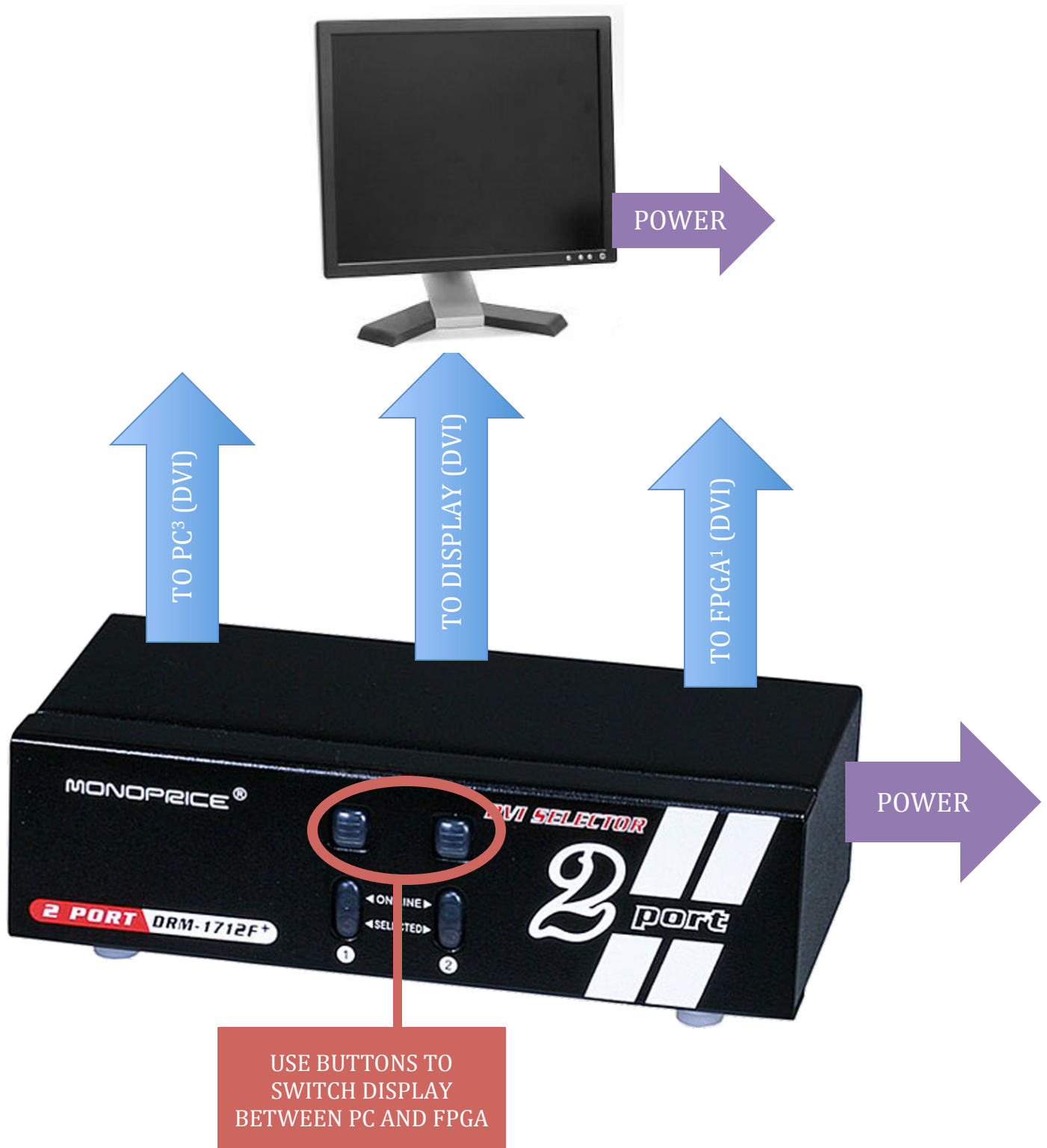
EE108 LAB SETUP



1. FPGA



2. DVI SWITCH BOX



3. PC

