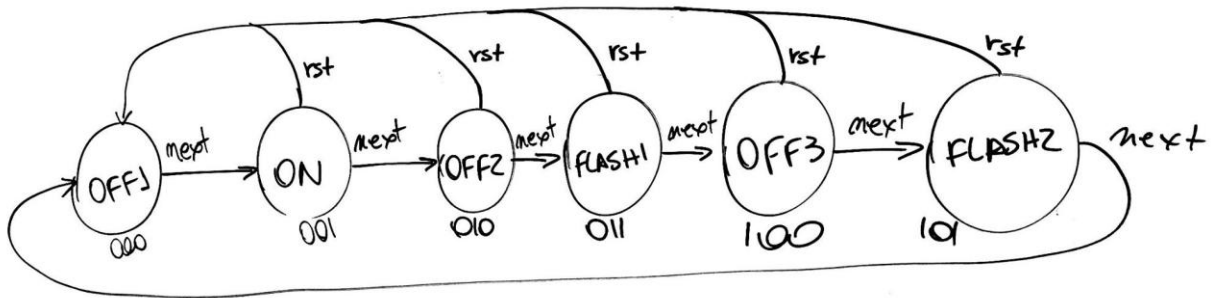


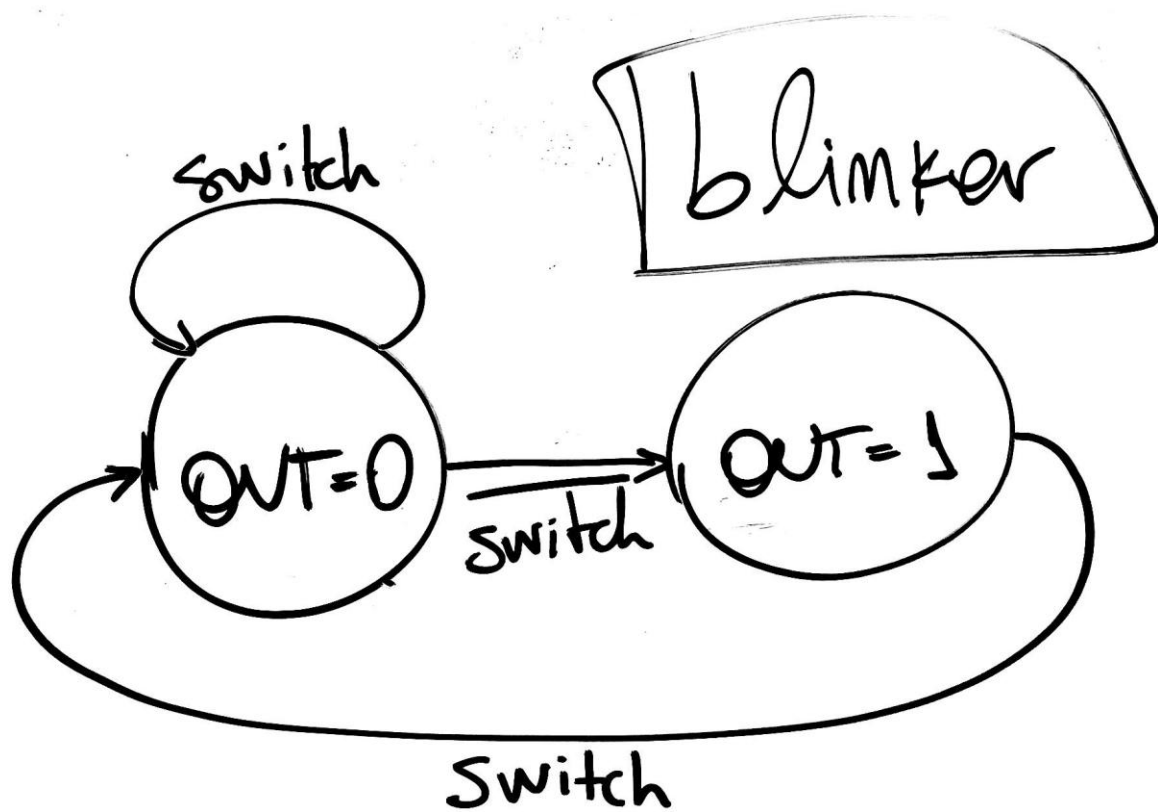
## Lab Schematic – Group 2

### a. State diagrams

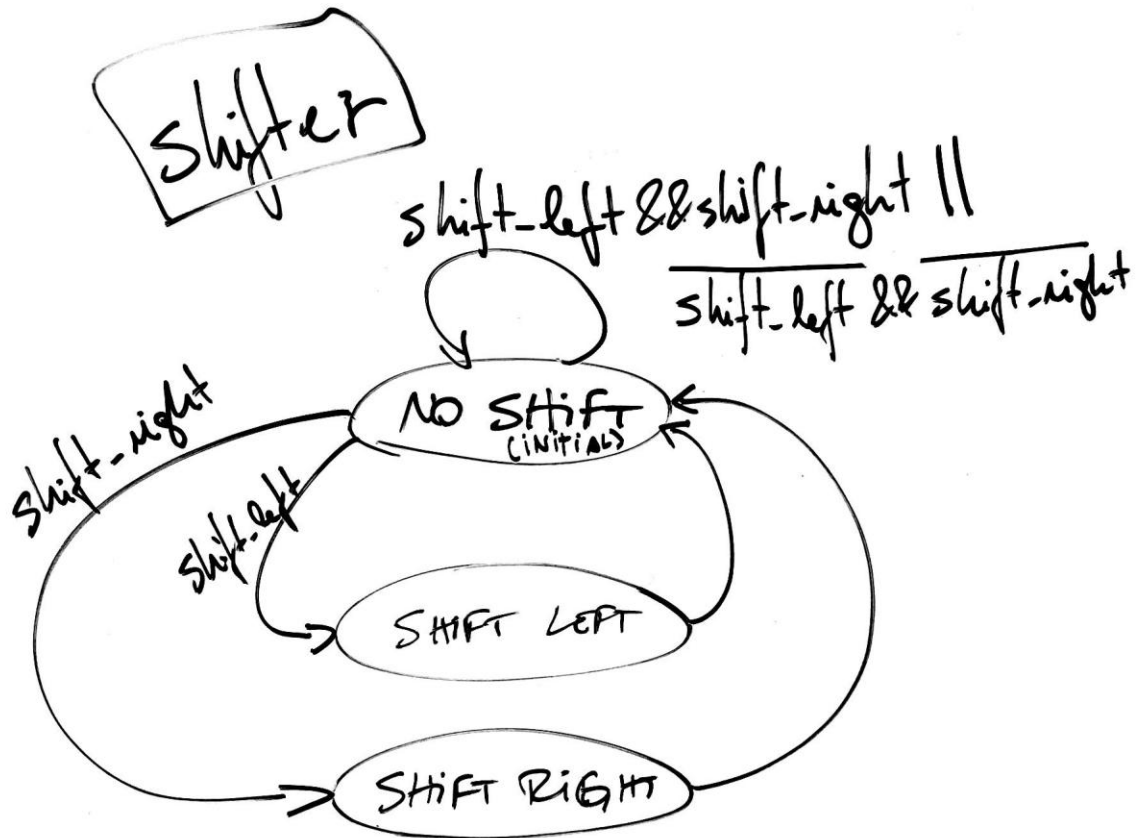
Master FSM:



Blinker:



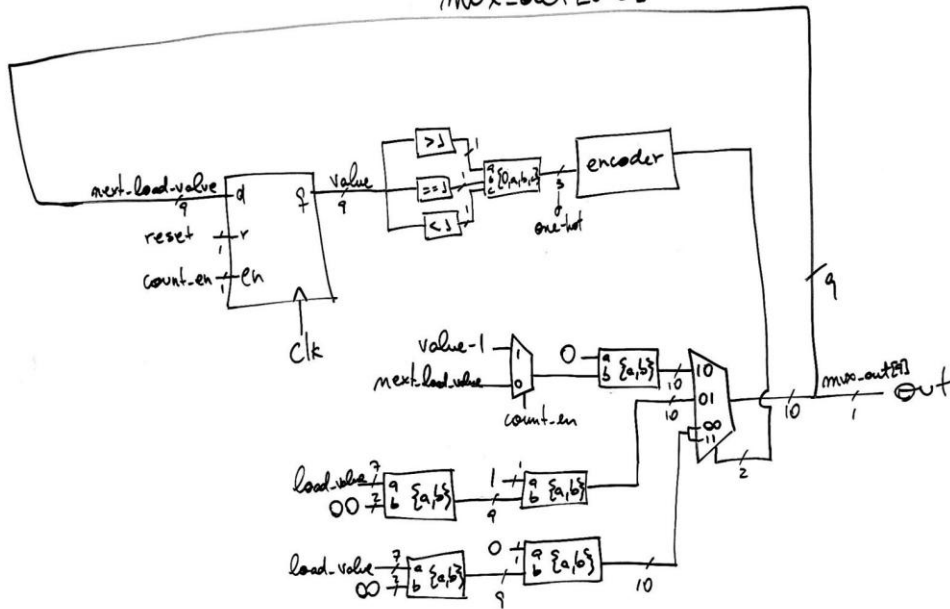
Shifter:



b. Block diagrams:

Timer:

timer - block diagram  
mux\_out[8:0]



Beat 32:

