Address	Label	Mnemonics H	excode	Bytes	M-Cycles	T-States
0000		LDA 201E	<b>3A</b>	3	4	13
0001			1E			
0002			20			
0003		MOV C,A	4F	1	1	4
0004		<b>MVI B,00</b>	06	2	2	7
0005			00			
0006		<b>LXI H,2000</b>	21	3	3	10
0007			00			
8000			20			
0009		DAD B	09	1	3	10
000A		DCX H	2B	1	1	6
000B		MOV D,H	<b>54</b>	1	1	4
000C		MOV E,L	<b>5D</b>	1	1	4
000D		LDA 201F	<b>3A</b>	3	4	13
000E			1F			
000F			20			
0010		MOV B,C	41	1	1	4
0011		MOV C,A	4F	1	1	4
0012		MOV A,B	<b>78</b>	1	1	4
0013		SUB C	91	1	1	4
0014		MOV C,A	4F	1	1	4
0015		MOV A,B	<b>78</b>	1	1	4
0016		<b>MVI B,00</b>	06	2	2	7
0017			00			
0018		DAD B	09	1	3	10
0019		MOV B,A	<b>47</b>	1	1	4
001A	LOOP	LDAX D	<b>1A</b>	1	2	7
001B		MOV M,A	<b>77</b>	1	2	7
001C		DCX D	<b>1B</b>	1	1	6
001D		DCX H	2B	1	1	6
001E		DCR B	05	1	1	4
001F		JNZ LOOP	C2	3	3	10
0020			<b>1A</b>			
0021			00			
0022		HLT	<b>76</b>	1	2	5