Address	Label	Mnemonics H	excode	Bytes	M-Cycles	T-States
0000		LDA 2500	3 <b>A</b>	3	4	13
0001			00			
0002			25			
0003		MOV B,A	47	1	1	4
0004		LXI H,2501	21	3	3	10
0005			01			
0006			<b>25</b>			
0007		<b>LXI D,2600</b>	11	3	3	10
0008			00			
0009			<b>26</b>			
000A	LOOP	MOV A,M	<b>7E</b>	1	2	7
000B		RLC	07	1	1	4
000C		<b>JNC SKIP</b>	D2	3	3	10
000D			<b>15</b>			
000E			00			
000F		<b>MOV A,M</b>	<b>7E</b>	1	2	7
0010		STAX D	12	1	2	7
0011		INX D	<b>13</b>	1	1	6
0012		JMP SKIP2	<b>C3</b>	3	3	10
0013			<b>1D</b>			
0014			00			
0015	SKIP	RRC	<b>OF</b>	1	1	4
0016		RRC	<b>OF</b>	1	1	4
0017		<b>JNC SKIP2</b>	D2	3	3	10
0018			<b>1D</b>			
0019			00			
001A		<b>MOV A,M</b>	<b>7E</b>	1	2	7
001B		STAX D	12	1	2	7
001C		INX D	<b>13</b>	1	1	6
001D	SKIP2	INX H	<b>23</b>	1	1	6
001E		DCR B	05	1	1	4
001F		JNZ LOOP	<b>C2</b>	3	3	10
0020			OA			
0021			00			
0022		HLT	<b>76</b>	1	2	5