McM51A Memory Map

McM51A MICRO MEMORY MAP

0FFFFH	0FFFFH

COMBINED PROGRAM
AND DATA MEMORY
AREA

8000H / 7FFFH — 8000H / 7FFFH

UNUSED PROGRAM MEMORY AREA	UNUSED DATA I/O

2000H / 1FFFH — 2000H / 1FFFH

| UNACCESSABLE | CHIP SELECT 1 |
| | CHIP SELECT 2 |

1800H / 17FFH

1000H / 0FFFH — 1000H / 0FFFH

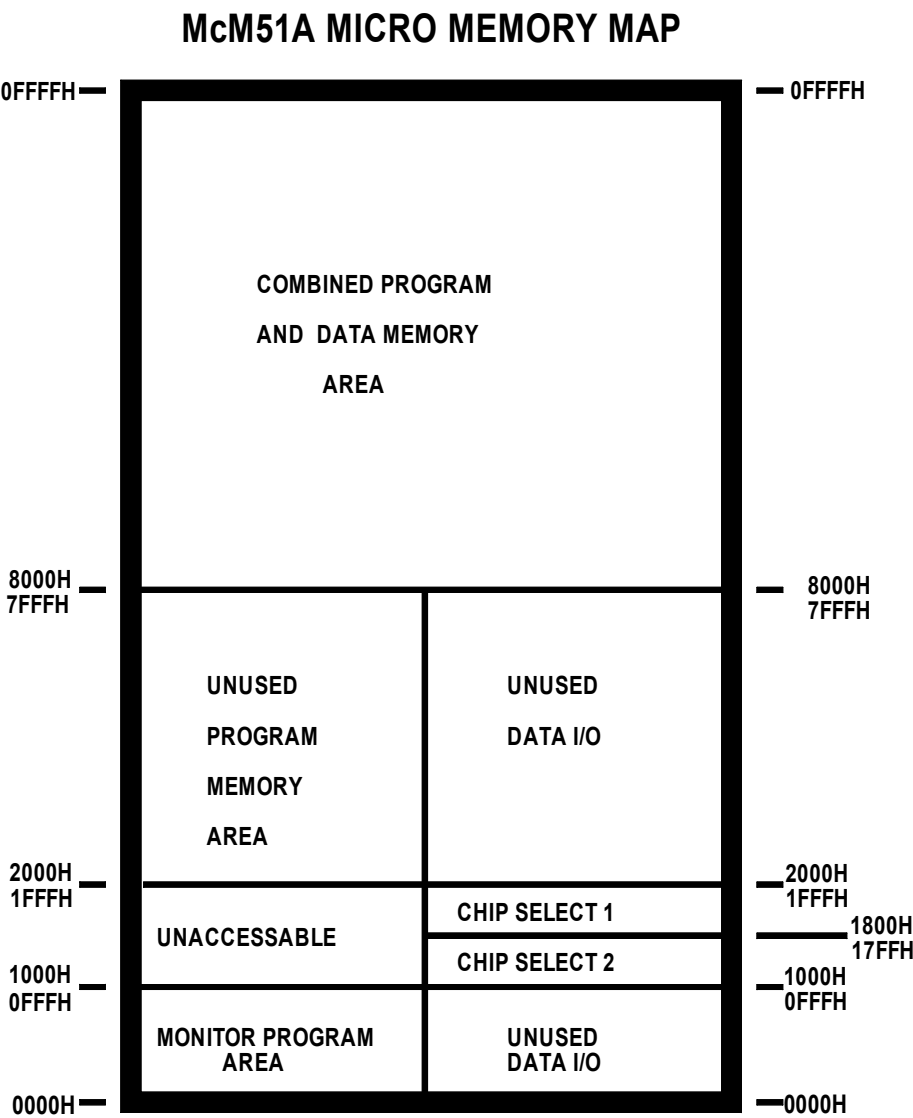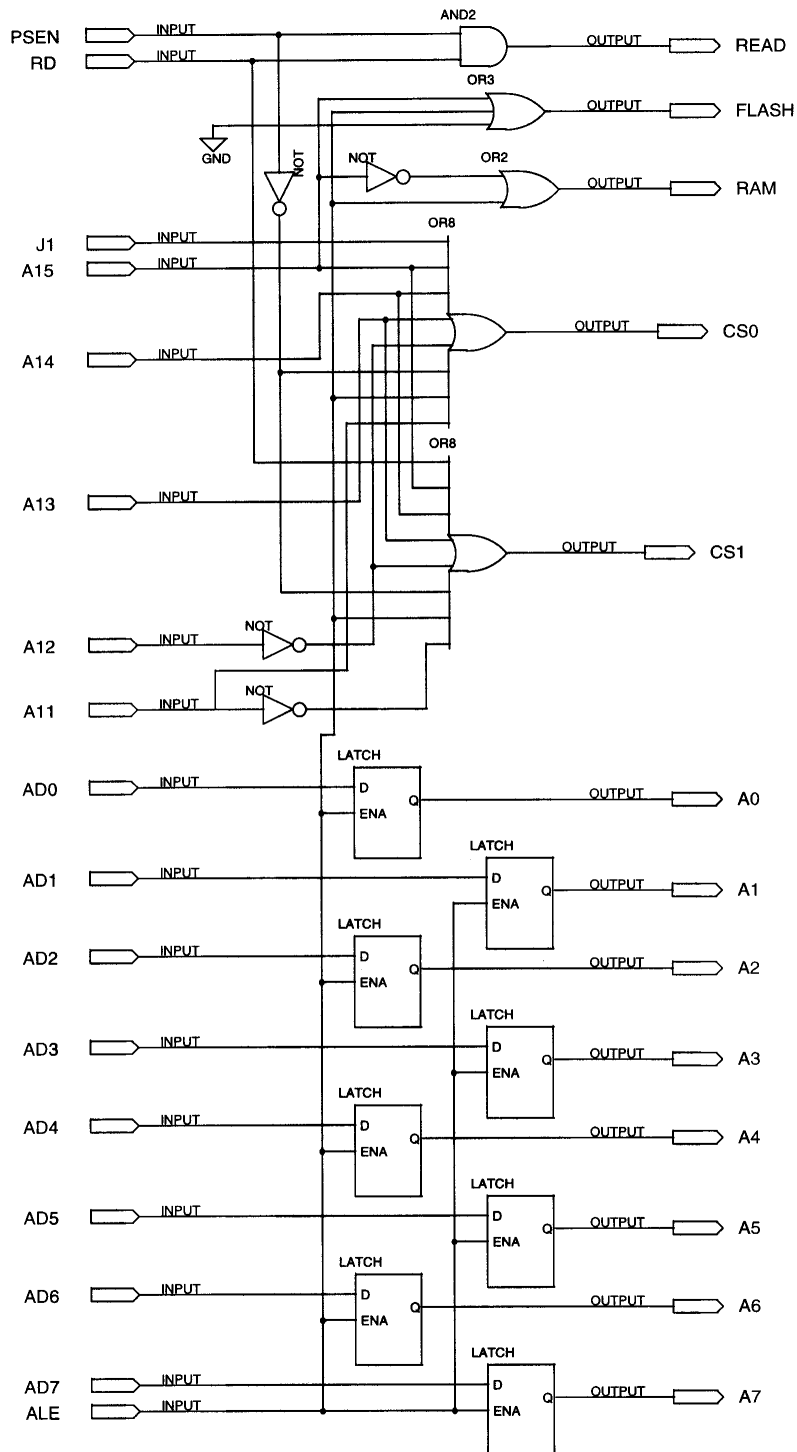| MONITOR PROGRAM AREA	UNUSED DATA I/O

0000H — 0000H

McM51A Decode Logic (programmed on Altera 3032 CPLD)

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