

Schemes	Bit Security	Devices	MHz	Cycles	Time( $\mu s$ )
R-LWEenc	128 (pre)	V6LX75T	313	6,300 / 2,800	20.1 / 9.1
R-LWEenc	128 (pre)	S6LX9	144	136,986	946
			189	66,338	351
BLISS	128 (pre)	S6LX25	129	16,210	126.6
			142	9,835	69.3
IBE	80 (pre)	S6LX25	174	13,958 / 9,530	80.2 / 54.8