	1	2	3	Proposed
Ancilla Inputs	1	0	0	0
Garbage Outputs	1	0	0	0
Quantum Cost	17n-12	26n-29	15n-9	13n-8
Delay Δ	10n	24n-27	13n-7	11n-4

1 is the design in

2 is the design in

3 is the design in