	Cray XK7 (Titan)	Cray XC30 (Edison)
Core	AMD Interlagos	Intel Ivy Bridge
Clock (GHz)	2.2	2.4
Private Cache (KB)	16 + 2048	32 + 256
DP GFlop/s/core	8.8	19.2
Socket Arch.	Opteron 6172	Xeon E5-2695 v2
Cores per socket	16	12
Threads per socket	16	24^1
L3 cache per socket	$2\times8~\mathrm{MB}$	30 MB
Node Arch.	Hypertransport	QPI (8 GT/s)
Sockets/node	1	2
STREAM BW^2	31 GB/s	$104 \; \mathrm{GB/s}$
Memory per node	$32~\mathrm{GB}$	$64~\mathrm{GB}$
Interconnect	Gemini (3D Torus)	Aries (Dragonfly)