

Test Cases	
Case A	all cores active with DRAM @ 1866MHz;
Case B	inactive cores: GPS, camera, rotator and JPEG, with DRAM @ 1700MHz.
Memory Controller	
Total entries	42
Transaction queues	5
DRAM	
Volume	2GB
Max I/O bus freq.	1866MHz
CL-tRCD-tRP (cycles)	36-34-34
tWTR-tRTP-tWR (cycles)	19-14-34
tRRD-tFAW (cycles)	19-75
Channels-Ranks-Banks	2-2-8