Block type & repetition	Input channel	Output channel	Stride	g_1	g_2
Regular 3x3 conv	3	32	2		
BatchNorm+ReLU					
CLC block	32	64	1	16	2
CLC block	64	128	2	32	2
CLC block$\times a$	128	128	1	64	2
CLC block	128	256	2	64	2
CLC block$\times b$	256	256	1	128	2
CLC block	256	512	2	128	2
CLC block$\times c$	512	512	1	256	2
CLC block	512	1024	2	256	2
CLC block$\times d$	1024	1024	1	512	2
Average Pooling					
FC layer	1024	1000			