Example	PTAs	loc./PTA	X	P	$ V_0 $	tiles	states	trans.	Time
SR-latch	3	[3, 8]	3	3	1331	6	5	4	0.3
Flip-flop	5	[4, 16]	5	2	644	8	15	14	3
And-Or	3	[4, 8]	4	6	75600	4	64	72	118
Valmem Latch	7	[2, 5]	8	4	73062	5	21	20	96.3
CSMA/CD	3	[3, 8]	3	3	2000	140	349	545	269
RCP	5	[6, 11]	6	3	186050	19	5688	9312	7018
SPSMALL ₁	10	[3, 8]	10	2	3149	259	60	61	1194