Architecture	Clock Frequency	Power	Area	Speed
One Loki core	$\sim 450 \mathrm{MHz}$	$\sim 0.01 \mathrm{W}$	$0.5\mathrm{mm}^2$	64.3  cycles/byte =
One Loki core + specialised L1 cache	$\sim 450 \mathrm{MHz}$	$\sim \! 0.01 \mathrm{W}$	$0.5\mathrm{mm}^2$	34.8  cycles/byte =
Eight Loki cores	$\sim 450 \mathrm{MHz}$	$\sim \! 0.06 \mathrm{W}$	$1\mathrm{mm}^2$	9.8  cycles/byte = -
Eight Loki cores + specialised L1 cache	$\sim 450 \mathrm{MHz}$	$\sim \! 0.06 \mathrm{W}$	$1\mathrm{mm}^2$	6.5  cycles/byte = 6.5  cycles/byte
One specialised Loki tile (as described)	$\sim 450 \mathrm{MHz}$	$\sim \! 0.06 \mathrm{W}$	$1\mathrm{mm}^2$	5.1  cycles/byte = 100
Sixteen specialised Loki tiles	$\sim 450 \mathrm{MHz}$	$\sim \! 1 \mathrm{W}$	$16\mathrm{mm}^2$	0.32  cycles/byte = 1
One core of Intel® Core <sup>TM</sup> i7-980X	3.33 GHz	33.5W	$60 \mathrm{mm}^2$	1.3  cycles/byte = 2
ARM9TDMI	$150 \mathrm{\ MHz}$	0.12W	$6.55 \mathrm{mm}^2$	45  cycles/byte = 3
ATI Mobility Radeon <sup>TM</sup> HD 5650	$650~\mathrm{MHz}$	19W	$104 \mathrm{mm}^2$	1.9  cycles/byte = 3
GeForce 8800 GTS	$1.625~\mathrm{GHz}$	135W	$484 \mathrm{mm}^2$	17.1  cycles/byte =