

Device	Coordinator latency	Acceptor latency	Throughput	Clock rate
Digilent NetFPGA SUME: 4x10G port switch	1.280 $\mu s$	1.515 $\mu s$	60M packets/s	200MHz
Alpha Data ADM-PCIE-KU3: 2x40G NIC	1.024 $\mu s$	1.212 $\mu s$	60M packets/s	250MHz
Xilinx VCU109: 4x100G line card	0.867 $\mu s$	1.023 $\mu s$	150M packets/s	300MHz