Scenario Parameters	Application 1	Application 2
Channel Coding Rate	1/3	
QAM Modulation	QPSK	16-QAM
Frequency Bandwidth	5MHz	$10 \mathrm{MHz}$
Data quantization	10 bits	14 bits
Frame Type	1 OFDM symbol pilot	
	every 10 OFDM symbols of data	
FPGA	Xilinx Virtex-6 LX240T	
Clock Frequency	100 MHz	