Shared Address Loads	
pgas_ldbu	Load Byte Unsigned (8 bits)
pgas_ldwu	Load Word Unsigned (16 bits)
pgas_ldl	Load Long Unsigned (32 bits)
pgas_ldq	Load Quad Unsigned (64 bits)
pgas_lds	Load S_float (32 bits, float)
pgas_ldt	Load T_float (64 bits, double)
Shared Address Stores	
pgas_stb	Store Byte Unsigned (8 bits)
pgas_stw	Store Word Unsigned (16 bits)
pgas_stl	Store Long Unsigned (32 bits)
pgas_stq	Store Quad Unsigned (64 bits)
pgas_sts	Store S_float (32 bits, float)
pgas_stt	Store T_float (64 bits, double)
Shared Address Incrementations	
pgas_inc_imm	Address increment, immediate
pgas_inc_reg	Address increment, register
Initialization	
set_threads	Initialize the 'threads' register
set_base_address	Set the base address
	look-up table