

	Stage No. G	Memory depth	Combinational ALUTs	Registers	Memory bits	Clock frequency	Throughput (info bits)	Require at a BE
Code 1-S	422	512	106288	68609	4402268	100 MHz	0.5 Gbps	3.42 dB
Code 2-S	512	512	104938	68634	4402268	100 MHz	0.5 Gbps	3.40 dB
Code 3-S	1024	1024	73066	50087	5829352	100 MHz	0.5 Gbps	3.48 dB
Code 4-S	1024	1024	62823	43745	4844140	100 MHz	0.5 Gbps	3.60 dB
Code 1-P	422	512	175420	105427	17558528	100 MHz	2.0 Gbps	3.42 dB
Code 2-P	512	512	170102	105505	17558528	100 MHz	2.0 Gbps	3.40 dB
Code 3-P	1024	1024	134102	86654	23283712	100 MHz	2.0 Gbps	3.48 dB
Code 4-P	1024	1024	120804	80342	19348480	100 MHz	2.0 Gbps	3.60 dB
Wang	—	—	28229	26926	5800000	190 MHz	0.2 Gbps	4.40 dB