Irreversible Specification		Reversible Specification					
Input	*Output*	*Input*	A_1	A_2	*Output*	G_1	G_2
00	00	00	0	0	00	0	0
01	10	01	0	0	10	0	0
10	10	10	0	0	10	1	1
11	01	11	0	0	01	0	1