

L2 Cache Technology	Area ( $mm^2$ )	RL ( $ns$ )	WL ( $ns$ )	RE ( $nJ$ )	WE ( $nJ$ )	LP ( $mW$ )
512KB SRAM	1.410	1.277	1.277	0.293	0.293	1753.444
1MB eDRAM	0.745	1.072	1.022	0.289	0.424	337.329
1MB STT-RAM	0.526	1.340	10.218	0.280	0.654	212.022
512KB LRSC	0.243	1.260	2.153	0.233	0.269	104.797
512KB HRSC	0.357	1.261	10.153	0.233	0.601	114.915

RL: Read Latency,

WL: Write Latency, RE: Read Energy, WE: Write Energy, LP: Leakage Power