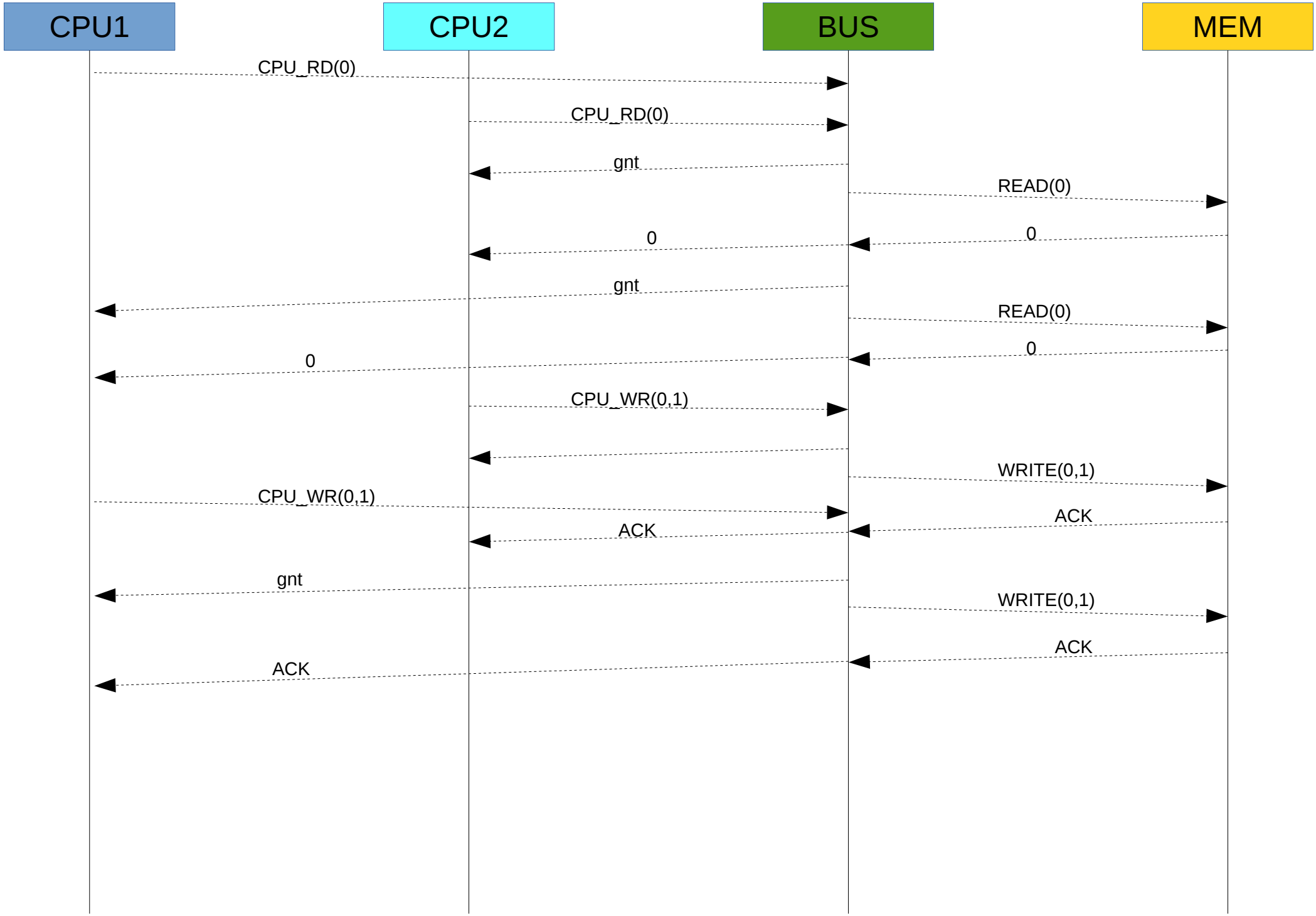
CPU1
CPU2
BUS
MEM
CPU_RD(0)
CPU_RD(0)
gnt
READ(0)
0
0
gnt
READ(0)
0
0
CPU_WR(0,1)
WRITE(0,1)
CPU_WR(0,1)
ACK
ACK
gnt
WRITE(0,1)
ACK
ACK