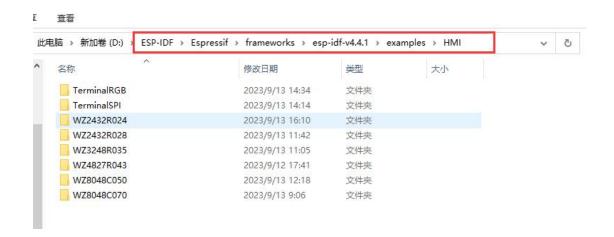
TerminalRGB Use a tutorial

Place the downloaded project under the IDF directory (as shown below):



Let's first learn at the use of commands:

cd xxx---Moving to the xxx directory, xxx represents the name of the directory, for example: cd example

idf.py set-target esp32s3---Set the target chip for example: esp32s3

idf.py fullclean---Delete the entire build directory, including all the CMake configuration output files.

idf.py clean---It removes the building output files from the building directory and cleans up the entire project..

idf.py menuconfig---Configure the target chip

idf.py build---Compile a private code base

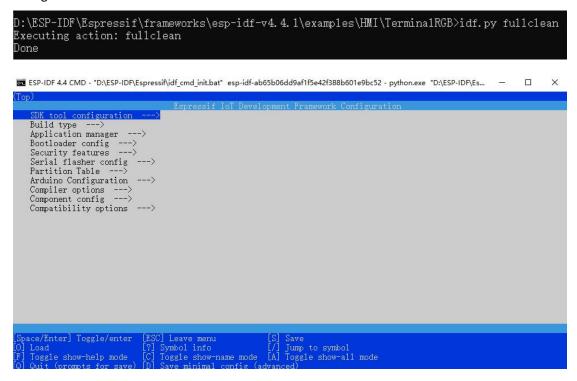
idf.py -p com3 flash---Download the program to the target chip

idf.py -p com3 flash monitor---Once compile burn and open monitoring

Now we open the terminal and go to the TerminalRGB project catalog

```
D:\ESP-IDF\Espressif\frameworks\esp-idf-v4.4.1\examples\HMI>cd Termina1RGB
D:\ESP-IDF\Espressif\frameworks\esp-idf-v4.4.1\examples\HMI\Termina1RGB>
```

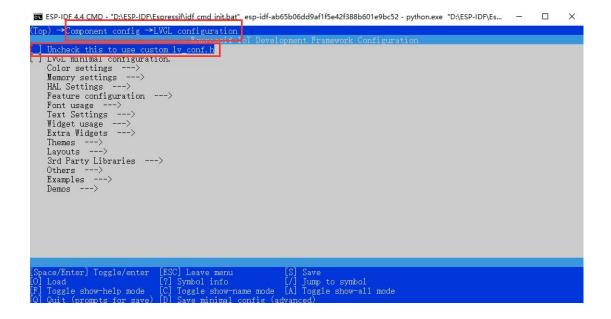
Now we have to empty the project idf.py fullclean once first, and then go into the configuration



Now modify the options by following the following steps:

```
| Space/Enter | Toggle/enter | ESC | Leave menu | Space/Enter | Toggle show-help mode | Toggle show-nimal config (advanced) | Toggle show-all mode | T
```

| ESP-IDF 4.4 CMD - "D:\ESP-IDF | <u>\Espressif\idf cmd init.b</u> at" esp-idf-ab65b06dd9af1f5e42f388b601e9bc52 - python.exe "D:\ESP-IDF\Es − □ × |
|--|---|
| p) = Component config = | |
| CPU frequency (160 MHz | Espressi IoT Development Framework Configuration |
| Cache config > | CDT DAT |
| Support for external, SPI RAM config | |
| Use TRAX tracing leatu Enable Ultra Low Power | |
| Make exception and pan | ic handlers JTAG/OCD aware |
| Hardware brownout dete Brownout voltage 1 | |
| Timers used for gettim | eofday function (RTC and high-resolution timer)> |
| | rnal 150kHz RC oscillator)> r RTC_SLOW_CLK calibration |
| 00) Extra delay in deep | sleep wake stub (in us) |
| No Binary Blobs Place RTC_DATA_ATTR an | d RTC_RODATA_ATTR variables into RTC fast memory segment |
| Use fixed static RAM s | |
| | |
| | |
| | |
| | |
| (2) | [page] . [page] |
| ace/Enter] Toggle/enter Load | ESC] Leave menu [S] Save [?] Symbol info [/] Jump to symbol |
| Toggle show-help mode | [C] Toggle show-name mode [A] Toggle show-all mode |
| Quit (prompts for save |) [D] Save minimal config (advanced) |
| | |
| ECD IDE 4.4 CMD "DAECD IDE | 75 |
| | *Espressif\idf_cmd_init.bat" esp-idf-ab65b06dd9af1f5e42f388b601e9bc52 - python.exe "D:\ESP-IDF\Es □ X |
| p) → Component config - | →ESP32S3-Specific →Support for external, SPI-connected RAM →SPI RAM config |
| p) → Component config - Mode (QUAD/OCT) of SPI | PESP32S3-Specific →Support for external, SPI-connected RAM →SPI RAM config RAM chip in use (Octal Mode PSRAM)> |
| Mode (QUAD/OCT) of SPI Type of SPIRAM chip in PSRAM Clock and CS IO | →ESP32S3-Specific →Support for external, SPI-connected RAM →SPI RAM config RAM chip in use (Octal Mode PSRAM)> a use (Auto-detect)> for ESP32S3> |
| p) → Component config - Mode (QUAD/OCT) of SPI Type of SPIRAM chip in PSRAM Clock and CS IO Cache fetch instructio | PESP32S3-Specific →Support for external, SPI-connected RAM →SPI RAM config RAM chip in use (Octal Mode PSRAM)> a use (Auto-detect)> for ESP32S3> ms from SPI RAM |
| Mode (QUAD/OCT) of SPI lype of SPIKAM chip in PSRAM Clock and CS IO Cache fetch instructio Cache load read only d Set RAM clock speed (8 | PESP32S3-Specific →Support for external, SPI-connected RAM →SPI RAM config RAM chip in use (Octal Mode PSRAM)> in use (Auto-detect)> for ESP32S3> ms from SPI RAM lata from SPI RAM lounder of the content |
| p) → Component config - Mode (QUAD/OCT) of SPI Hype of SPIRAM chip in PSRAM Clock and CS IO Cache fetch instructio Cache load read only of Set RAM clock speed (8 Initialize SPI RAM dur | PESP32S3-Specific →Support for external, SPI-connected RAM →SPI RAM config RAM chip in use (Octal Mode PSRAM)> a use (Auto-detect)> for ESP32S3> ons from SPI RAM lata from SPI RA |
| Mode (QUAD/OCT) of SPI Type of SPIKAM chip in PSRAM Clock and CS IO Cache fetch instructio Cache load read only d Set RAM clock speed (8 Initialize SPI KAM when SPI RAM access method | *ESP32S3-Specific ->Support for external, SPI-connected RAM ->SPI RAM config RAM chip in use (Octal Mode PSRAM)> in use (Auto-detect)> for ESP32S3> ms from SPI RAM lata from SPI RAM lata from SPI RAM lower startup ing startup not found (Make RAM allocatable using malloc() as well)> |
| P) Component config Those (QUAD/OCT) of SPI Mode (QUAD/OCT) of SPI Type of SPIKAM chip in PSRAM Clock and CS IO Cache fetch instructio Cache load read only of Set RAM clock speed (8 Initialize SPI RAM dur Ignore PSRAM when SPI RAM access method Run memory test on SPI | *ESP32S3-Specific ->Support for external, SPI-connected RAM ->SPI RAM config RAM chip in use (Octal Mode PSRAM)> in use (Auto-detect)> for ESP32S3> ms from SPI RAM lata from SPI RAM lata from SPI RAM lower startup ing startup not found (Make RAM allocatable using malloc() as well)> |
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| Mode (QUAD/OCT) of SPI Type of SPIRAM chip in PSRAM Clock and CS IO Cache Inad read only of Set RAM clock speed (8 Initialize SPI RAM when SPI RAM access method Run memory test on SPI Styl Maximum malloc() s Try to allocate memori | ESP32S3-Specific →Support for external, SPI-connected RAM →SPI RAM config RAM chip in use (Octal Mode PSRAM)> for ESP32S3> ons from SPI RAM lata from SPI RAM lata from SPI RAM lata from SPI RAM lata from SPI RAM (OMHz clock speed)> ling startup not found (Make RAM allocatable using malloc() as well)> RAM initialization size, in bytes, to always put in internal memory |
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| Mode (QUAD/OCT) of SPI Type of SPIKAM chip in PSRAM Clock and CS IO Cache fetch instructio Cache land read only of Set RAM clock speed (8 Initialize SPI KAM when SPI RAM access method Run memory test on SPI 384) Maximum malloc() s Try to allocate memori | RAM chip in use (Octal Mode PSRAM)> in use (Auto-detect)> for ESP32S3> ms from SPI RAM (OMHz clock speed)> ing startup not found (Make RAM allocatable using malloc() as well)> RAM initialization size, in bytes, to always put in internal memory es of WiFi and LWIF in SPIRAM firstly. If failed, allocate internal memory |
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| Mode (QUAD/OCT) of SPI Mode (QUAD/OCT) of SPI Iype of SPIRAM chip in PSRAM Clock and CS IO Cache lead read only d Set RAM clock speed (8 Initialize SPI RAM dur Ignore PSRAM when SPI RAM access method Run memory test on SPI 384) Maximum malloc() s Try to allocate memori (768) Reserve this amoun | RAM chip in use (Octal Mode PSRAM)> n use (Auto-detect)> for ESP32S3> ms from SPI RAM NOMHz clock speed)> Ting startup not found (Make RAM allocatable using malloc() as well)> RAM initialization size, in bytes, to always put in internal memory es of WiFi and LWIP in SPIRAM firstly. If failed, allocate internal memory at of bytes for data that specifically needs to be in DMA or internal memory |
| Mode (QUAD/OCT) of SPI Type of SPIKAM chip in PSRAM Clock and CS IO Cache fetch instructio Cache land read only of Set RAM clock speed (8 Initialize SPI KAM when SPI RAM access method Run memory test on SPI 384) Maximum malloc() s Try to allocate memori | RAM chip in use (Octal Mode PSRAM)> n use (Auto-detect)> for ESP32S3> ms from SPI RAM NOMHz clock speed)> Ting startup not found (Make RAM allocatable using malloc() as well)> RAM initialization size, in bytes, to always put in internal memory es of WiFi and LWIP in SPIRAM firstly. If failed, allocate internal memory at of bytes for data that specifically needs to be in DMA or internal memory |



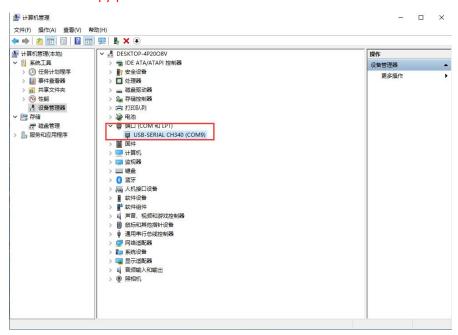
Save the exit after setup, and then execute the idf.py build

```
emis/1vgl-3 D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/lwip D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/mbedtls D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/mbedtls D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/mbedtls D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/mbedtls D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/mbedtls D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/mbedtls D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/newlib D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/newlib D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/newlib D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/newlib D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/newlib D:/ESP-IDF/Espressif/frameworks/esp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/upp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/components/psp-idf-v4.4.1/compone
```

Waiting for the compilation to complete, the following figure interface appears:

```
Text | Strict |
```

Perform the idf.py-p com9 flash



success!

```
Writing at 0x00072d8e... (34 %)
Writing at 0x00072d8e... (38 %)
Writing at 0x00098b7d... (38 %)
Writing at 0x00098b7d... (42 %)
Writing at 0x000ad811... (46 %)
Writing at 0x000ad811... (46 %)
Writing at 0x000ad813... (50 %)
Writing at 0x000b9990... (57 %)
Writing at 0x000b9990... (61 %)
Writing at 0x000cb9... (65 %)
Writing at 0x000cb9... (65 %)
Writing at 0x000cb345... (65 %)
Writing at 0x000cb79... (69 %)
Writing at 0x000d83d3... (30 %)
Writing at 0x000d83d5... (80 %)
Writing at 0x000d83d5... (80 %)
Writing at 0x000d805f... (84 %)
Writing at 0x000d805f... (88 %)
Writing at 0x000d805f... (88 %)
Writing at 0x000d805f... (92 %)
Writing at 0x000f46b0... (92 %)
Writing at 0x000f46b0... (92 %)
Writing at 0x000f40b0... (100 %)
Wrote 984224 bytes (410199 compressed) at 0x00010000 in 11.7 seconds (effective 673.4 kbit/s)...
Hash of data verified.
Compressed 3072 bytes to 105...
Writing at 0x00008000... (100 %)
Wrote 984228 bytes (105 compressed) at 0x00008000 in 0.1 seconds (effective 314.3 kbit/s)...
Hash of data verified.
Leaving...
Hash of data verified.
Leaving...
Hard resetting via RTS pin...
Done
```