Work Log

Saturday April 10

* Met with team to begin document for initial design to complete milestone 1.
* Decided to use accumulator

Sunday April 11, 2021

* Met with team to continue work for design
* Came up with idea to have several “accumulator registers” that can be switched between to save work without including more temporary registers that continue to hold data

Monday April 12,2021

* Team meet to finish document
* Wanting to ask if we can change the size of instructions if they are j or other instructions
  + Will read opcode, if opcode first bit (most significant) is a 1 then it reads the full opcode and then reads 16 bits for an address to jump to
  + If opcode leads with 0, will read 10 bits for memory or immediate
* Made decision to just use 10 bit immediate and just sign extend them