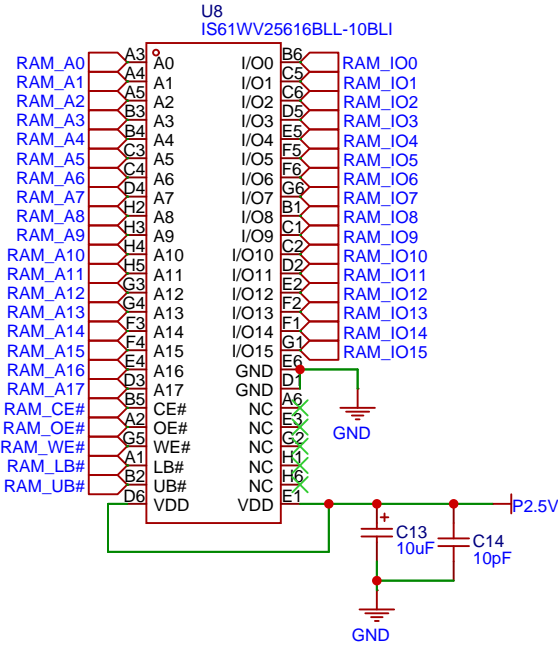
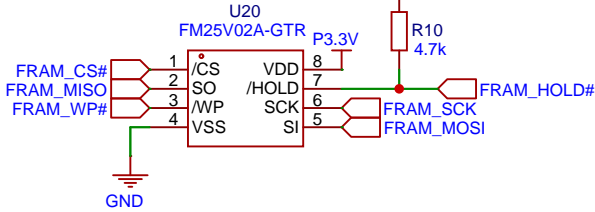


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| TITLE: | | REV: 1.0 | |
| AdaCoreFPGADevBoard_RHIT_2025-03-15 | | | |
| EasyEDA | Company: | Rose Hulman Institute of Technology | Sheet: 1/1 |
| | Date: | 2024-09-25 | Drawn By: hsinr |

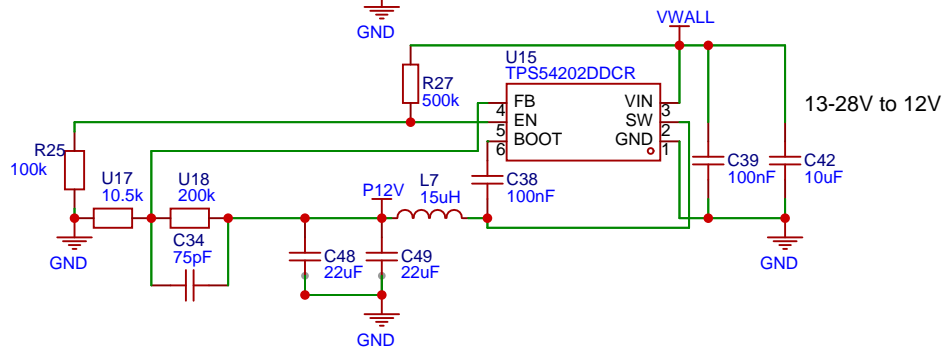
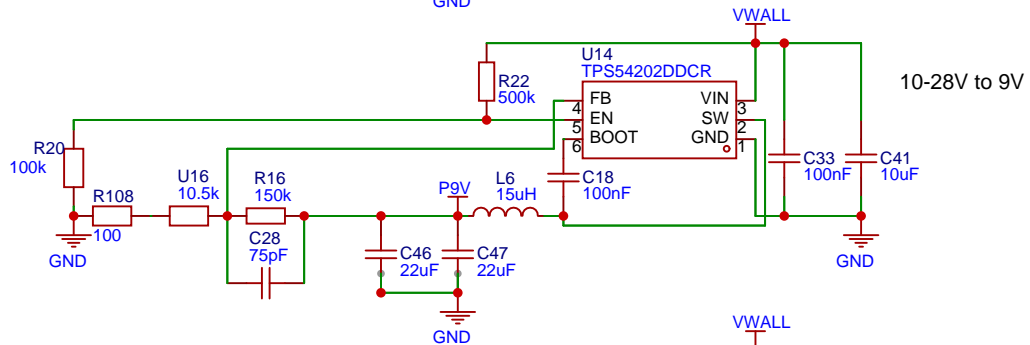
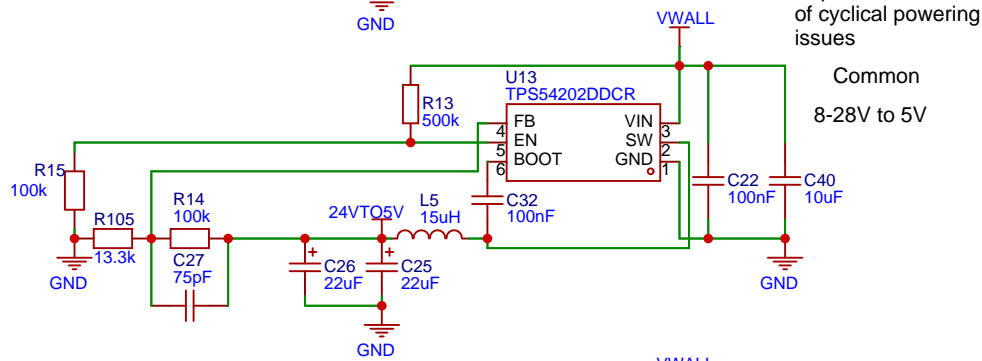
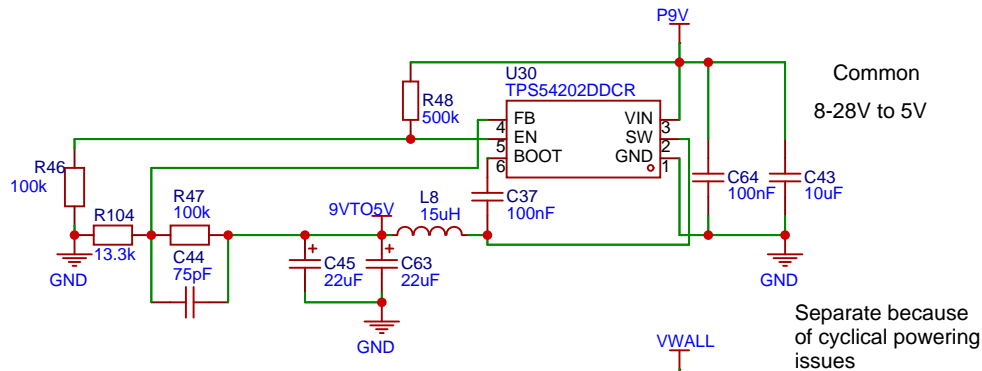
SRAM 1Mbyte



FRAM 256kbits SPI







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| TITLE: AdaCoreFPGADevBoard_RHIT_2025-03-15 | | REV: 1.0 |
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| | Date: 2024-09-25 | Drawn By: hsinr |