Device Utilization Summary				[-]
Slice Logic Utilization	**Used**	**Available**	**Utilization**	**Note(s)**
Number of Slice Registers	16	18,224	1%	
Number used as Flip Flops	16			
Number used as Latches	0			
Number used as Latch-thrus	0			
Number used as AND/OR logics	0			
Number of Slice LUTs	16	9,112	1%	
Number used as logic	0	9,112	0%	
Number used as Memory	16	2,176	1%	
Number used as Dual Port RAM	0			
Number used as Single Port RAM	0			
Number used as Shift Register	16			
Number using O6 output only	16			
Number using O5 output only	0			
Number using O5 and O6	0			
Number of occupied Slices	5	2,278	1%	
Number of MUXCYs used	0	4,556	0%	
Number of LUT Flip Flop pairs used	16			
Number with an unused Flip Flop	0	16	0%	
Number with an unused LUT	0	16	0%	
Number of fully used LUT-FF pairs	16	16	100%	
Number of unique control sets	2			
Number of slice register sites lost to control set restrictions	0	18,224	0%	