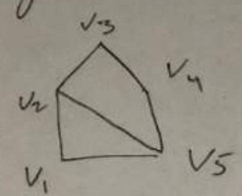
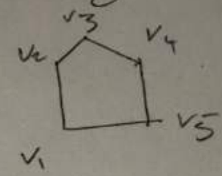
G52

We generalise the G455 case to the following

$\Gamma =$ 

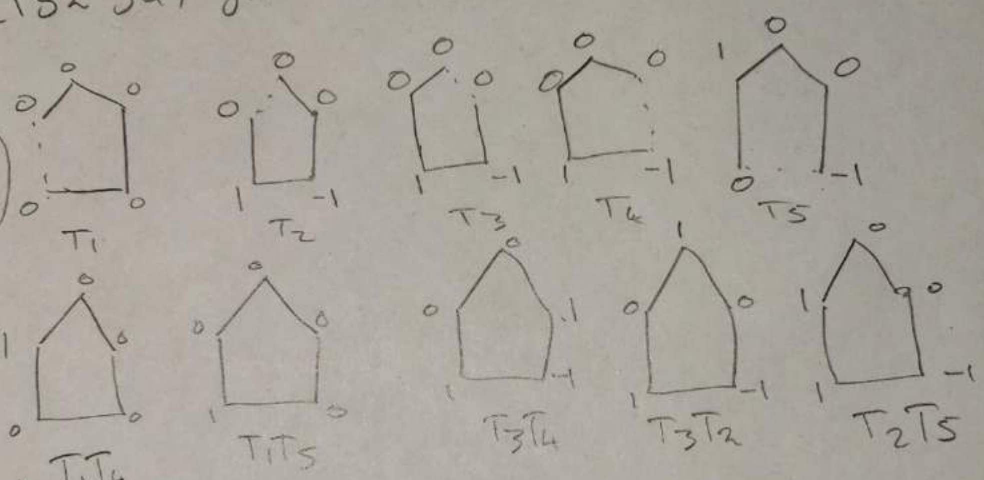
where we use that I_5 is 

Test example before running all 5-cycles

Using cycle-to-assignment for

cycle (15234) get

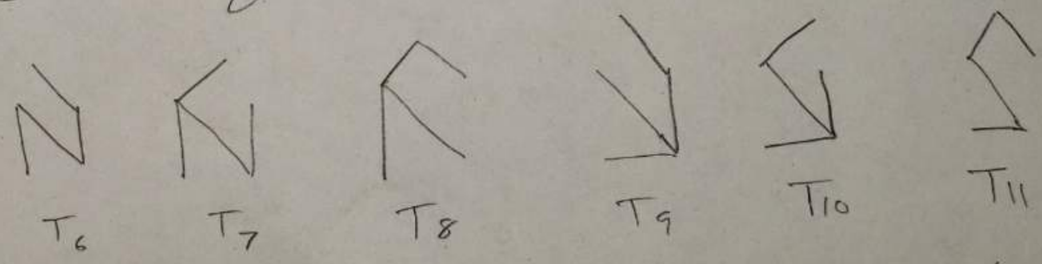
(ordering determined by cycle-to-assignt)



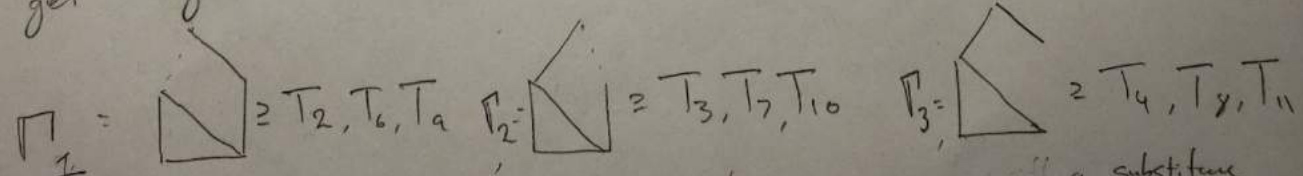
Assignt which chip-add to these: T_1T_4

There are 11 spanning trees for  the remaining 6 are

(I choose this ordering)



To get assignments on $T_6, \dots, T_{11}$ we take some subgraphs Γ_i containing them

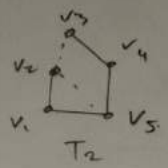


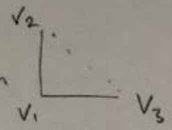
Each Γ_i has a unique tree of I_5 which we use as a substitute for $\underline{0}$ in mod-cycle-to-assignts for the subcycle  after removing the rational tail part.

Correction Procedure

• n-cycle (15234)

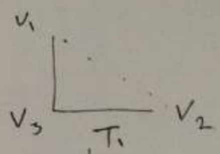
• For data_1 (similar for data_2, data_3)

• For Γ_1 take 

have red_ass on 

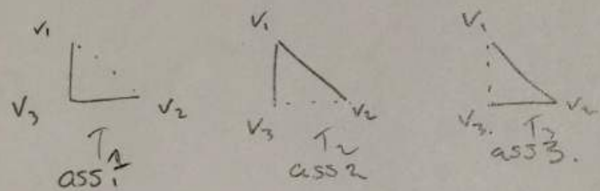
after remove_rt_GS2

↓ rotate by 1 red_ass



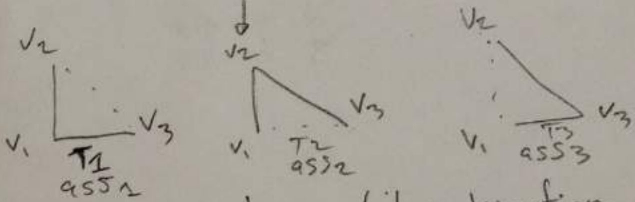
red_ass on it

↓ mod_cycle_to_assignments



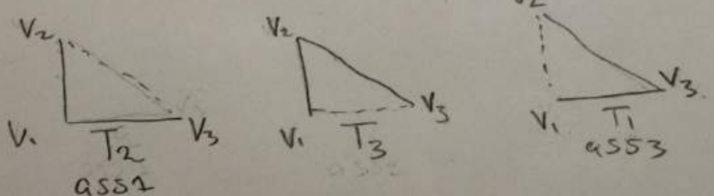
(mod-cycle-to-assignment labeling of vertices)

↓ opp-rotate by 1



(In labelling which is compatible with Γ)

↓ map lst in bijection.



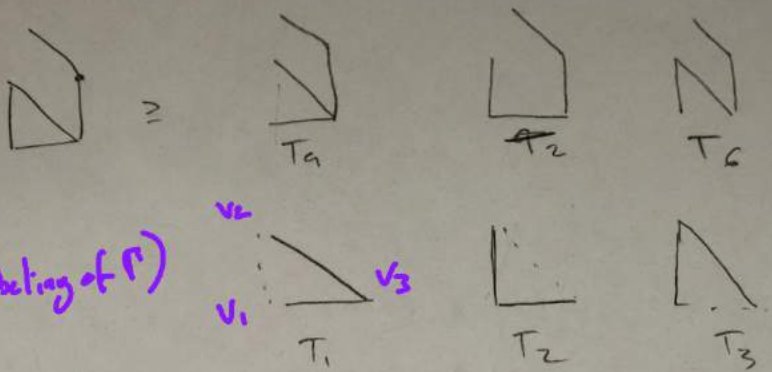
(Spanning trees of Γ_3 now match to mapping in ass_lst)

No dye to spy dues.

hence ~~[2, 1, 1]~~
[2, 3, 1]

*

add-bach-rt/
ass-1st bijection of spanning trees.

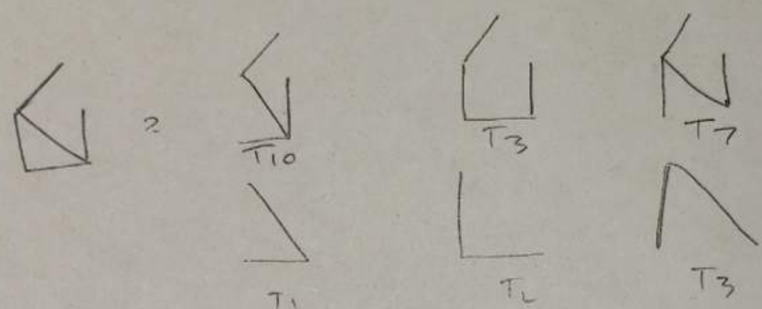
Γ_1 = 

in Γ

(labeling of Γ)

in I_3

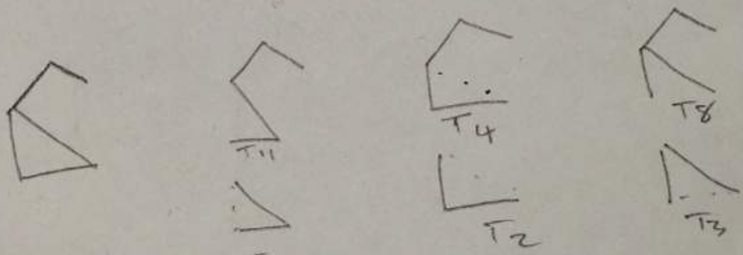
so we map
$1 \longmapsto 9$
$2 \longmapsto 2$
$3 \longmapsto 6$.

similarly Γ_2 = 

Γ

in I_3

so
$1 \longmapsto 10$
$2 \longmapsto 3$
$3 \longmapsto 7$.

and Γ_3 = 

so
$1 \longmapsto 11$
$2 \longmapsto 4$
$3 \longmapsto 8$

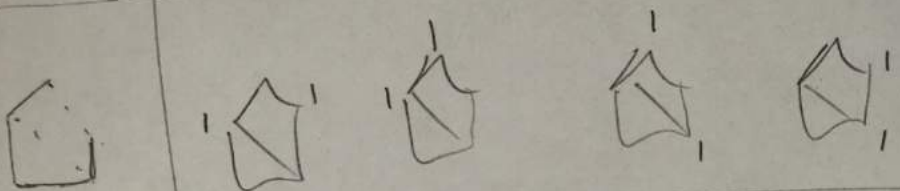
Breaks

T_1

0	2	0	0	0
1	1	0	0	0
0	1	0	0	1
1	0	0	0	1

T_2

0	2	0	0	0
0	1	1	0	0
0	1	0	0	1
0	0	1	0	1

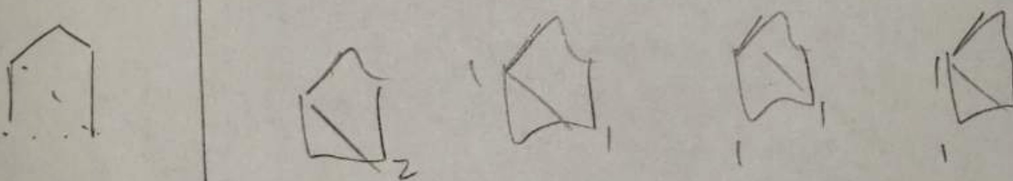


T_3

0	1	0	1	0
0	1	1	0	0
0	0	1	0	1
0	0	0	1	1

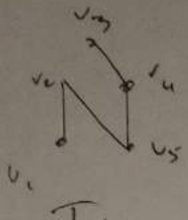
T_4

0	0	0	0	2
0	1	0	0	1
0	0	0	1	1
0	1	0	1	0

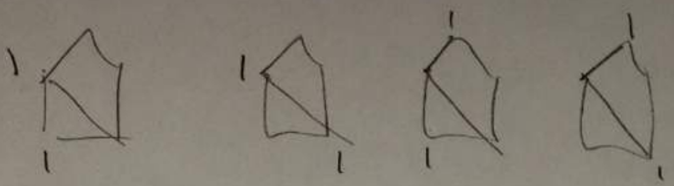


T_5

0	0	0	0	2
0	1	0	0	1
1	0	0	0	1
1	1	0	0	0.

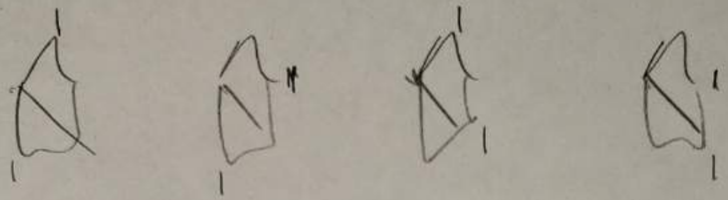


T6



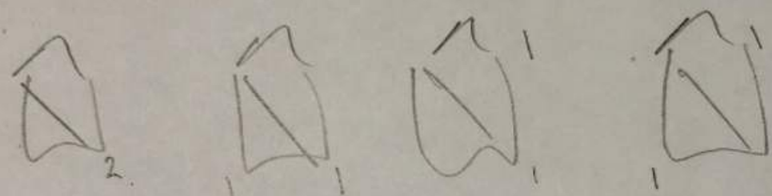
1	1	0	0	0
0	1	0	0	1
1	0	1	0	0
0	0	1	0	1

T7

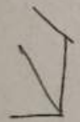


1	0	1	0	0
1	0	0	1	0
0	0	1	0	1
0	0	0	1	1

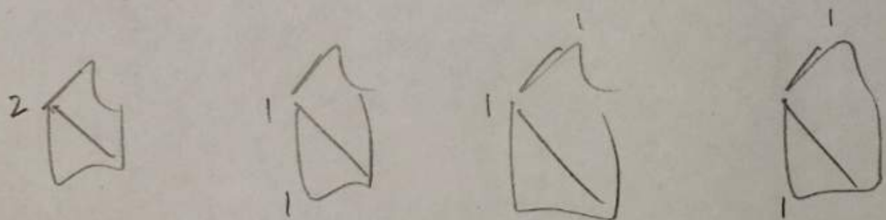
T8



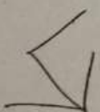
0	0	0	0	2
1	0	0	0	1
0	0	0	1	1
1	0	0	1	0



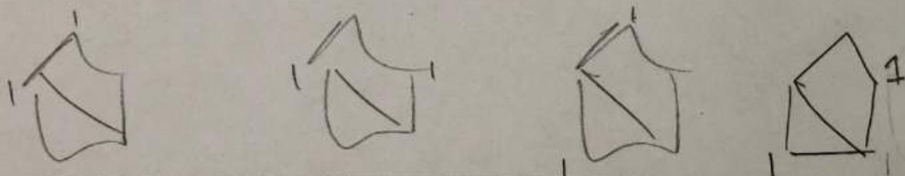
T9



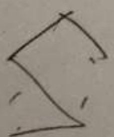
0	2	0	0	0
1	1	0	0	0
0	1	1	0	0
1	0	1	0	0



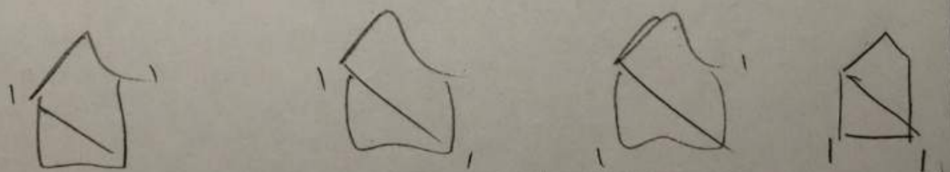
T10



0	1	1	0	0
0	1	0	1	0
1	0	1	0	0
1	0	0	1	0



T11



0	1	0	1	0
0	1	0	0	1
1	0	0	1	0
1	0	0	0	1