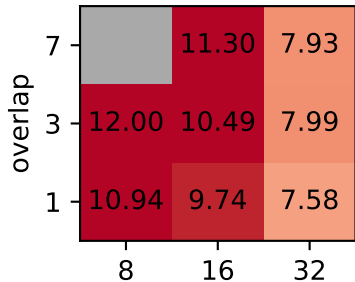
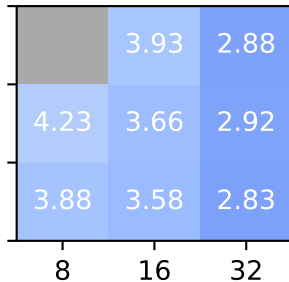


Average t-test for Circuit [σ]

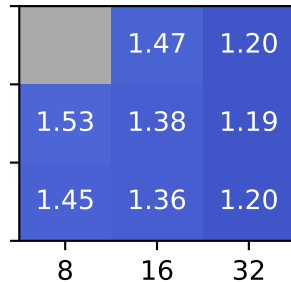
Tile Frequency



2 Tile Patterns



4 Tile Patterns



10.0

7.5

5.0

2.5