

Date	Topic	Reading	Assignments
1/26 1/27	Computer Organization Review	Kann(1) - Chapter 1 Kann(2) – Chapter 2	
2/2 2/3	Computer Organization Review (Continued)		Homework #1 – Pretest (Part I)
2/9 2/10	IC's State Programming	Kann(2) – Chapters 6, 7, 8, 9, and 10	Homework #2 – Pretest (Part II)
2/16 2/17	Multiplication and Division Hardware	Patterson – Chapter 3 Kann(3) – Entire Text	Homework #3 – IC's and State Programming
2/23 2/24	One Address CPU	Kann(3) – Entire Text	Homework #4 – Multiplication and Division Hardware
3/2 3/3	One Address CPU		Homework #5 – One Address CPU Assignment #1 Project topics due
3/9 3/10	MIPS Assembly and MIPS Machine Code	Kann(1) Chapters 2, 3, and 4	Homework #5 – One Address CPU Assignment #2
3/16 3/17	MIPS CPU data path and control unit	Patterson, Chapter 4	Homework #6 – MIPS Assembly and Machine Code
3/23 3/24	Break		
3/30 3/31	Instruction level parallelism: pipelining	Patterson, Chapter 5	Homework #7 – Control Path
4/6 4/7	The Memory Subsystem	Patterson, Chapter 6	Homework #8 – Pipelining
4/13 4/14	Parallel Computing	Material provided by instructor	Homework #9 – Memory Subsystem
4/20 4/21	The I/O subsystem	Material provided by instructor	Homework #10 – Parallel Computing
12/7	Presentation of projects and review		Homework #11 I/O subsystem
5/3	Final	Final	Final

5/4			
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Kann(1) - <https://cupola.gettysburg.edu/oer/2/>

Kann(2) - <https://cupola.gettysburg.edu/oer/1/>

Kann(3) - <https://cupola.gettysburg.edu/oer/3/>