

**Product Specification** \_

## NHD-2.4-240320AF-CSXP

## **TFT Liquid Crystal Display**

**NHD-** Newhaven Display

**2.4**- 2.4" Diagonal

**240320-** 240xRGBx320 Pixels

**AF-** Model

**C-** Built-in Controller

**S-** High Brightness, White LED Backlight

X- TFT

**P-** IPS, Wide Temperature







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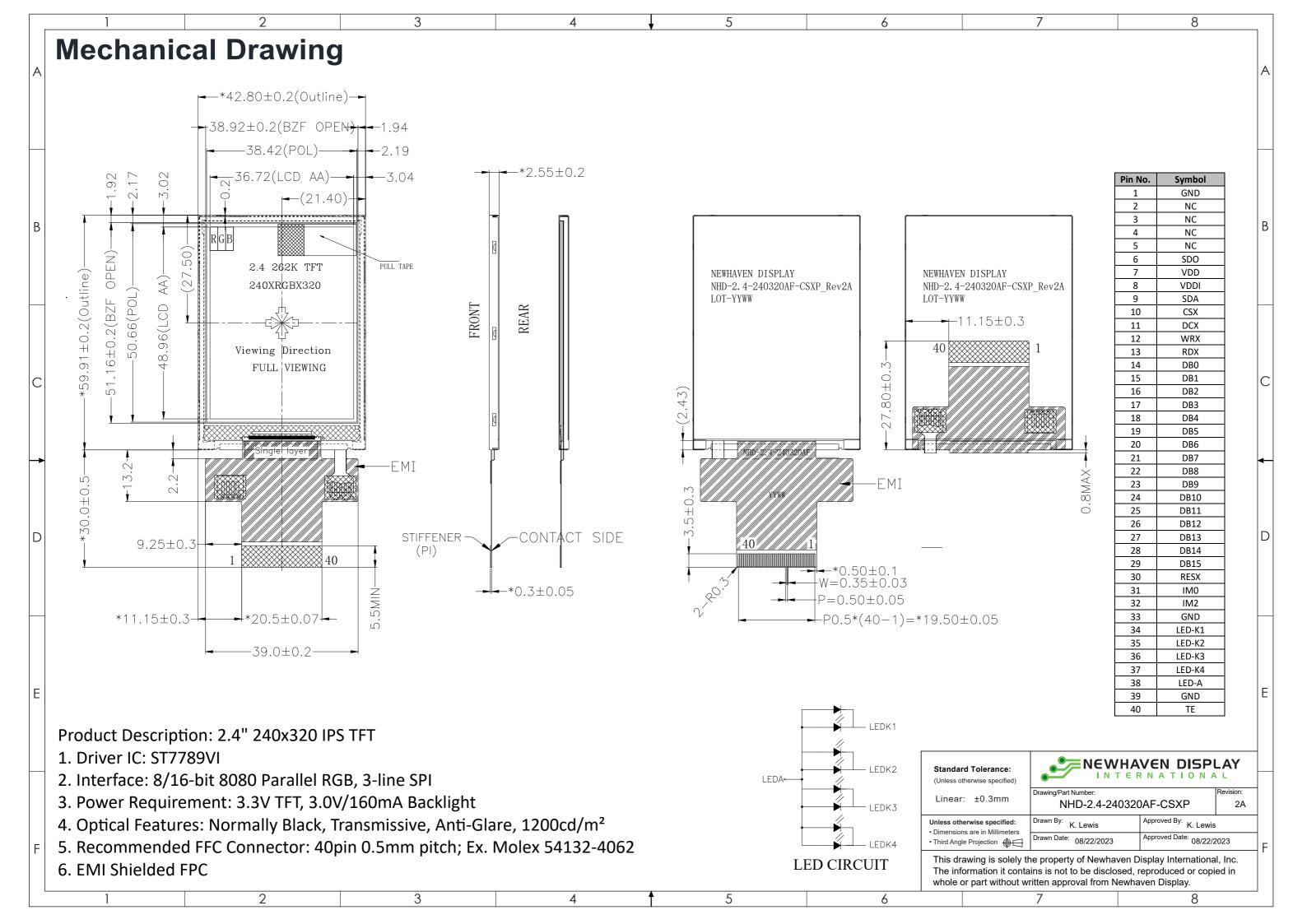
#### **Additional Resources**

- Support Forum: <a href="https://support.newhavendisplay.com/hc/en-us/community/topics">https://support.newhavendisplay.com/hc/en-us/community/topics</a>
- ➤ **GitHub:** <a href="https://github.com/newhavendisplay">https://github.com/newhavendisplay</a>
- **Example Code:** <a href="https://support.newhavendisplay.com/hc/en-us/categories/4409527834135-Example-Code/">https://support.newhavendisplay.com/hc/en-us/categories/4409527834135-Example-Code/</a>
- **Knowledge Center:** <a href="https://www.newhavendisplay.com/knowledge">https://www.newhavendisplay.com/knowledge</a> center.html
- ➤ Quality Center: <a href="https://www.newhavendisplay.com/quality">https://www.newhavendisplay.com/quality</a> center.html
- Precautions for using LCDs/LCMs: <a href="https://www.newhavendisplay.com/specs/precautions.pdf">https://www.newhavendisplay.com/specs/precautions.pdf</a>
- ➤ Warranty / Terms & Conditions: <a href="https://www.newhavendisplay.com/terms.html">https://www.newhavendisplay.com/terms.html</a>



# **Document Revision History**

Revision	Date	Description	Changed By
-	09/19/2023	Initial Release	KL





# **Pin Description**

Pin No.	Symbol	External Connection	Function Description
1	GND	Power Supply	Ground
2	NC	-	No Connect
3	NC	-	No Connect
4	NC	-	No Connect
5	NC	-	No Connect
6	SDO	MPU	Serial Data Out
7	VDD	Power Supply	Supply Voltage for LCD (3.3V)
8	VDDI	Power Supply	Supply Voltage for Logic
9	SDA	MPU	Serial Data In
10	CSX	MPU	Active LOW Chip Select signal
11	DCX	MPU	Data / Command selection: '1' = Data; '0' = Command
12	WRX	MPU	Active LOW Write signal
13	RDX	MPU	Active LOW Read signal
14	DB0	MPU	Bi-directional data bus
15	DB1	MPU	
16	DB2	MPU	8-bit: use DB8-DB15
17	DB3	MPU	16-bit: use DB0-DB15
18	DB4	MPU	
19	DB5	MPU	
20	DB6	MPU	
21	DB7	MPU	
22	DB8	MPU	
23	DB9	MPU	
24	DB10	MPU	
25	DB11	MPU	
26	DB12	MPU	
27	DB13	MPU	
28	DB14	MPU	
29	DB15	MPU	
30	RESX	MPU	Active LOW Reset signal
31	IM0	MPU	Interface Mode Select
32	IM2	MPU	Interface Mode Select
33	GND	Power Supply	Ground
34	LED-K1	Power Supply	
35	LED-K2	Power Supply	Backlight Cathode (Ground)
36	LED-K3	Power Supply	
37	LED-K4	Power Supply	
38	LED-A	Power Supply	Backlight Anode (3.0V/160mA)
39	GND	Power Supply	Ground
40	TE	MPU	Tearing Effect Output

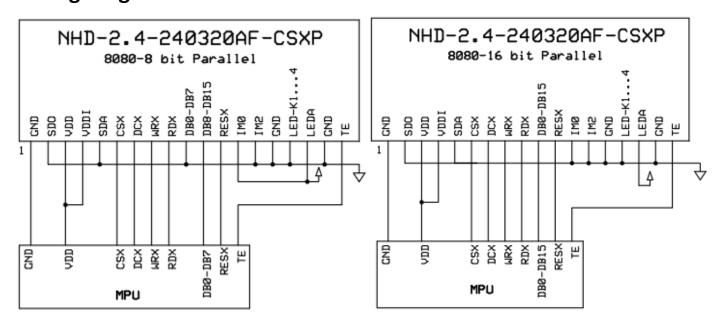
Recommended LCD connector: 40-pin, 0.5mm pitch FFC connector Molex P/N: 54132-4062 or similar

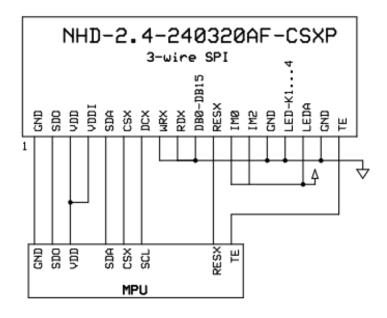
## **Interface Selection**

Pin	8-bit 8080-II	16-bit 8080-II	3-wire
Name	Parallel	Parallel	SPI
IM0	1	0	1
IM2	0	0	1



### **Wiring Diagram**







### **Electrical Characteristics**

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	T <sub>OP</sub>	Absolute Max	-20	-	+70	°C
Storage Temperature Range	T <sub>ST</sub>	Absolute Max	-30	-	+80	°C
Supply Voltage for LCD	$V_{DD}$	-	2.4	3.3	3.6	V
Supply Voltage for Logic	$V_{DDI}$	-	1.65	1.8	3.6	V
Supply Current	I <sub>DD</sub>	$V_{DD} = 3.3V$	5	10	15	mA
"H" Level input	ViH	-	0.7 * V <sub>DDI</sub>	-	$V_{DDI}$	V
"L" Level input	VIL	-	GND	-	0.3 * V <sub>DDI</sub>	V
"H" Level output	V <sub>OH</sub>	-	0.8 * V <sub>DDI</sub>	-	$V_{DDI}$	V
"L" Level output	Vol	-	GND	•	0.2 * V <sub>DDI</sub>	V
Backlight Supply Current	I <sub>LED</sub>	-	80	160	200	mA
Backlight Supply Voltage	V <sub>LED</sub>	I <sub>LED</sub> = 160mA	2.7	3.0	3.4	V
Backlight Lifetime*	-	$I_{LED} = 160 \text{mA}$ $T_{OP} = 25^{\circ}\text{C}$	30,000	50,000	-	Hrs.

<sup>\*</sup>Backlight Lifetime is rated as Hours until **half-brightness**, under normal operating conditions. The LED of the backlight is driven by current drain; drive voltage is for reference only. Drive voltage must be selected to ensure backlight current drain is below MAX level stated.

## **Optical Characteristics**

	Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Outimed	Тор	Тор			70	80	-	0
Optimal	Bottom		φΥ-	CD > 10	70	80	-	0
Viewing	Left		θХ-	CR ≥ 10	70	80	-	0
Angles	Right		θХ-		70	80	-	0
Contrast Rati	io		CR	-	1000	1500	-	-
Luminance	Luminance		Lv	I <sub>LED</sub> = 160mA	1000	1200	-	cd/m²
Response Tir	Response Time		T <sub>R</sub> + T <sub>F</sub>	T <sub>OP</sub> = 25°C	-	35	45	ms
		Red	X <sub>R</sub>	-	0.585	0.635	0.685	-
			YR	-	0.291	0.341	0.391	-
		6	X <sub>G</sub>	-	0.276	0.326	0.376	ı
Chuamad	.::	Green	Y <sub>G</sub>	-	0.569	0.619	0.669	-
Chromat	Chromaticity		X <sub>B</sub>	-	0.105	0.155	0.205	-
		Blue	Y <sub>B</sub>	-	0.026	0.076	0.126	-
		White	Xw	-	0.283	0.313	0.343	-
		vvnite	Yw	-	0.299	0.329	0.359	-

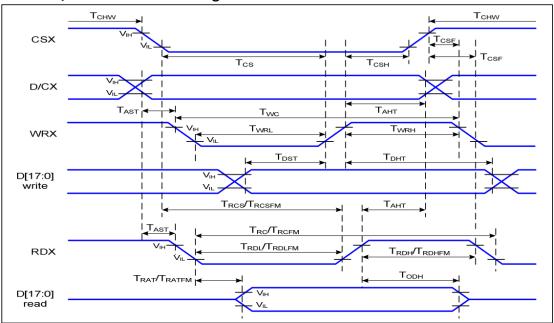
#### **Controller Information**

Built-in ST7789VI Controller: https://support.newhavendisplay.com/hc/en-us/articles/10814990300823-ST7789VI



## **Timing Characteristics for TFT**

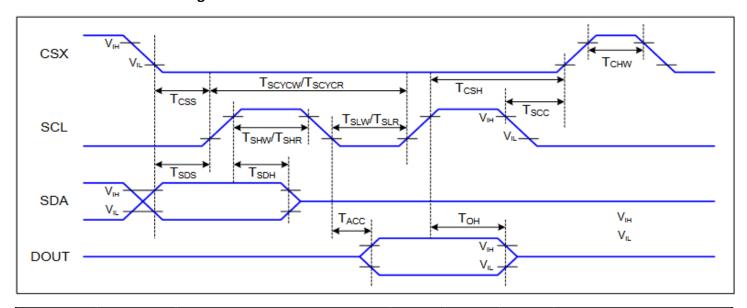
#### Parallel 8/16-bit Interface Timing Characteristics



Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T <sub>AST</sub>	Address setup time	0		ns	
DIOX	T <sub>AHT</sub>	Address hold time (Write/Read)	10		ns	-
	T <sub>CHW</sub>	Chip select "H" pulse width	0		ns	
	T <sub>CS</sub>	Chip select setup time (Write)	15		ns	
CSX	T <sub>RCS</sub>	Chip select setup time (Read ID)	45		ns	
COA	T <sub>RCSFM</sub>	Chip select setup time (Read FM)	355		ns	-
	T <sub>CSF</sub>	Chip select wait time (Write/Read)	10		ns	
	T <sub>CSH</sub>	Chip select hold time	10		ns	
	T <sub>wc</sub>	Write cycle	66		ns	
WRX	T <sub>WRH</sub>	Control pulse "H" duration	15		ns	
	T <sub>WRL</sub>	Control pulse "L" duration	15		ns	
	T <sub>RC</sub>	Read cycle (ID)	160		ns	
RDX (ID)	T <sub>RDH</sub>	Control pulse "H" duration (ID)	90		ns	When read ID data
	$T_RDL$	Control pulse "L" duration (ID)	45		ns	
RDX	T <sub>RCFM</sub>	Read cycle (FM)	450		ns	When read from
(FM)	T <sub>RDHFM</sub>	Control pulse "H" duration (FM)	90		ns	frame memory
(1 101)	T <sub>RDLFM</sub>	Control pulse "L" duration (FM)	355		ns	maine memory
D[17:0]	T <sub>DST</sub>	Data setup time	10		ns	For CL=30pF
	$T_DHT$	Data hold time	10		ns	
	T <sub>RAT</sub>	Read access time (ID)		40	ns	
	T <sub>RATFM</sub>	Read access time (FM)		340	ns	
	T <sub>ODH</sub>	Output disable time	20	80	ns	



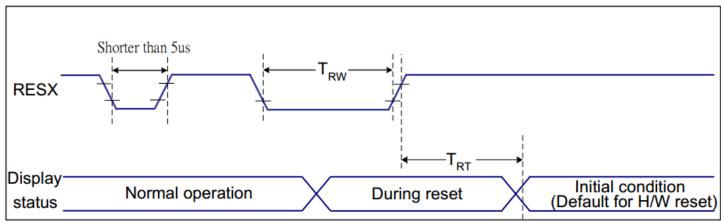
#### **3-line Serial interface Timing Characteristics**



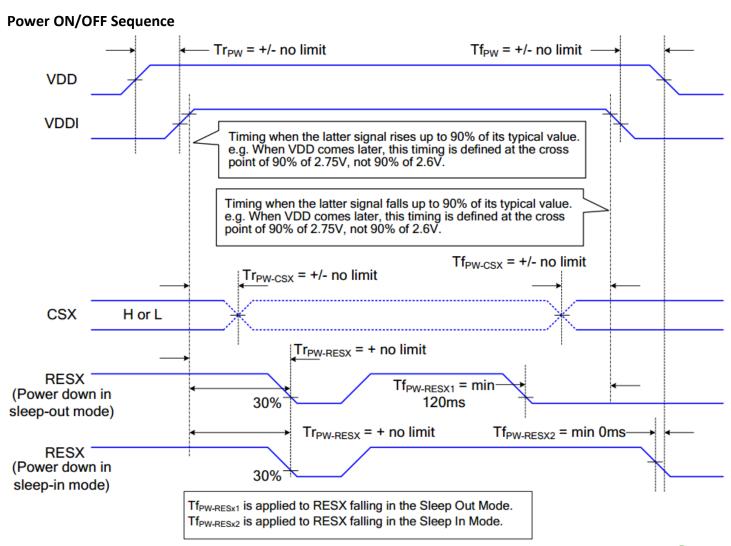
Signal	Symbol	Parameter	Min	Max	Unit	Description
	T <sub>CSS</sub>	Chip select setup time (write)	15		ns	
	T <sub>CSH</sub>	Chip select hold time (write)	15		ns	
CSX	T <sub>CSS</sub>	Chip select setup time (read)	60		ns	
	Tscc	Chip select hold time (read)	65		ns	
	T <sub>CHW</sub>	Chip select "H" pulse width	40		ns	
	Tscycw	Serial clock cycle (Write)	16		ns	
	Тѕнѡ	SCL "H" pulse width (Write)	7		ns	
CCI	T <sub>SLW</sub>	SCL "L" pulse width (Write)	7		ns	
SCL	T <sub>SCYCR</sub>	Serial clock cycle (Read)	150		ns	
	T <sub>SHR</sub>	SCL "H" pulse width (Read)	60		ns	
	Tslr	SCL "L" pulse width (Read)	60		ns	
SDA	T <sub>SDS</sub>	Data setup time	7		ns	
(DIN)	T <sub>SDH</sub>	Data hold time	7		ns	
DOUT	Tacc	Access time	10	50	ns	For maximum CL=30pF
DOUT	Тон	Output disable time	15	50	ns	For minimum CL=8pF



#### **Reset Timing**



Related Pins	Symbol Parameter		MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120 (Note 1, 6, 7)	ms





#### **Example Initialization Code**

```
void Command out(unsigned char c) //Function used for sending commands to TFT
  PORTA &= \sim (1 << PORTA3);
                             // Set DCX pin to LOW
                             // Assigning the Command Byte 'c' to PortL
  PORTL=c;
                            // Set WRX pin to LOW
 PORTA \&= \sim (1 << PORTA7);
  PORTA |= (1 << PORTA7);
                            // Set WRX pin to HIGH
void data out(unsigned char d) //Function used for sending data to TFT
  PORTA \mid = (1 << PORTA3);
                             // Set DCX pin to HIGH
                             // Assigning the Data Byte 'd' to PortL
  PORTL=d;
  PORTA &= \sim (1 << PORTA7);
                            // Set WRX pin to LOW
  PORTA \mid = (1 << PORTA7);
                            // Set WRX pin to HIGH
void TFT init(){
  digitalWrite (RESX, LOW);
  delay(250);
  digitalWrite(RESX, HIGH);
  delay(250);
  Command out (0x28); //display off
  Command out(0x11); //exit SLEEP mode
  delay(100);
  Command out(0x36); //MADCTL: memory data access control
  data_out(0x88);
  Command out(0x3A); //COLMOD: Interface Pixel format *** 65K-colors in 16bit/pixel (5-
6-5) format when using 16-bit interface to allow 1-byte per pixel
  data out (0x55);
  Command out(0xB2); //PORCTRK: Porch setting
  data out (0x0C);
  data out (0x0C);
  data out (0x00);
  data out (0x33);
  data out (0x33);
  Command out(0xB7); //GCTRL: Gate Control
  data out (0x35);
  Command out(0xBB); //VCOMS: VCOM setting
  data out (0x2B);
  Command out(0xC0); //LCMCTRL: LCM Control
  data out (0x2C);
  Command out(0xC2); //VDVVRHEN: VDV and VRH Command Enable
  data out (0x01);
  data out (0xFF);
  Command out (0xC3); //VRHS: VRH Set
  data out (0x11);
```



}

```
Command out(0xC4); //VDVS: VDV Set
data out (0x20);
Command out(0xC6); //FRCTRL2: Frame Rate control in normal mode
data out (0x0F);
Command out(0xD0); //PWCTRL1: Power Control 1
data out (0xA4);
data out (0xA1);
Command out(0xE0); //PVGAMCTRL: Positive Voltage Gamma control
data out (0xD0);
data out (0x00);
data out (0x05);
data out (0x0E);
data out (0x15);
data_out(0x0D);
data_out(0x37);
data_out(0x43);
data_out(0x47);
data out (0x09);
data out (0x15);
data out (0x12);
data out (0x16);
data out (0x19);
Command out(0xE1); //NVGAMCTRL: Negative Voltage Gamma control
data out (0xD0);
data out (0x00);
data out (0x05);
data out (0x0D);
data out (0x0C);
data out (0x06);
data_out(0x2D);
data out(0x44);
data out (0x40);
data out(0x0E);
data_out(0x1C);
data out (0x18);
data out (0x16);
data out (0x19);
Command out (0x2A); //X address set
data out (0x00);
data out (0x00);
data out (0x00);
data_out(0xEF);
Command out (0x2B); //Y address set
data out (0x00);
data out (0x00);
data out(0x01);
data out (0x3F);
delay(10);
Command_out(0x21); //Color inversion for IPS
Command_out(0x29); //display ON
delay(10);
```



# **Quality Information**

Test Item	Content of Test	Test Condition	Note
High Temperature Storage	Endurance test applying the high storage temperature for a long time.	+80°C, 240hrs	2
Low Temperature Storage	Endurance test applying the low storage temperature for a long time.	-30°C, 240hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the high thermal stress for a long time.	+70°C, 240hrs	2
Low Temperature Operation	Endurance test applying the electric stress (voltage & current) and the low thermal stress for a long time.	-20°C, 240hrs	1,2
High Temperature / Humidity Operation	Endurance test applying the electric stress (voltage & current) and the high thermal with high humidity stress for a long time.	+60°C, 90% RH, 240hrs	1,2
Thermal Shock resistance	Endurance test applying the electric stress (voltage & current) during a cycle of low and high thermal stress.	-30°C 30min -> 25°C 3min -> 80°C 30min = 1 cycle. For 100 cycles	
Vibration test	Endurance test applying vibration to simulate transportation and use.	10Hz-55Hz (1 min.), 1.5mm amplitude. 30 min. exposure for each directions X,Y,Z	3
Static electricity test	Endurance test applying electric static discharge.	Air charge: ±8kV 10 Times Contact charge: ±4kV 10 Times	

**Note 1:** No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

**Note 3:** Test performed on product itself, not inside a container.