

A diagram showing a single rounded rectangular box labeled "ARM".

ARM

A diagram showing a rounded rectangular box labeled "ARM" at the top. Below it, three smaller rounded rectangular boxes are arranged horizontally, labeled "Static HW 0", "Static HW 1", and "Static HW N". Ellipses "..." are placed between "Static HW 1" and "Static HW N". Lines connect the bottom of the "ARM" box to the top of each of the three hardware boxes.

ARM

Static HW 0 Static HW 1 ... Static HW N

A diagram showing a rounded rectangular box labeled "ARM" at the top. Below it, a single larger rounded rectangular box is labeled "Reconfigurable Logic". A vertical line connects the bottom of the "ARM" box to the top of the "Reconfigurable Logic" box.

ARM

Reconfigurable
Logic