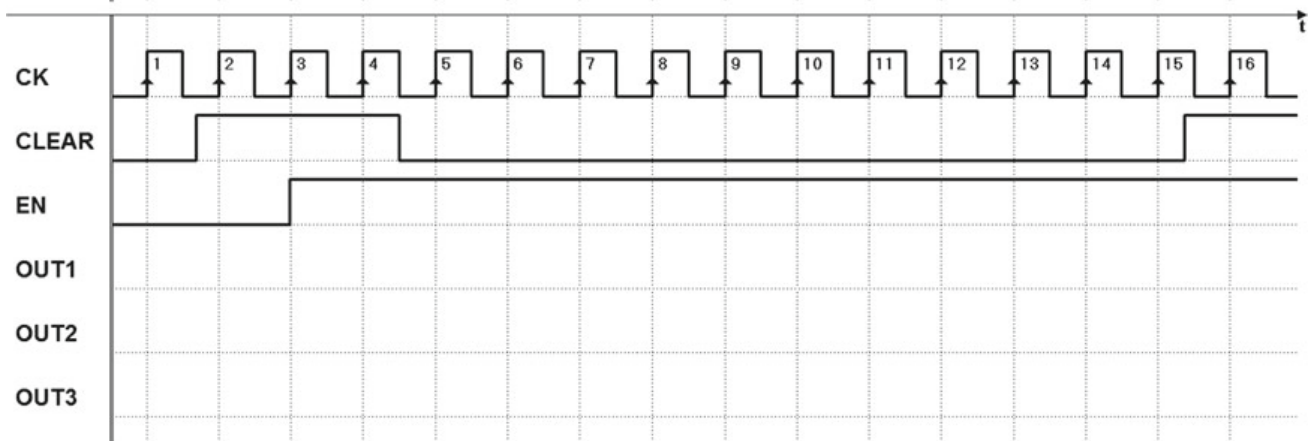
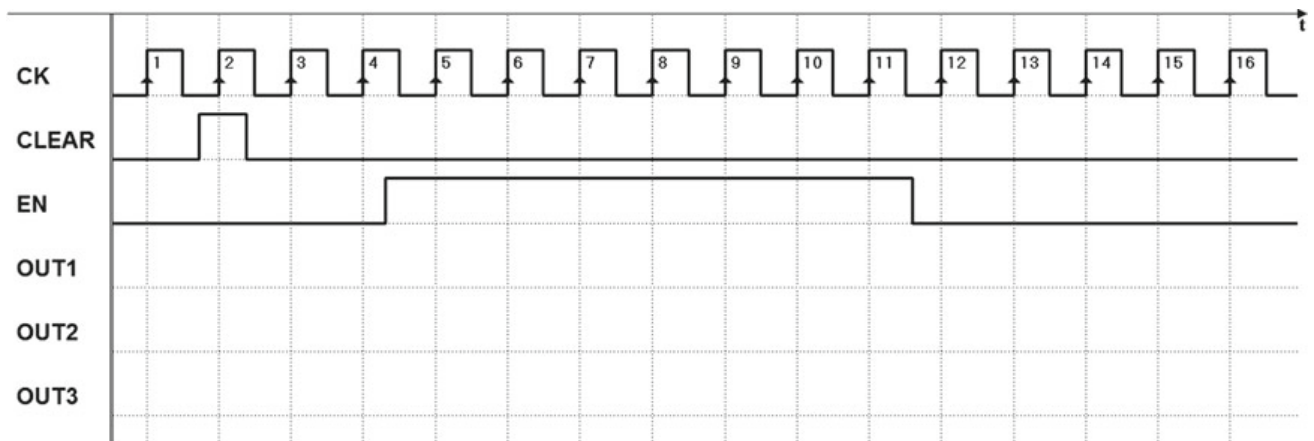
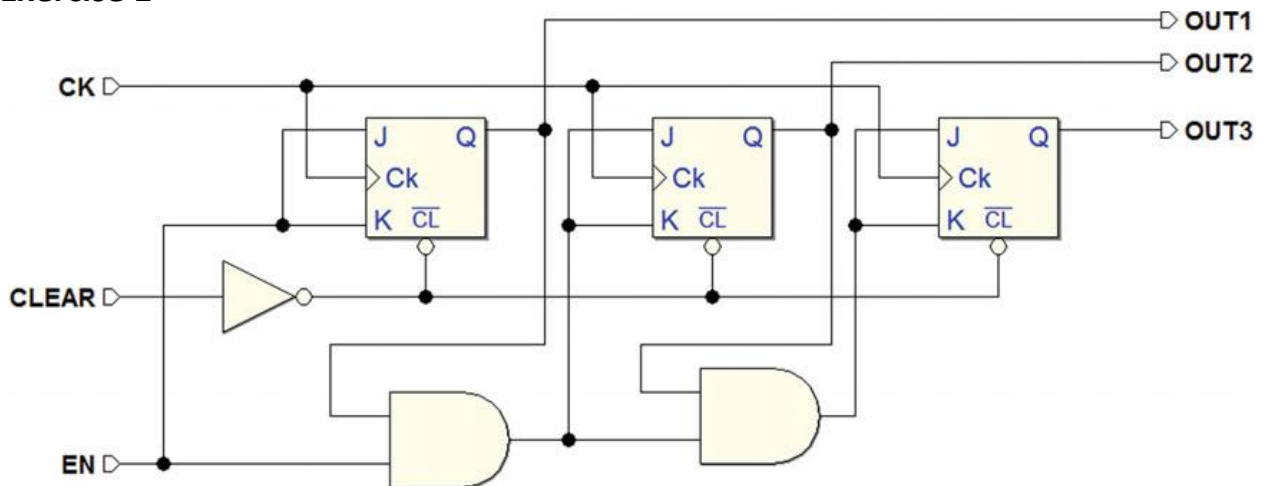


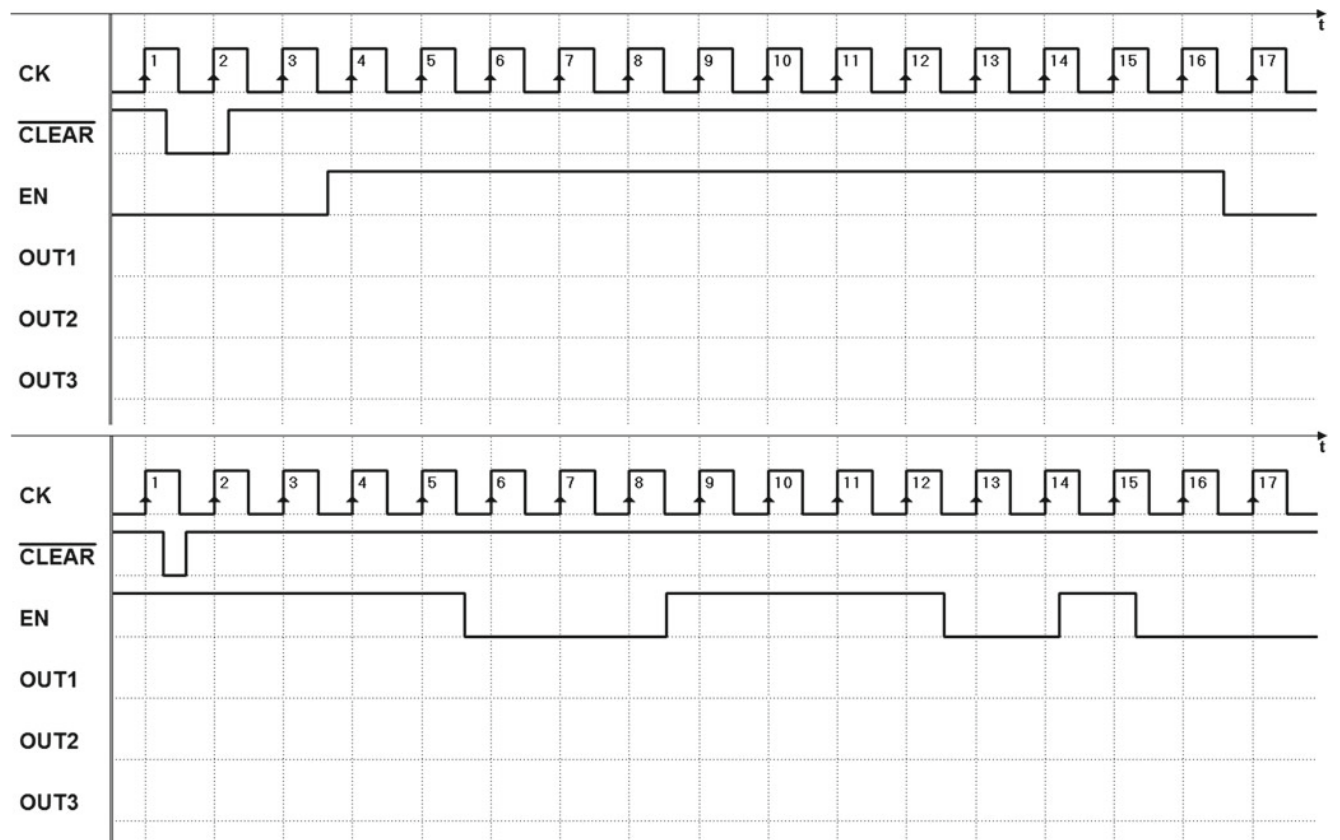
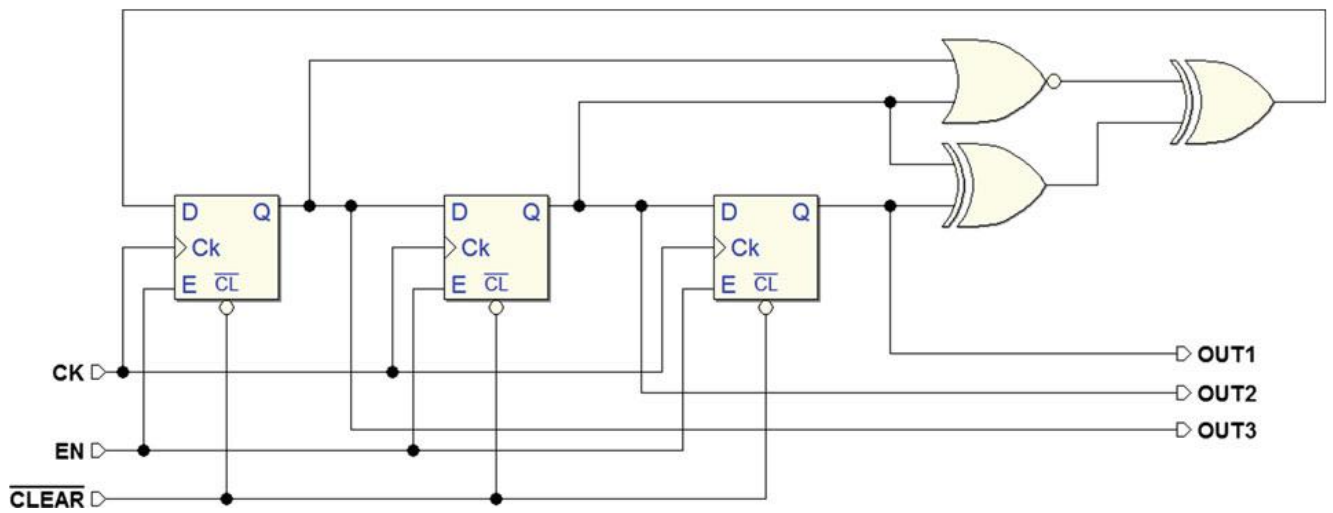
## Time analysis

Analyze the following synchronous sequential networks by completing the timing diagrams on the side page. It is recommended to manually complete these diagrams on paper first, using Deeds only to verifying your solutions.

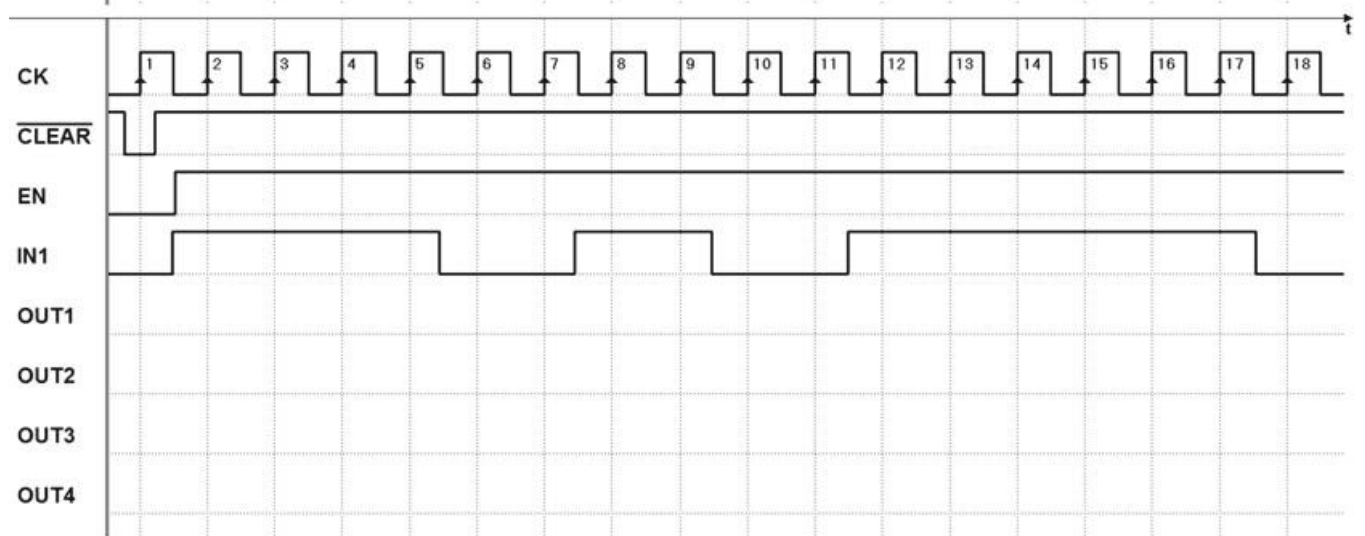
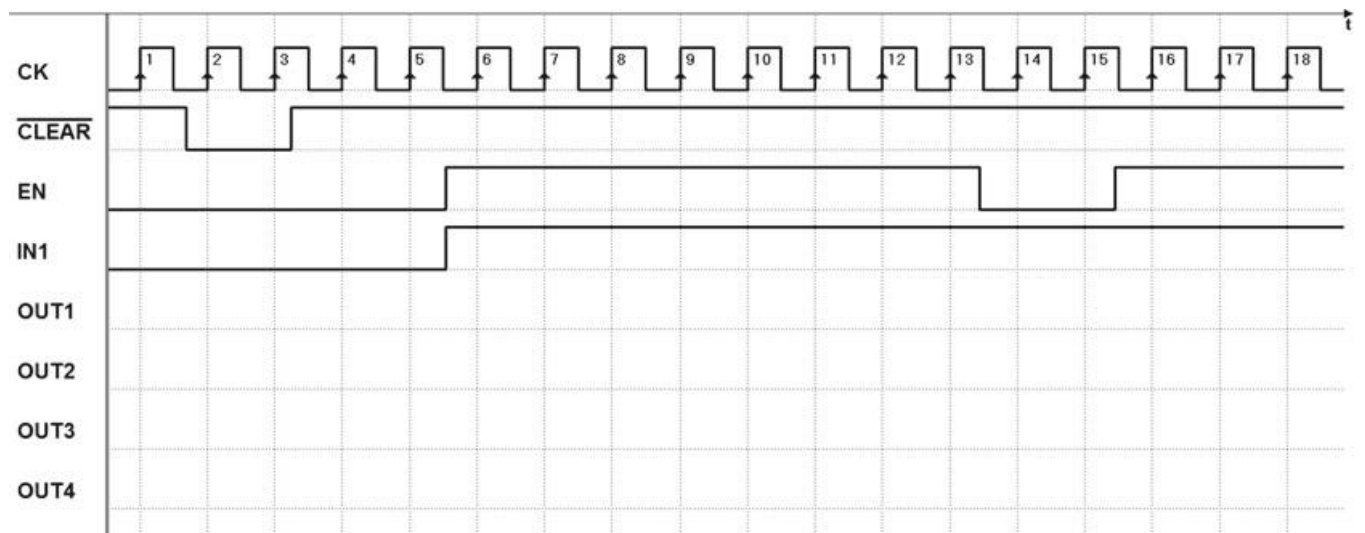
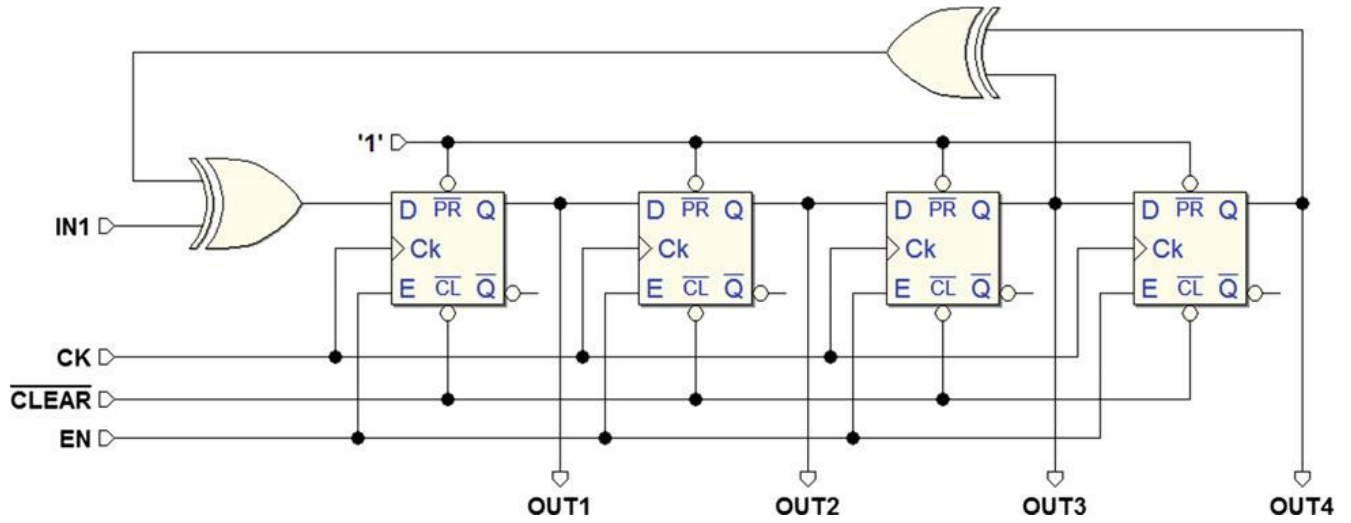
### Exercise 1



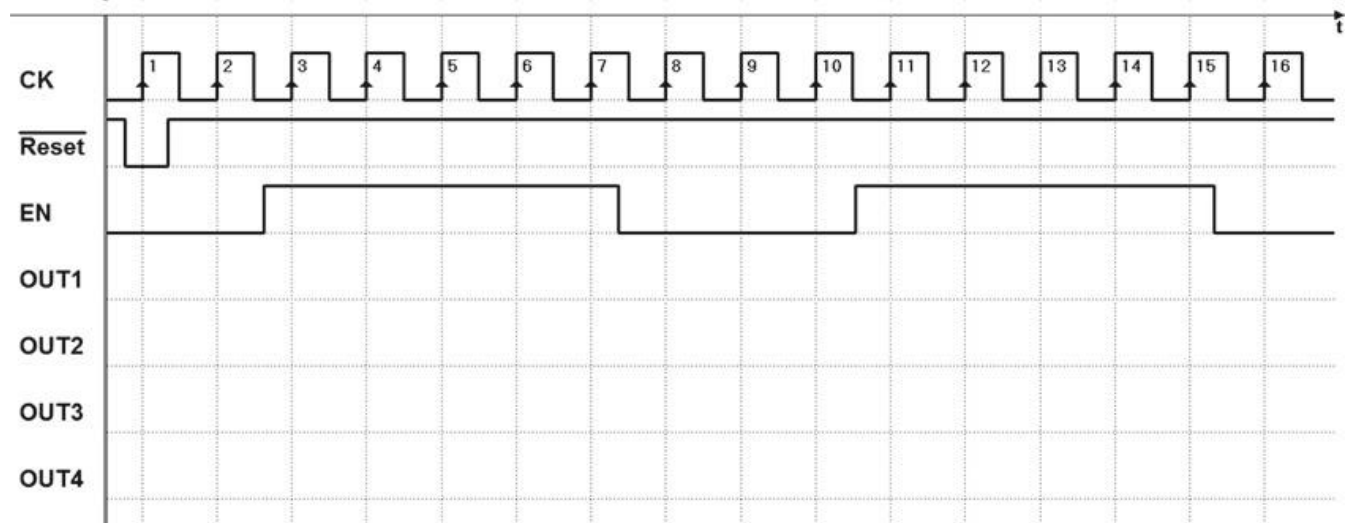
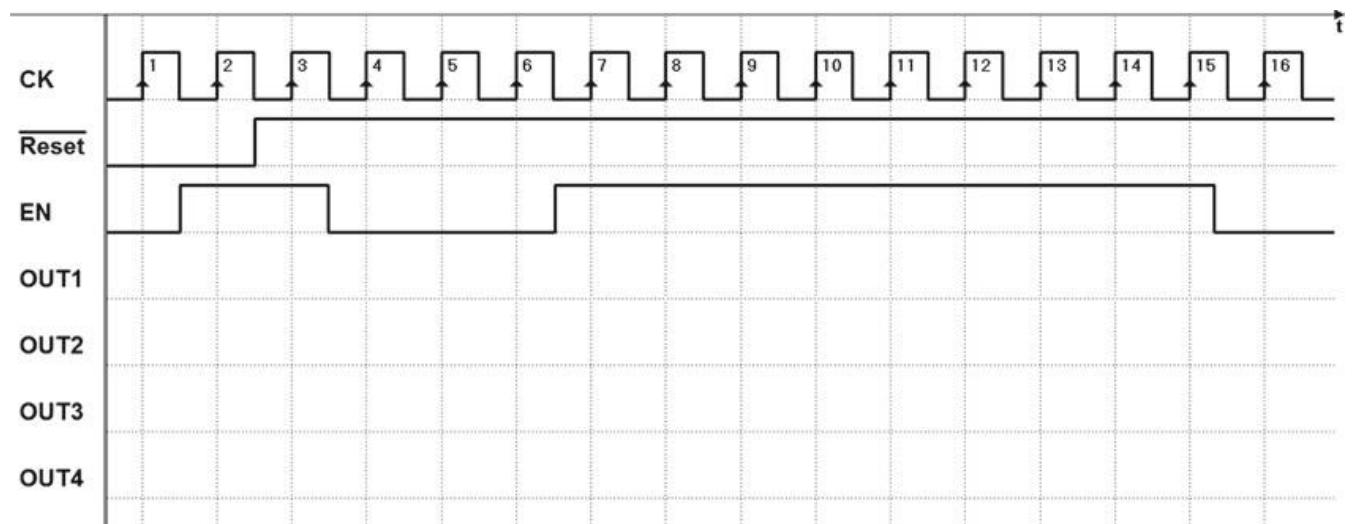
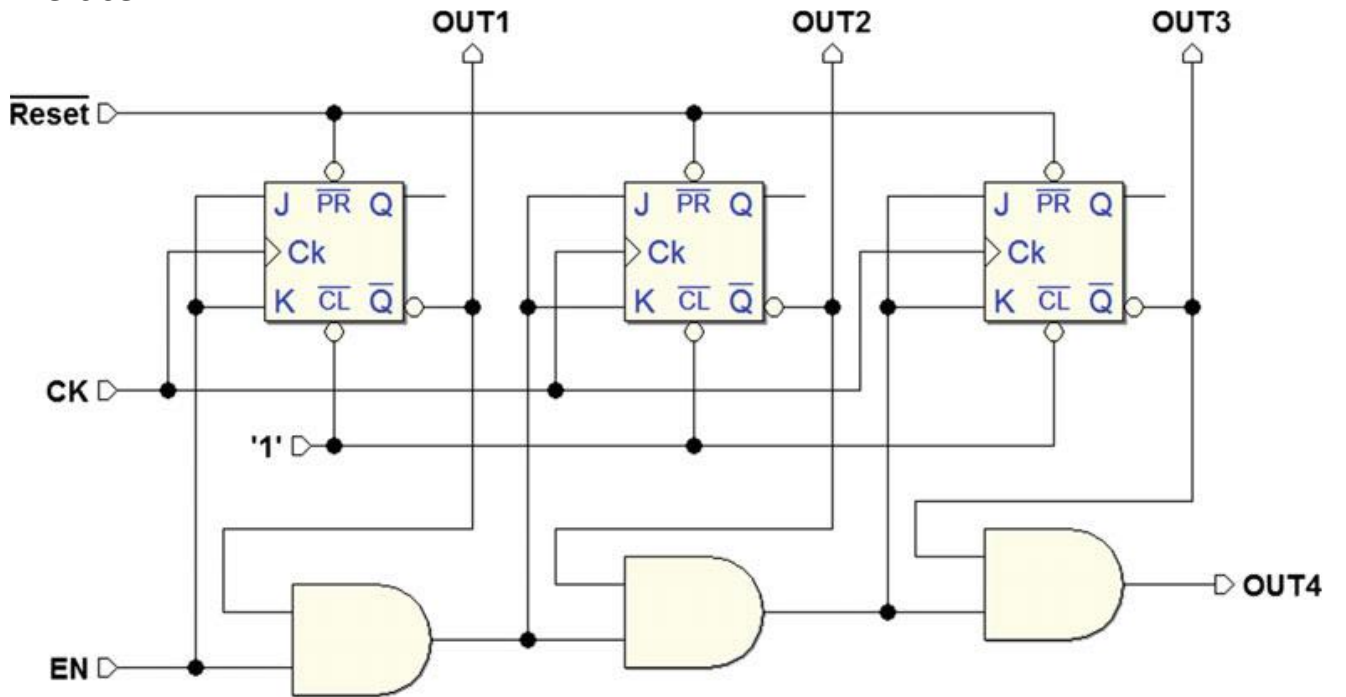
## Exercise 2



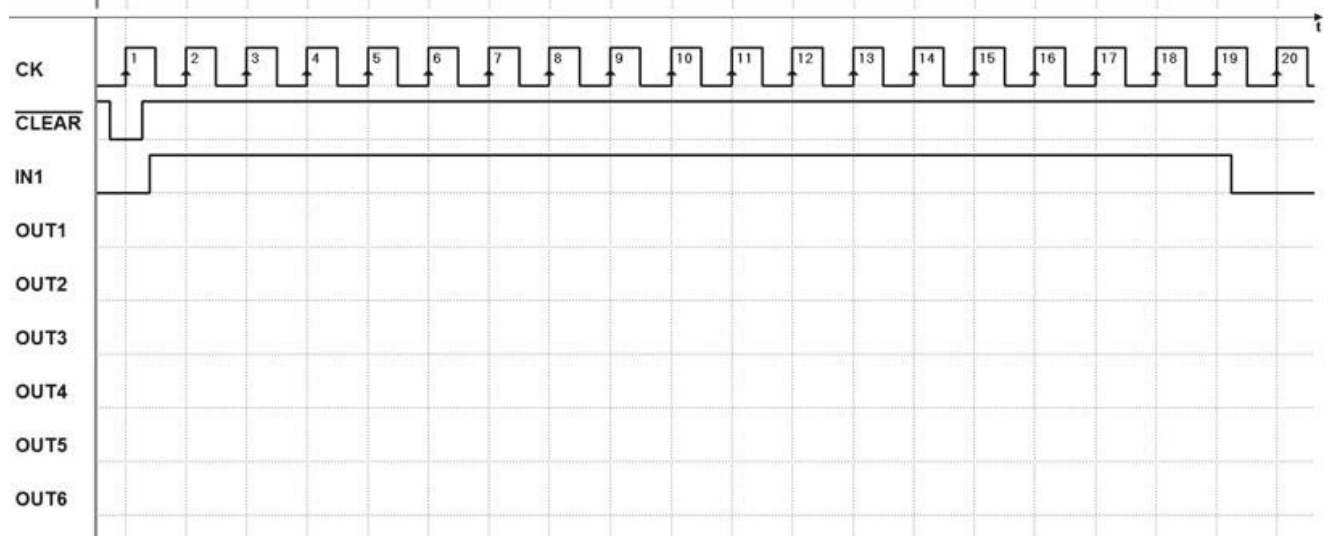
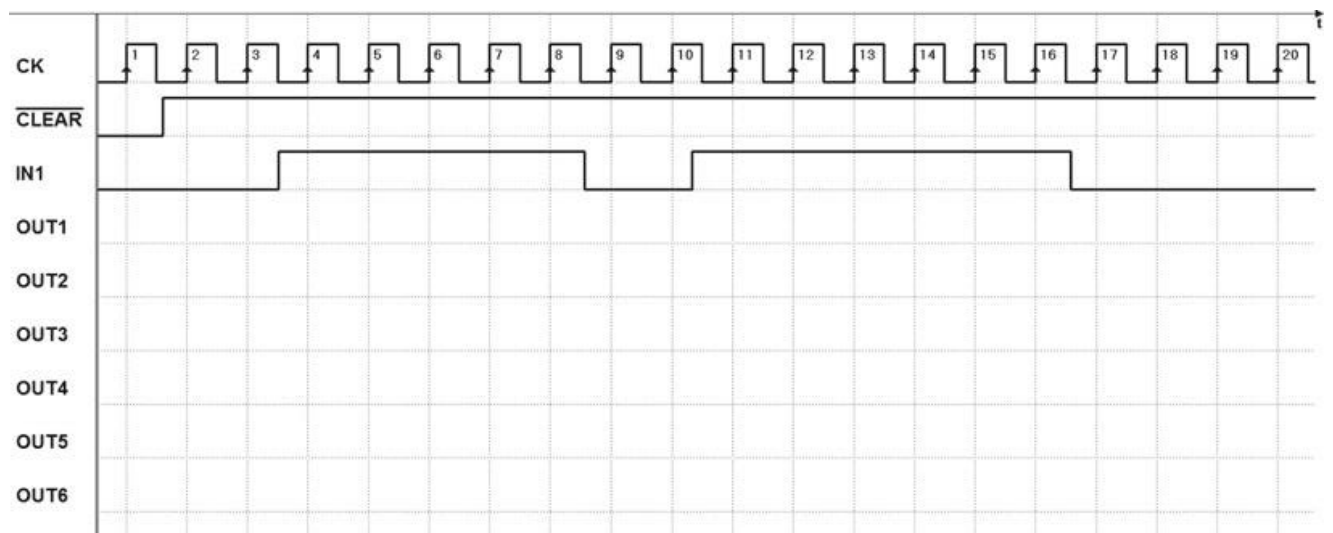
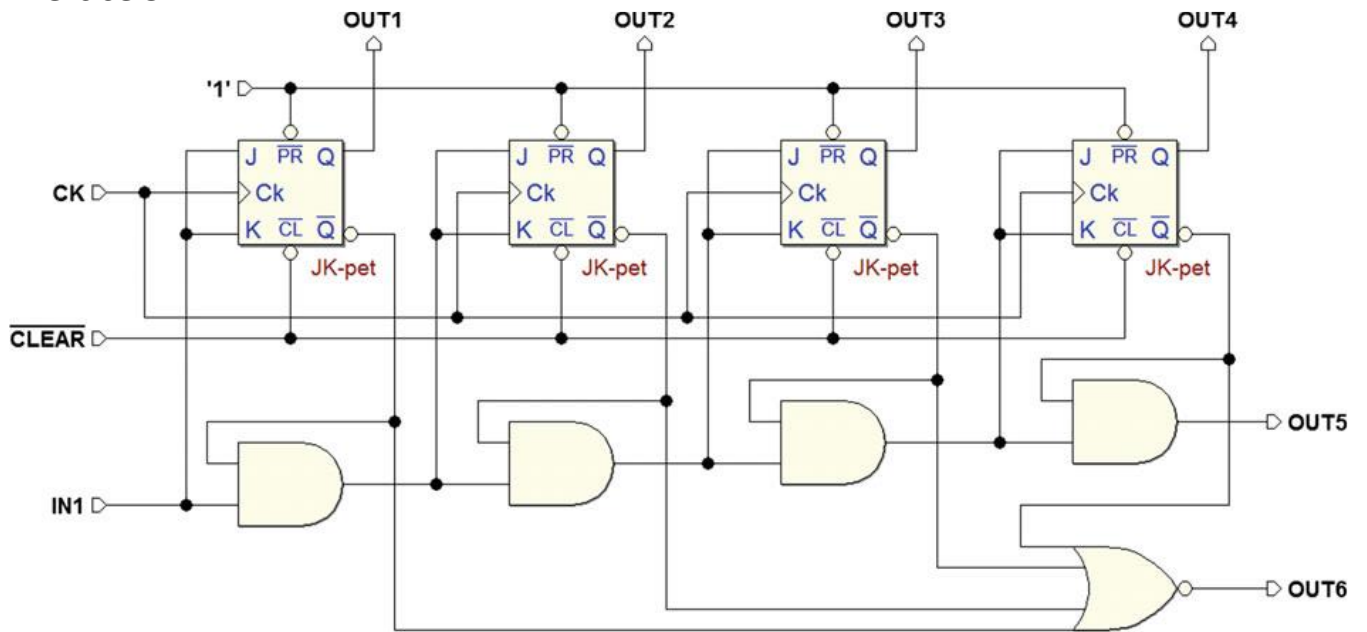
### Exercise 3



## Exercise 4

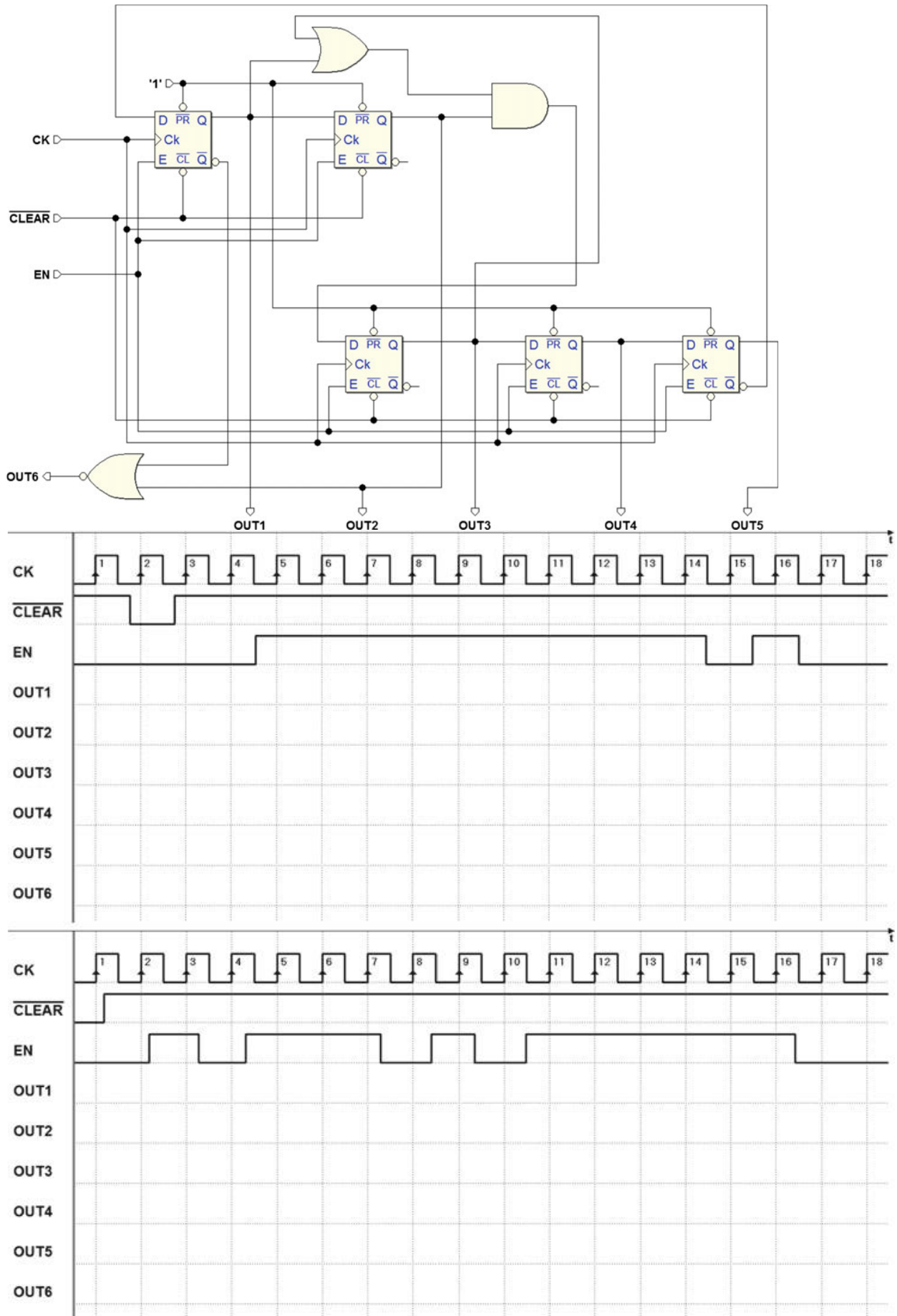


## Exercise 5

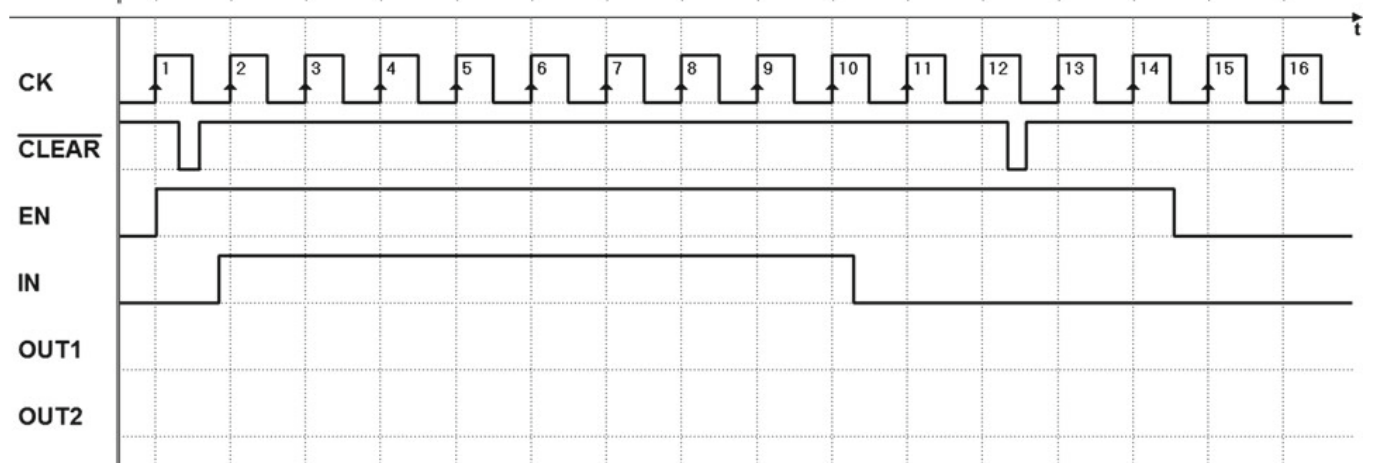
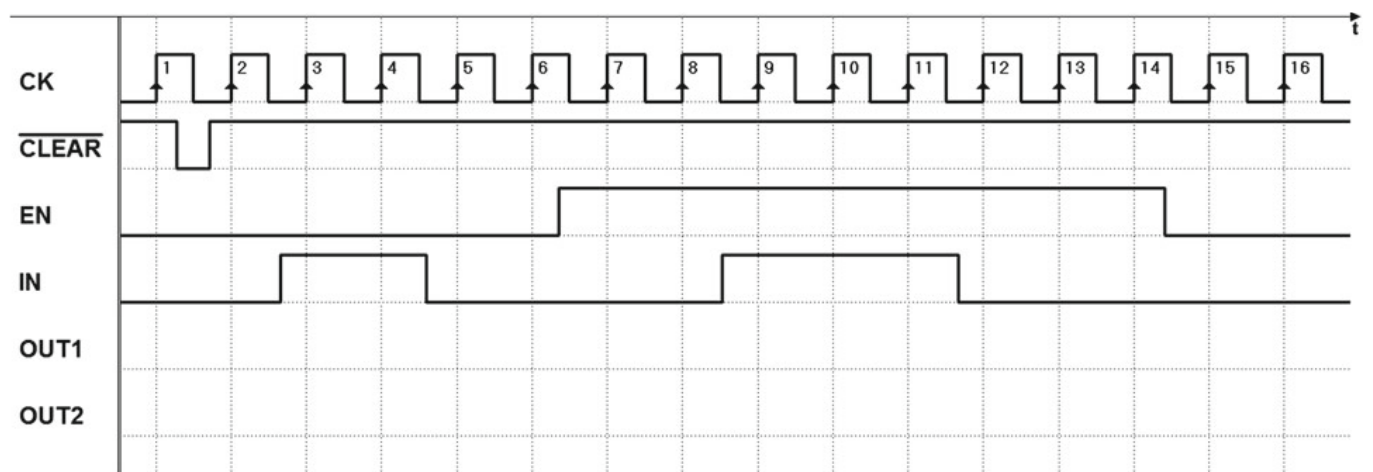
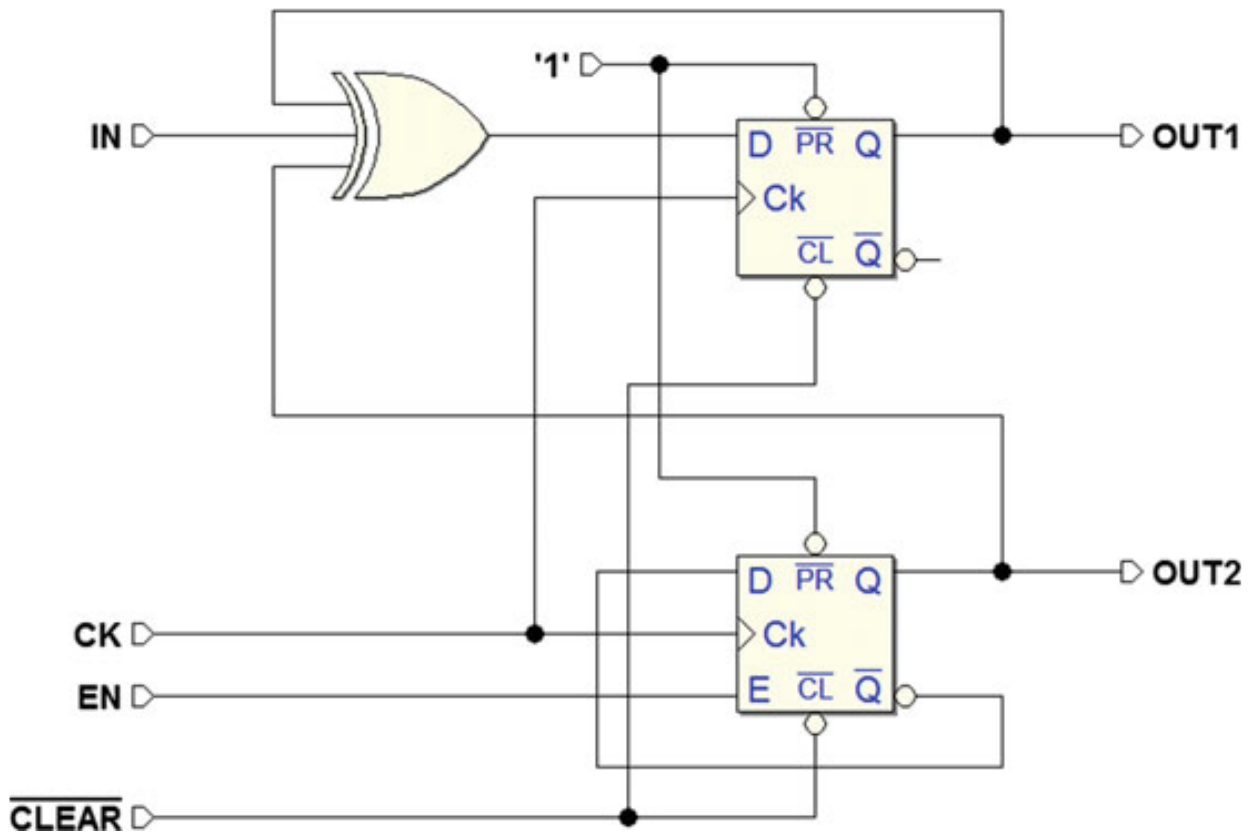




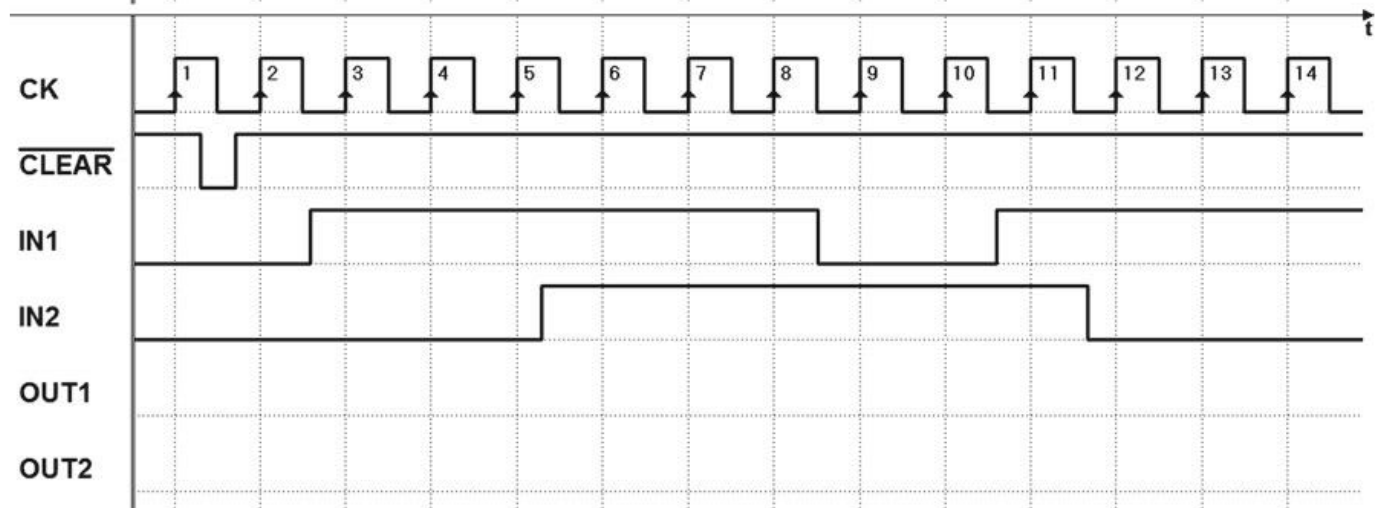
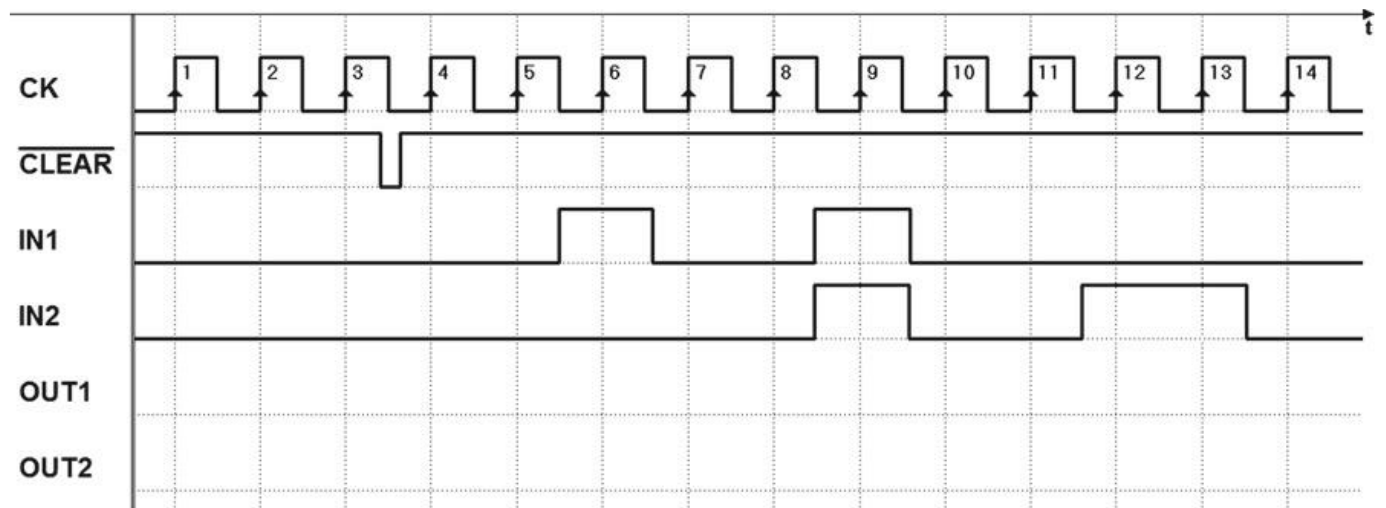
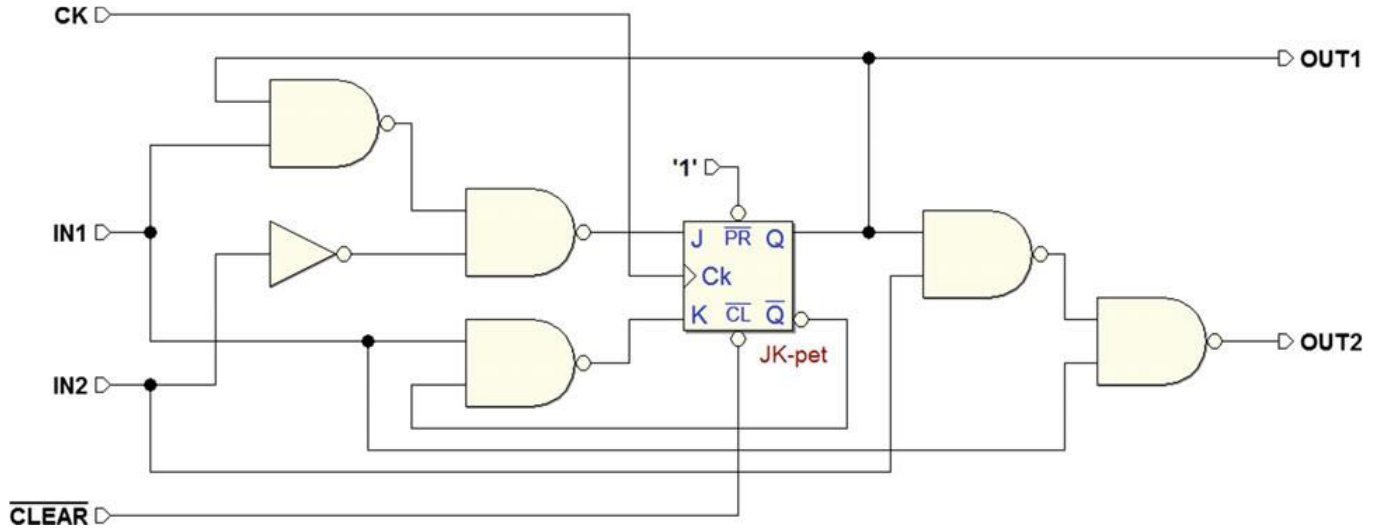
## Exercise 6



## Exercise 7

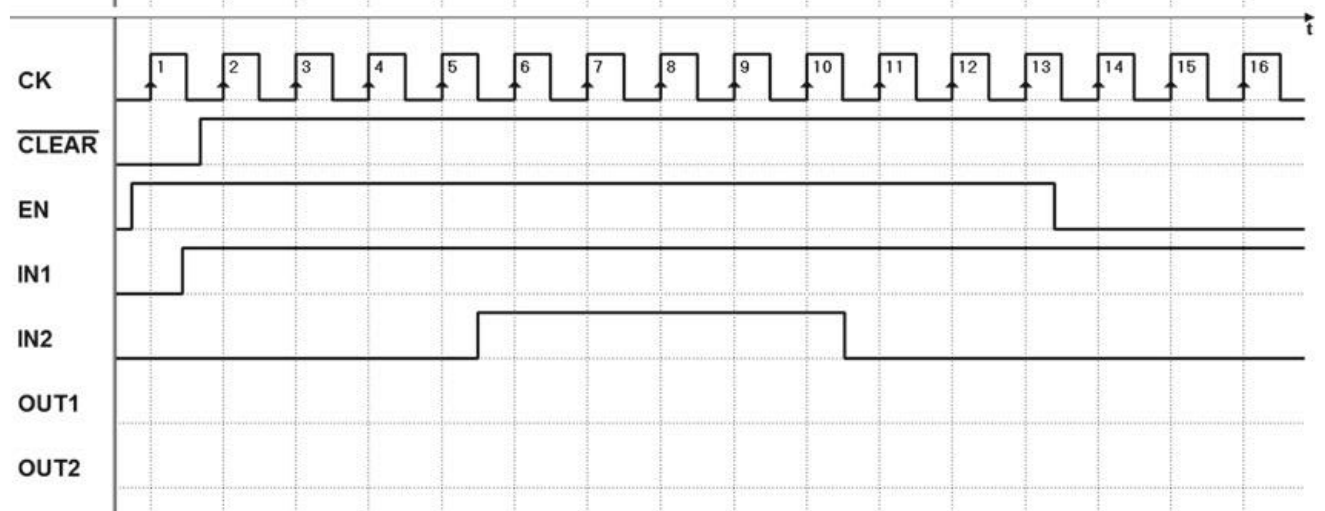
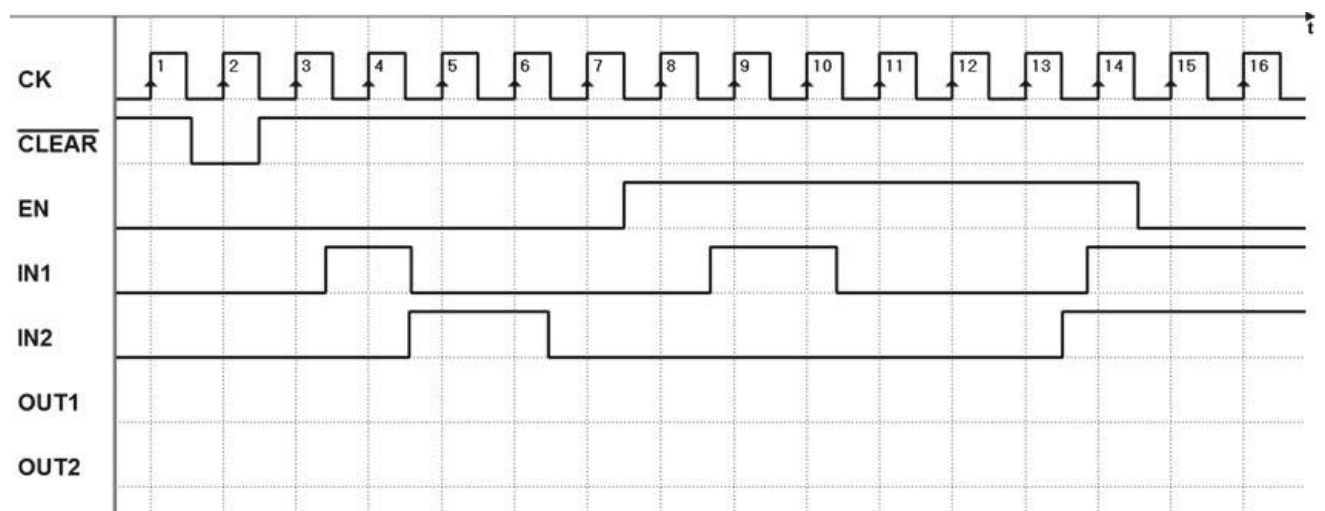
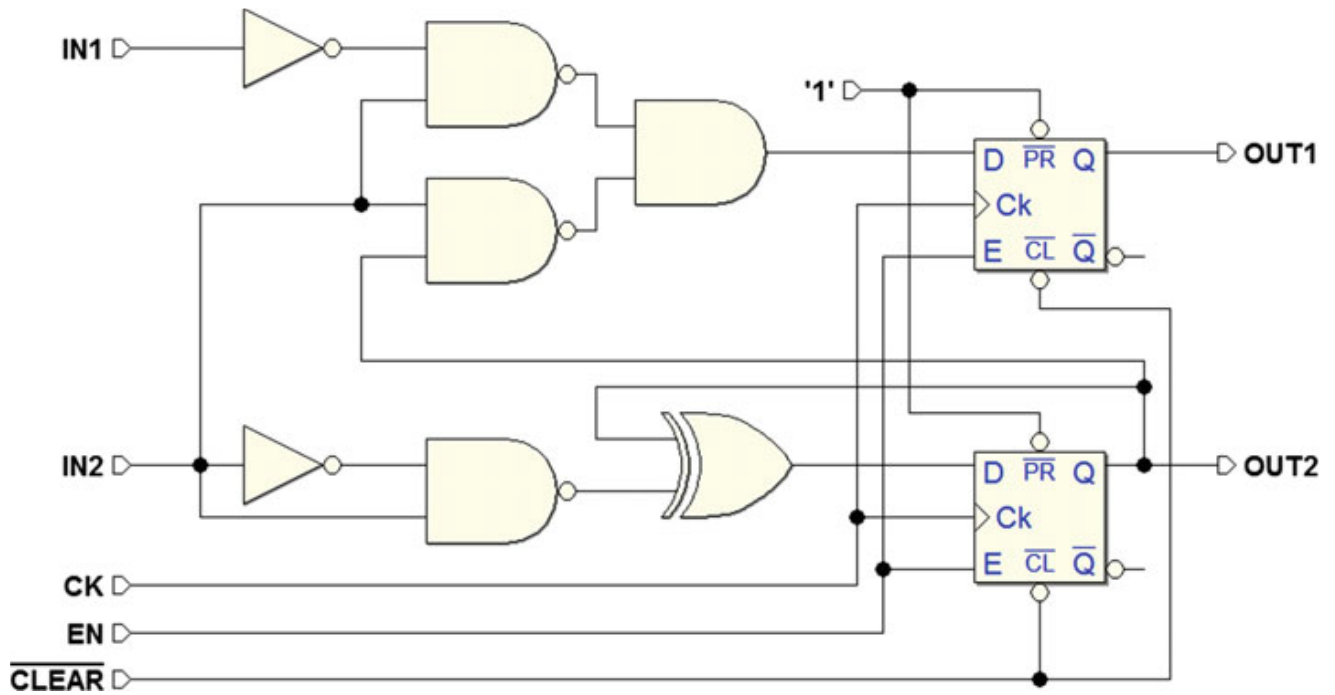


## Exercise 8

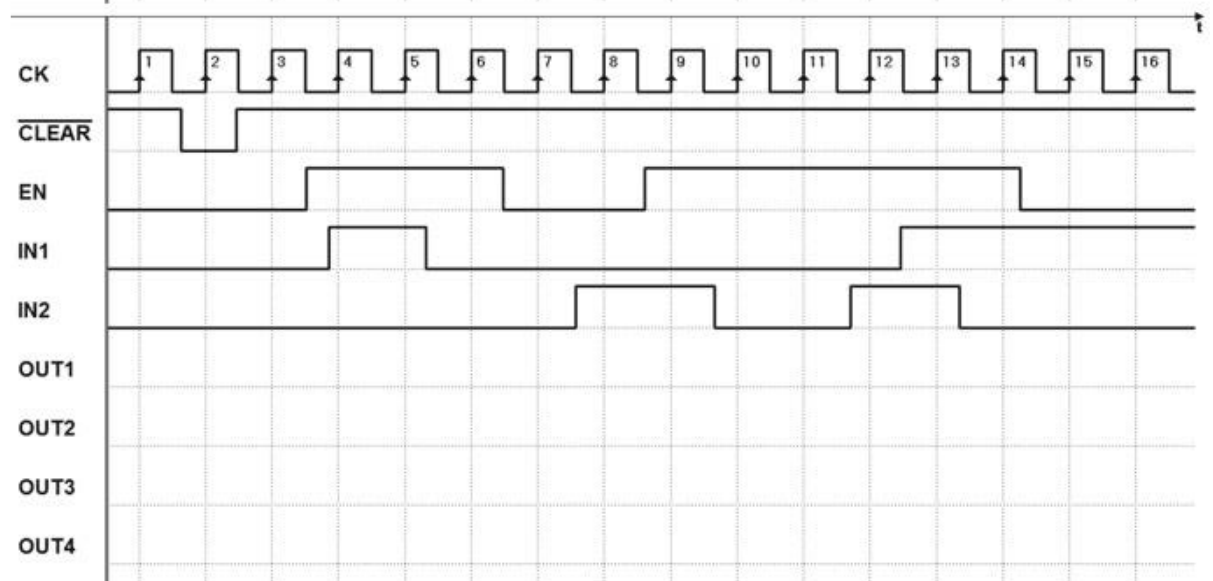
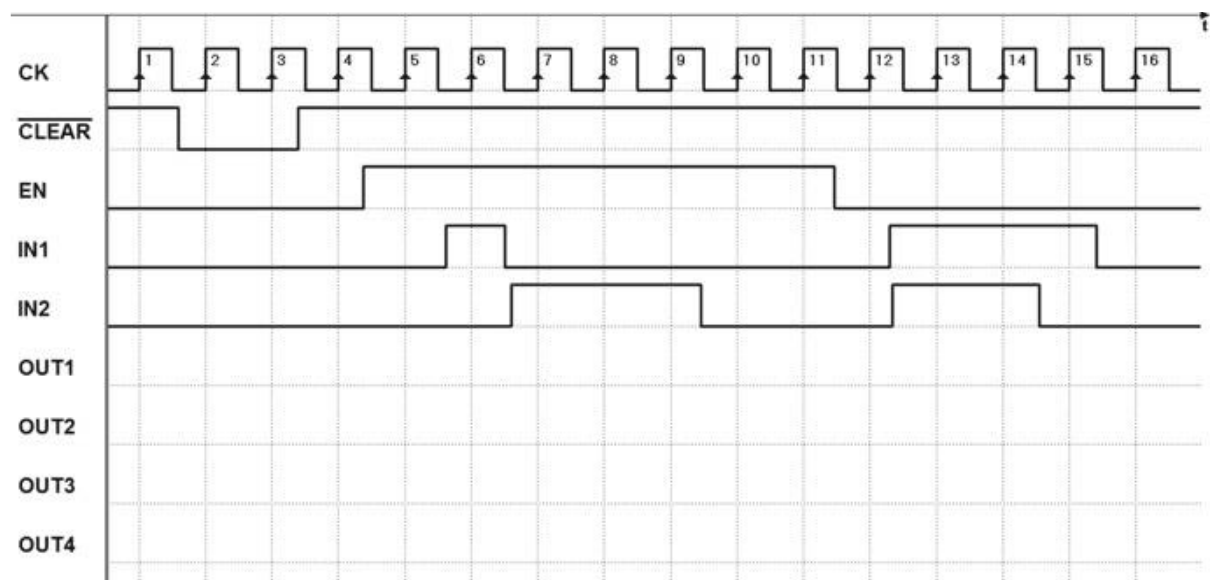
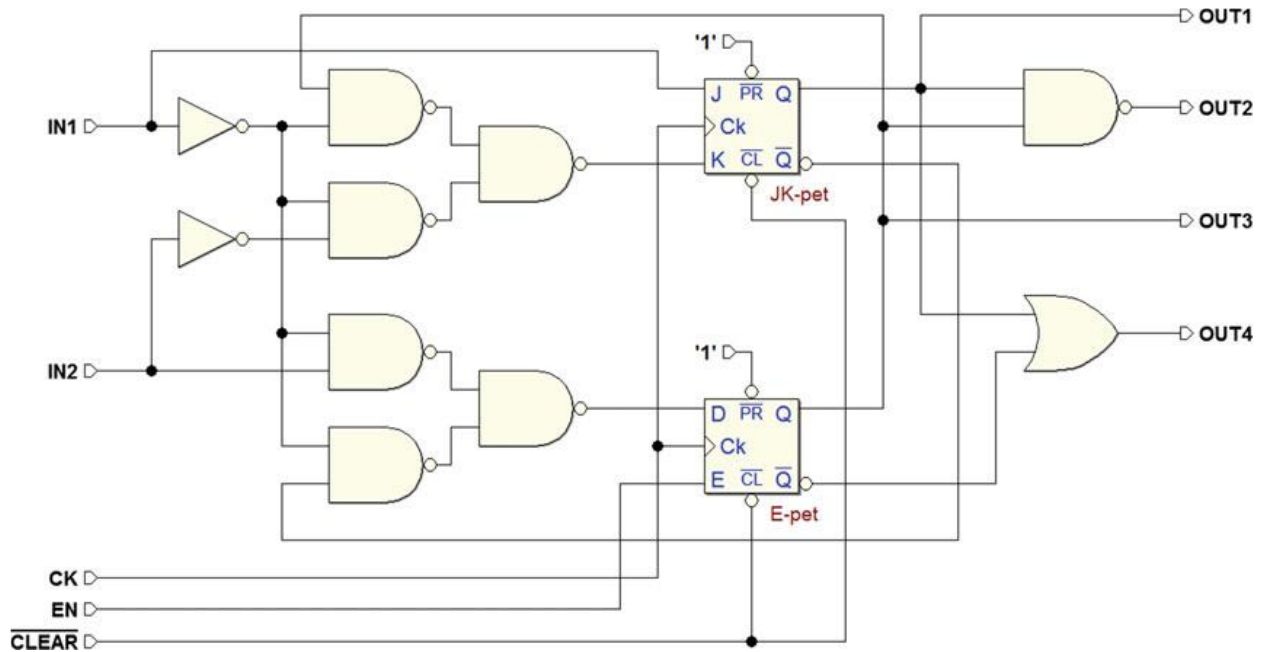




## Exercise 9

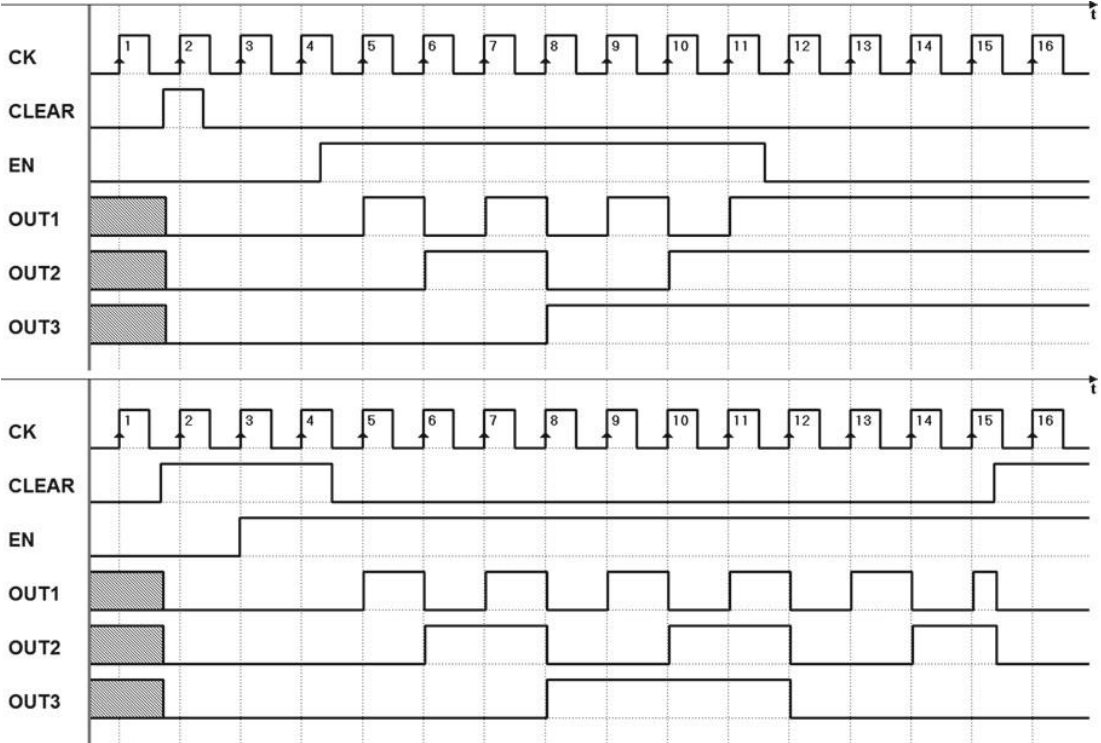


## Exercise 10

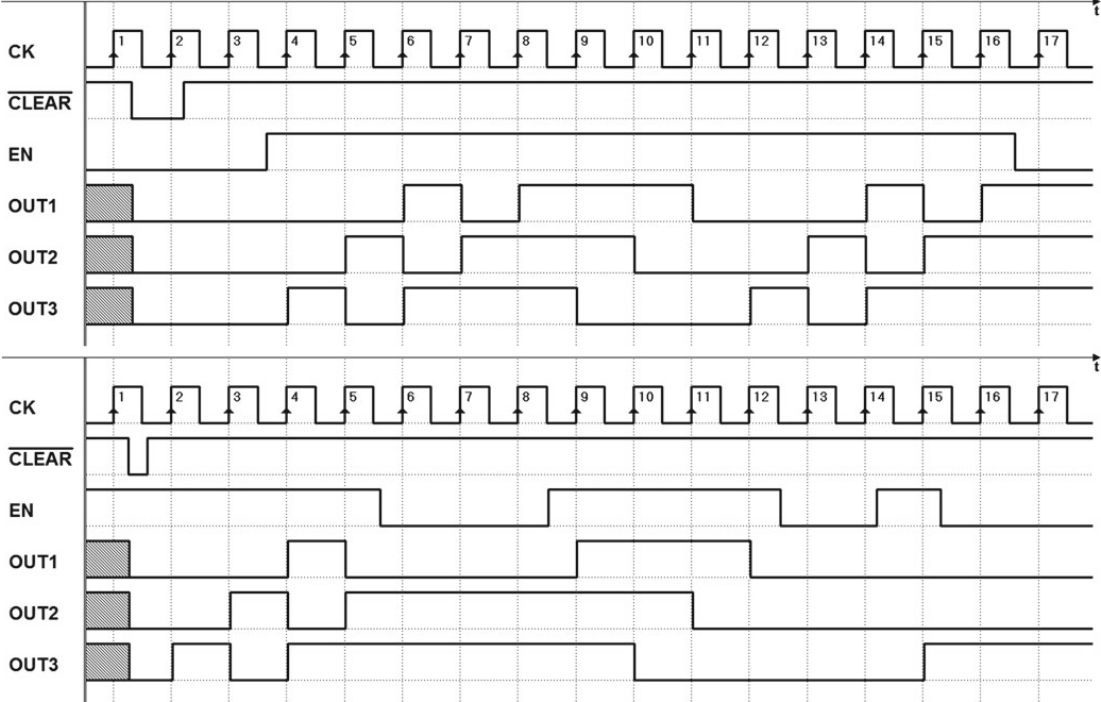


# Solutions

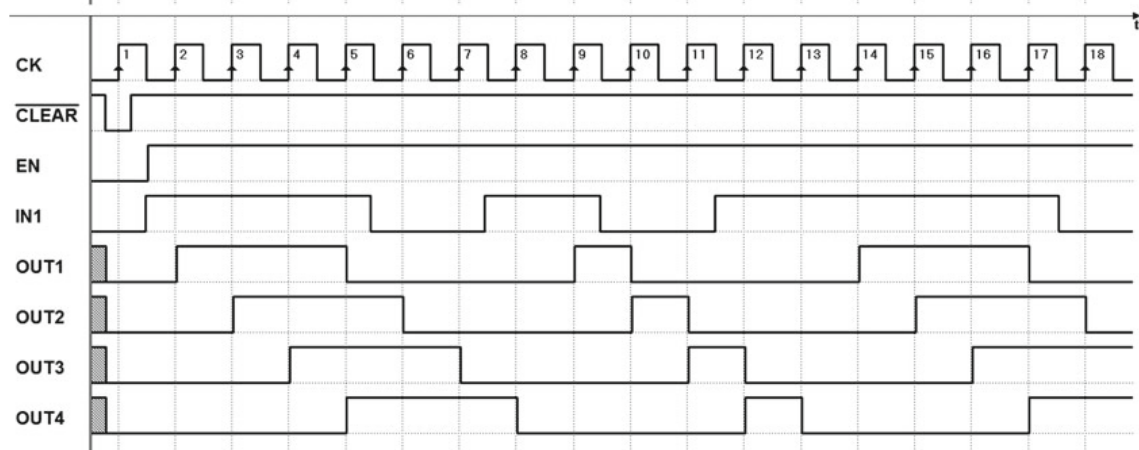
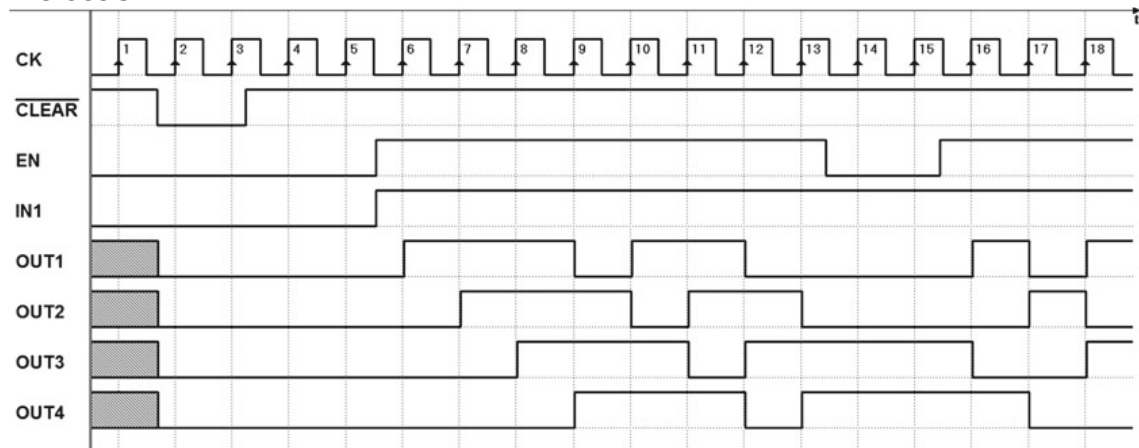
## Exercise 1



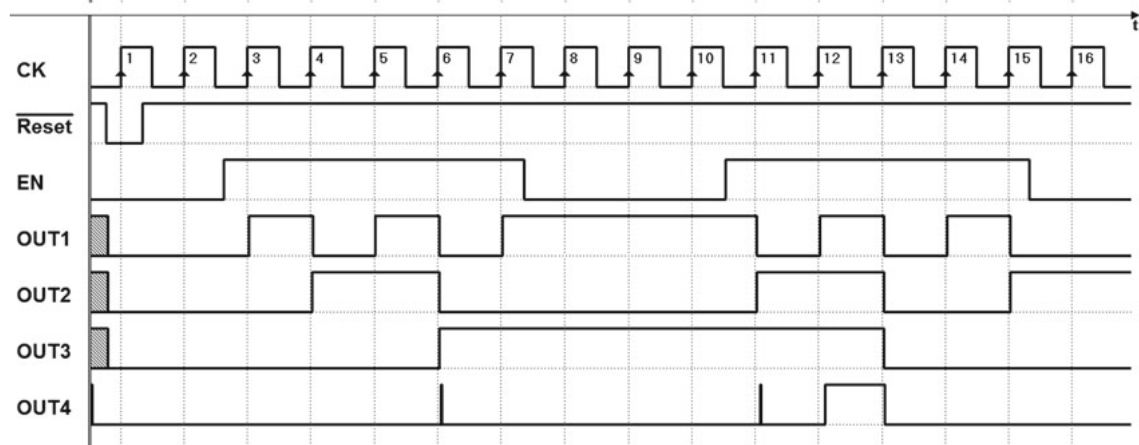
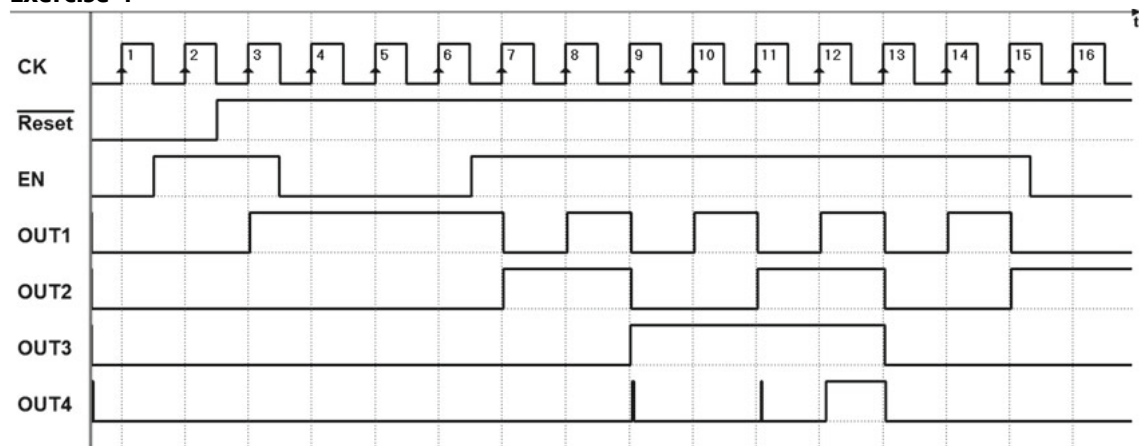
## Exercise 2



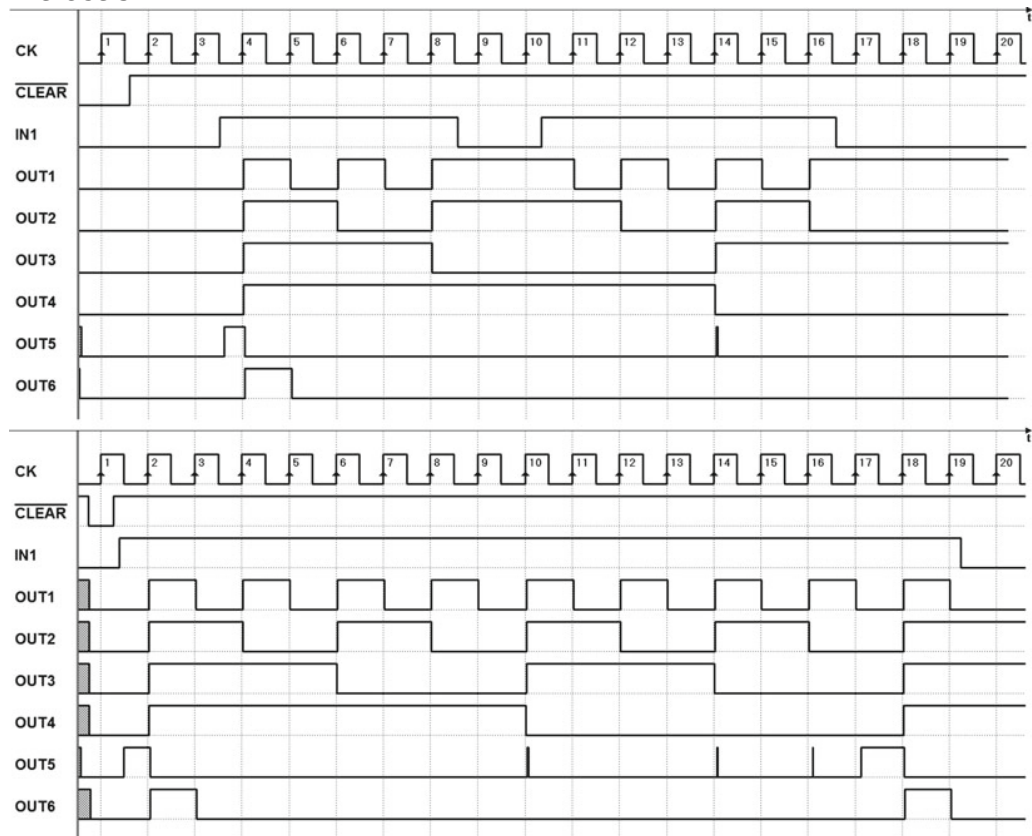
### Exercise 3



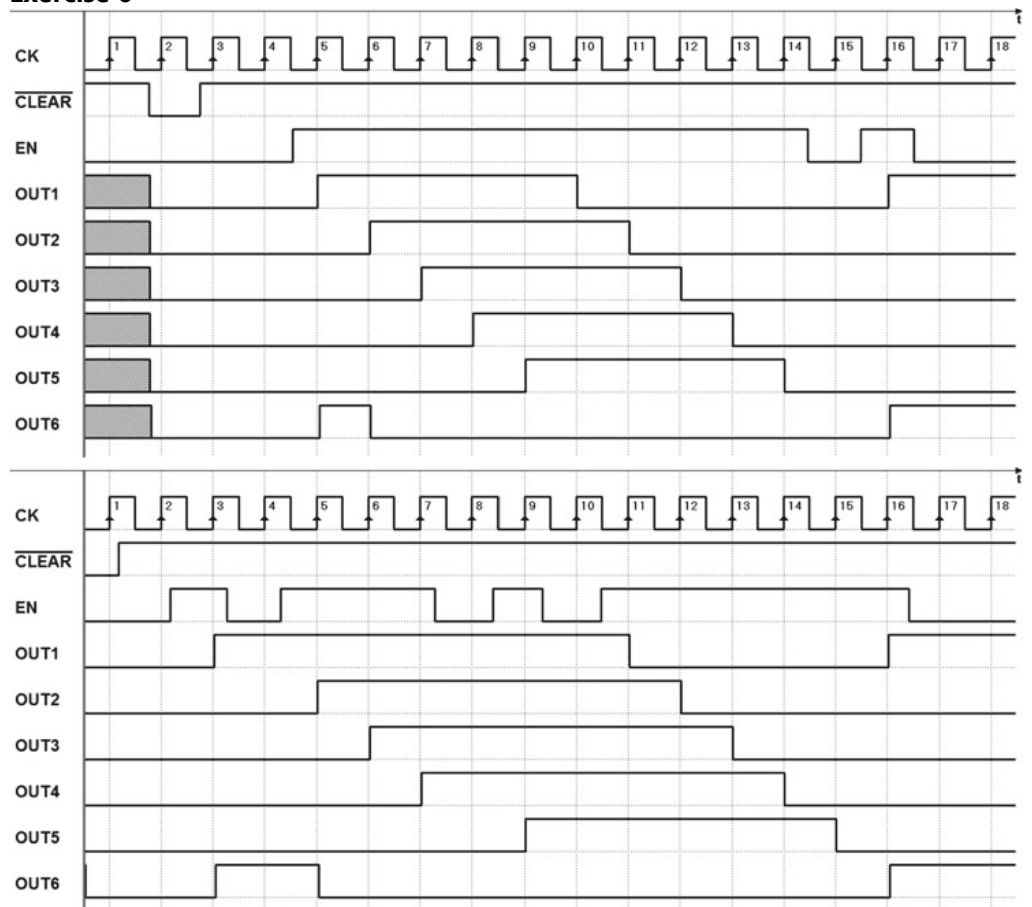
### Exercise 4



## Exercise 5

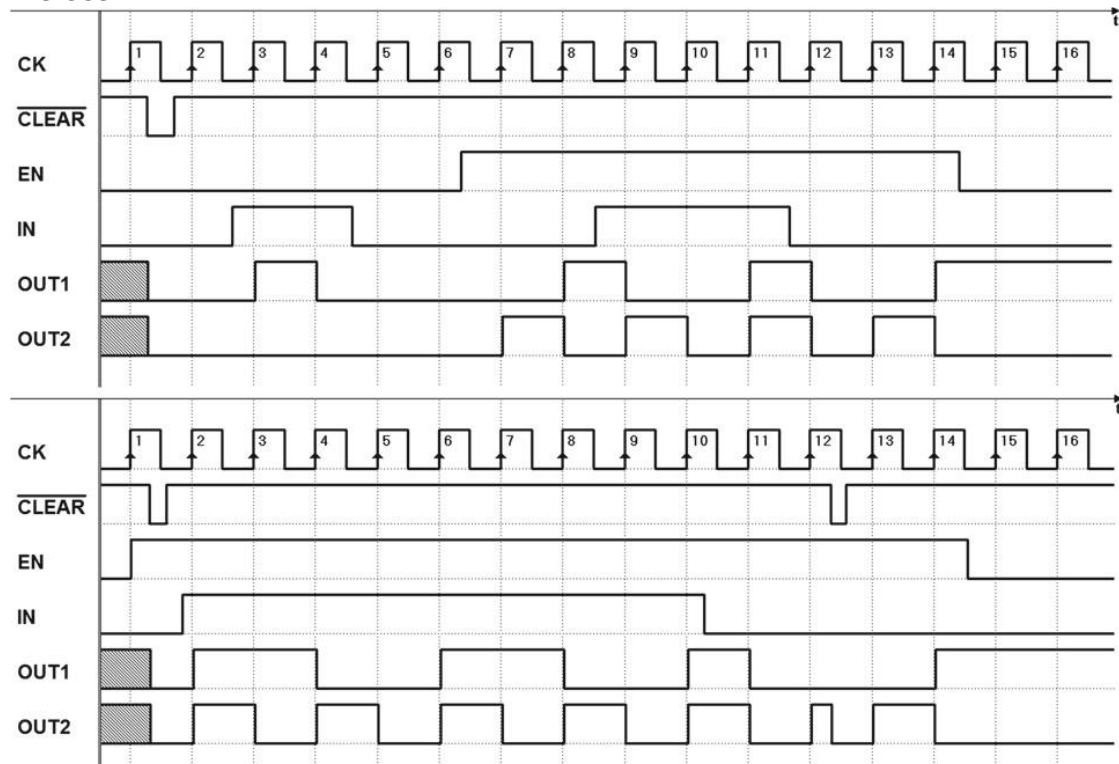


## Exercise 6

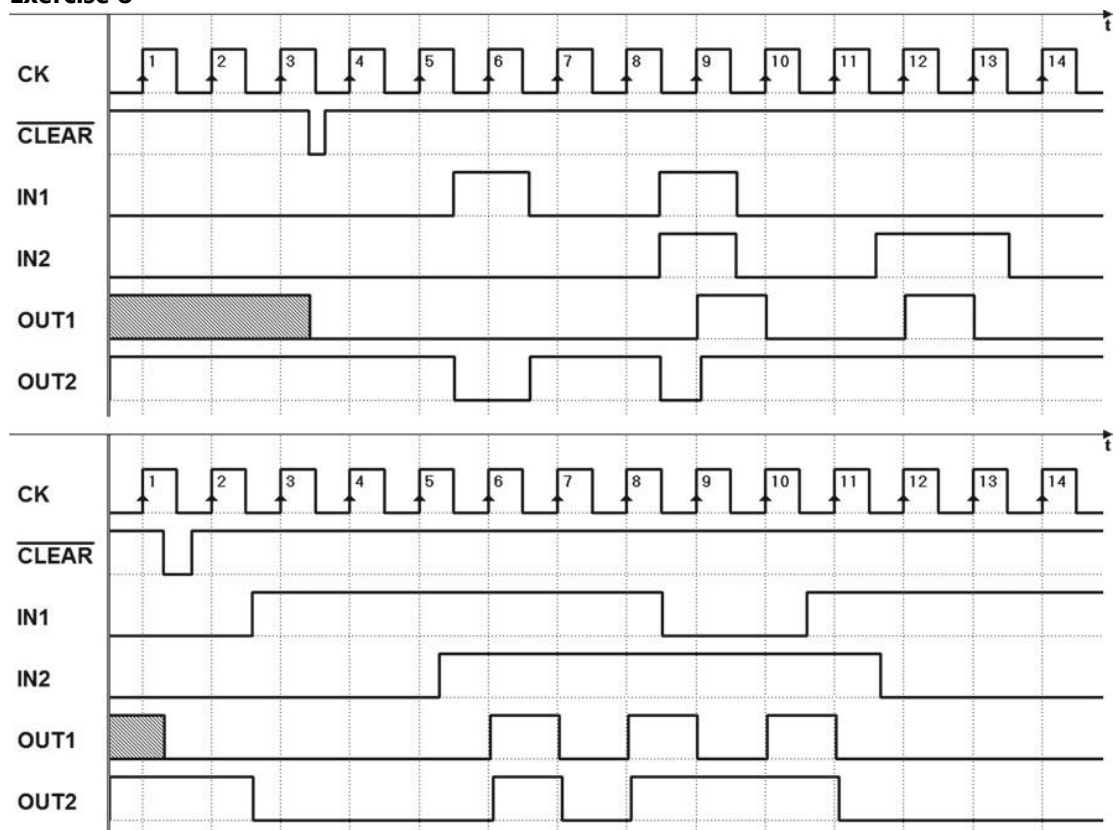




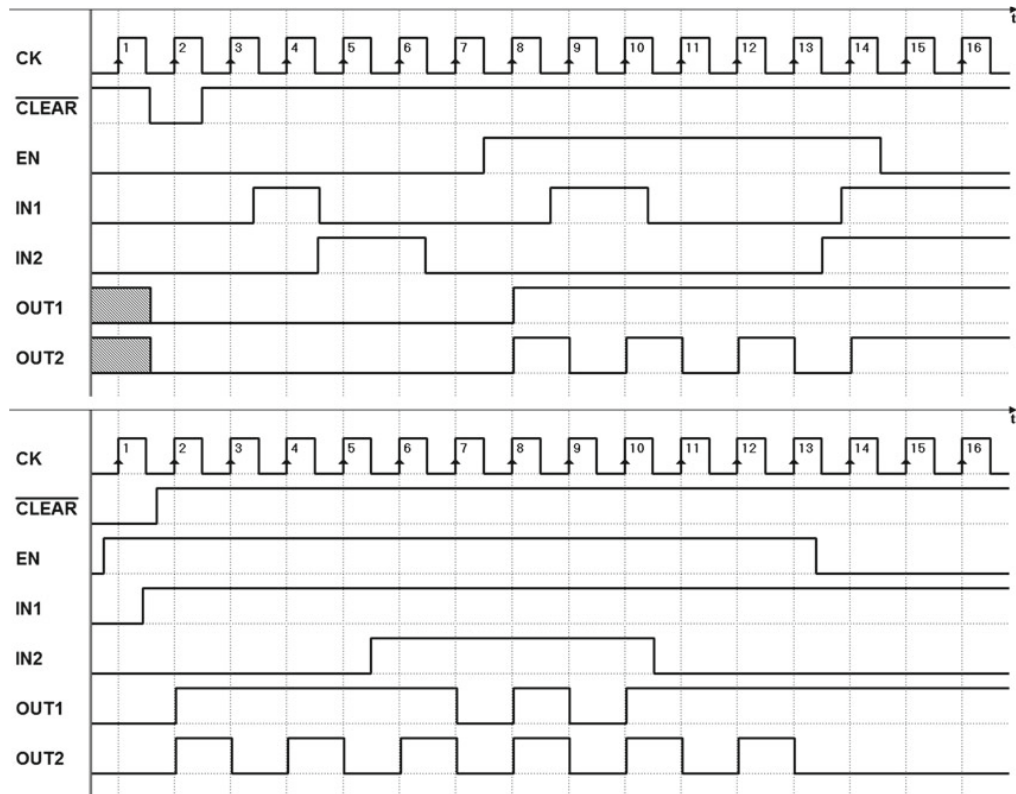
### Exercise 7



### Exercise 8



### Exercise 9



### Exercise 10

