

Accumulator waves

ms23.13

Structural architecture

The simulated accumulator has waveforms as described from 1.

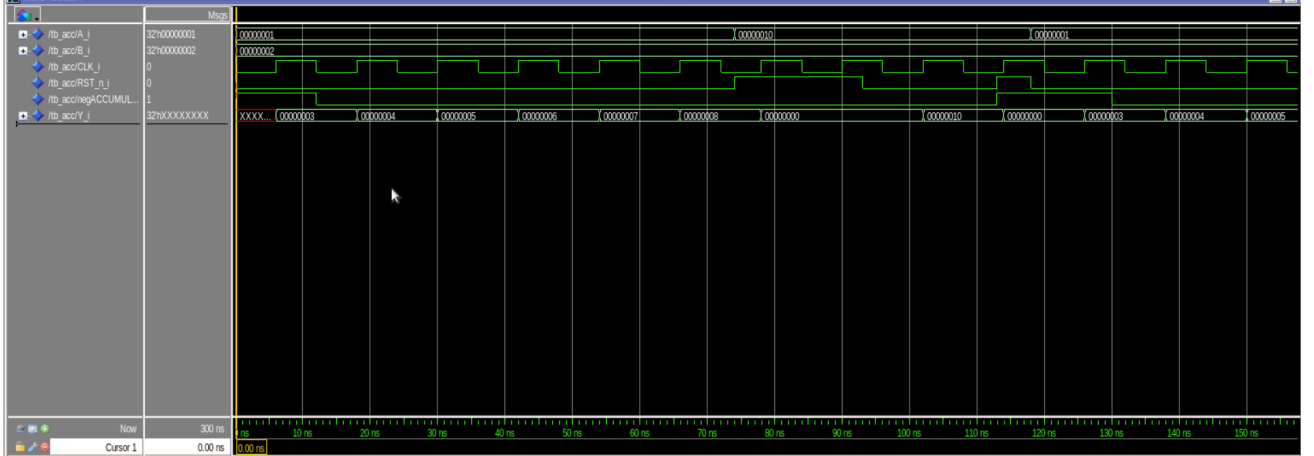


Figure 1: Output wave forms of the simulated accumulator

In particular the figure 2 shows how the output is resetted when RST became 1. Instead, when $ACCUMULATE = 1$, the output of the accumulator is the sum between A and B. This is because the ACCUMULATE signal is considered negated as design choice. Whenever the ACCUMULATE is 0, the output is the sum between A and the register value.

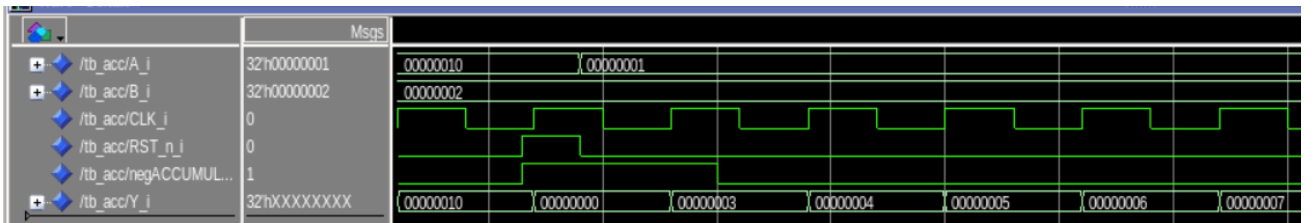


Figure 2: Output zoomed to the change of ACCUMULATE and RST_n