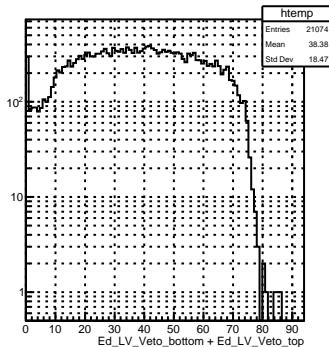
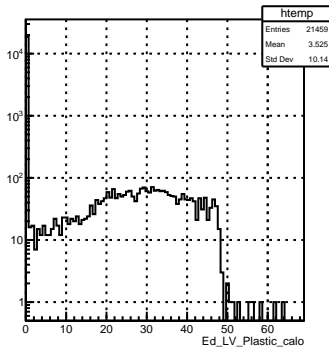


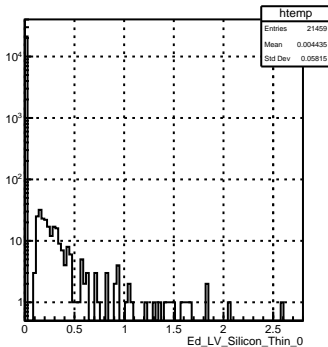
Ed_LV_Veto_bottom + Ed_LV_Veto_top (Ed_LV_Veto_bottom + Ed_LV_Veto_top > 0)



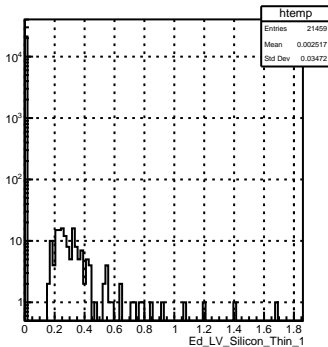
Ed_LV_Plastic_calor



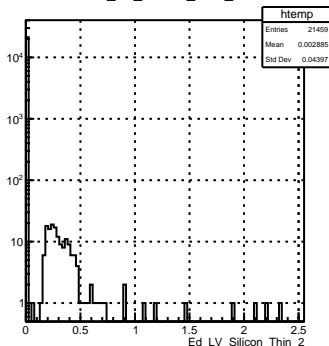
Ed_LV_Silicon_Thin_0



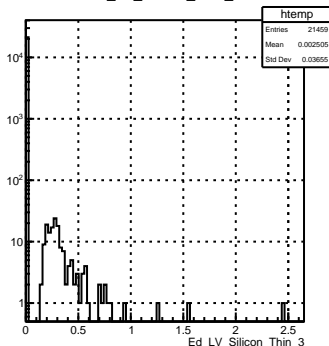
Ed_LV_Silicon_Thin_1



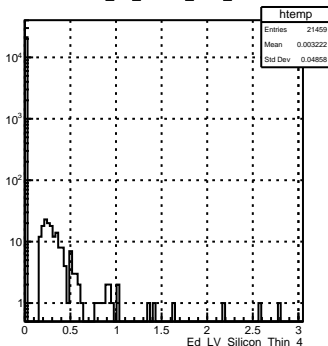
Ed_LV_Silicon_Thin_2



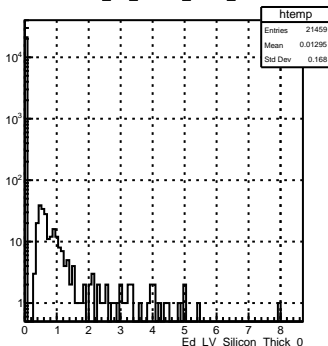
Ed_LV_Silicon_Thin_3



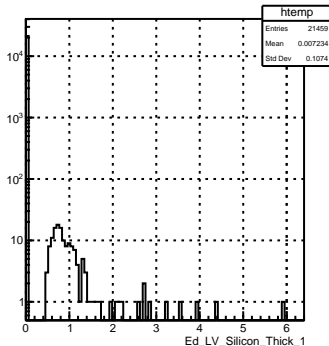
Ed_LV_Silicon_Thin_4



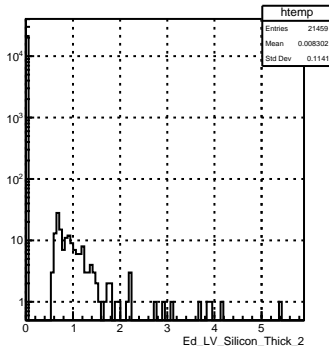
Ed_LV_Silicon_Thick_0



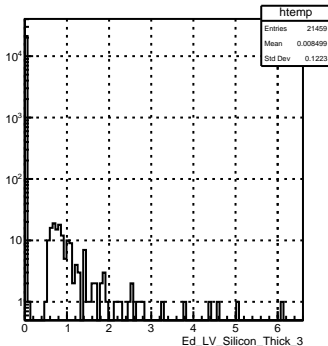
Ed_LV_Silicon_Thick_1



Ed_LV_Silicon_Thick_2



Ed_LV_Silicon_Thick_3



Ed_LV_Silicon_Thick_4

