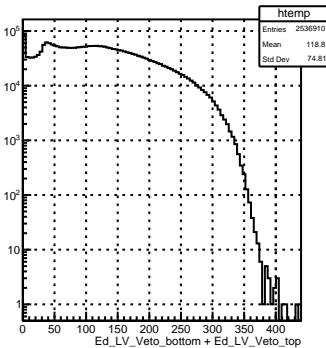
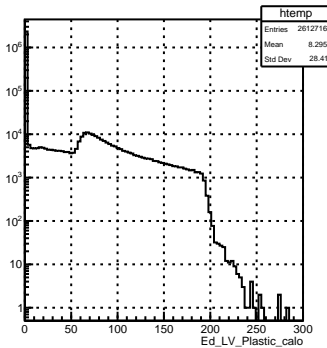


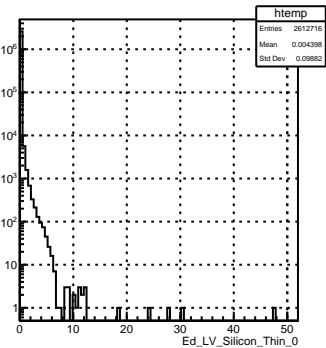
Ed\_LV\_Veto\_bottom + Ed\_LV\_Veto\_top (Ed\_LV\_Veto\_bottom + Ed\_LV\_Veto\_top<40)



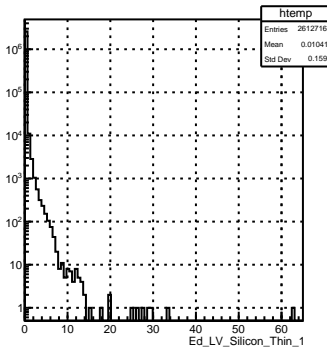
Ed\_LV\_Plastic\_calor



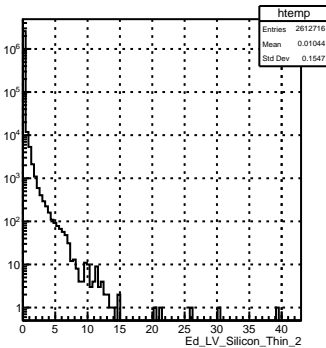
Ed\_LV\_Silicon\_Thin\_0



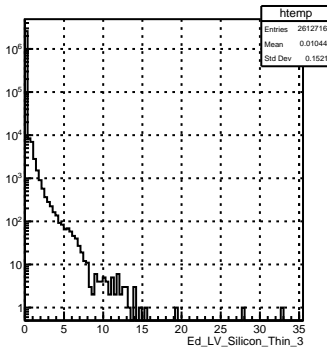
Ed\_LV\_Silicon\_Thin\_1



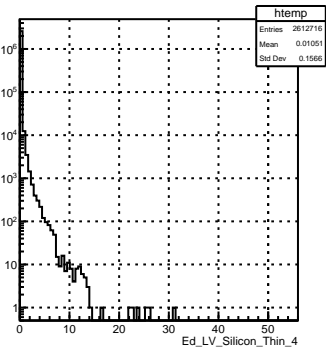
Ed\_LV\_Silicon\_Thin\_2



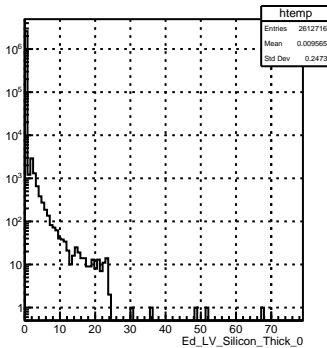
Ed\_LV\_Silicon\_Thin\_3



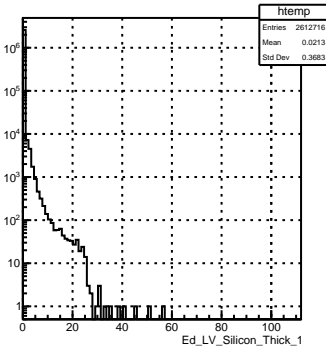
Ed\_LV\_Silicon\_Thin\_4



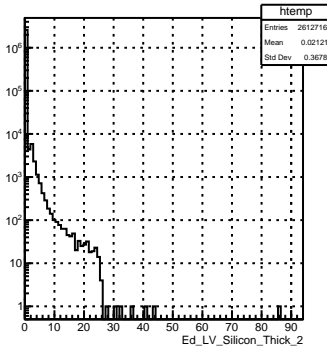
Ed\_LV\_Silicon\_Thick\_0



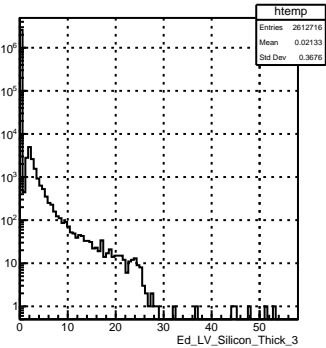
Ed\_LV\_Silicon\_Thick\_1



Ed\_LV\_Silicon\_Thick\_2



Ed\_LV\_Silicon\_Thick\_3



Ed\_LV\_Silicon\_Thick\_4

