

# **MOSFET** – Dual, N-Channel, Small Signal

## 20 V, 540 mA NTZD3154N

#### **Features**

- Low R<sub>DS(on)</sub> Improving System Efficiency
- Low Threshold Voltage
- Small Footprint 1.6 x 1.6 mm
- ESD Protected Gate
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Applications**

- Load/Power Switches
- Power Supply Converter Circuits
- Battery Management
- Cell Phones, Digital Cameras, PDAs, Pagers, etc.

#### MAXIMUM RATINGS (T<sub>.I</sub> = 25°C unless otherwise noted.)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage		V <sub>DSS</sub>	20	V	
Gate-to-Source Voltage			$V_{GS}$	±7.0	V
Continuous Drain Current	Steady	$T_A = 25^{\circ}C$	I_	540	mA
(Note 1)	State	$T_A = 85^{\circ}C$	I <sub>D</sub>	390	
Power Dissipation (Note 1)	Stea	dy State	P <sub>D</sub>	250	mW
Continuous Drain Current (Note 1)	t ≤ 5 s	$T_A = 25^{\circ}C$	I <sub>D</sub>	570	mA
(Note 1)	1 3 0 3	$T_A = 85^{\circ}C$	טי	410	
Power Dissipation (Note 1)	t ≤ 5 s		P <sub>D</sub>	280	mW
Pulsed Drain Current	t <sub>p</sub> = 10 μs		I <sub>DM</sub>	1.5	Α
Operating Junction and Storage Temperature		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	ô	
Source Current (Body Diode)		IS	350	mA	
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)		$T_L$	260	Ĉ	

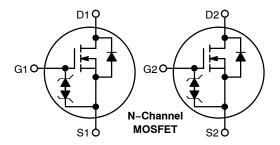
#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 1)	$R_{ hetaJA}$	500	°C/W
Junction-to-Ambient – $t \le 5 s$ (Note 1)		447	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface mounted on FR4 board using 1 in sq pad size (Cu. area = 1.127 in sq [1 oz] including traces).

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> Typ	I <sub>D</sub> Max (Note 1)
	400 mΩ @ 4.5 V	
20	500 mΩ @ 2.5 V	540 mA
	700 mΩ @ 1.8 V	



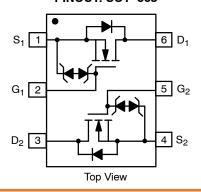


TV = Specific Device Code

M = Date Code= Pb-Free Package

(Note: Microdot may be in either location)

#### PINOUT: SOT-563



#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 4.

#### NTZD3154N

#### **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted.)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		20	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	-	-		14	-	mV/°C
Zero Gate Voltage Drain Current			T <sub>J</sub> = 25°C	-	-	1.0	μΑ
	I <sub>DSS</sub>	V <sub>DS</sub> = 16 V	T <sub>J</sub> = 125°C	-	-	5.0	
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = $\pm 4$	1.5 V	_	-	±5.0	μΑ
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D = 250$	μΑ	0.45	-	1.0	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>	-		-	2.0	-	mV/°C
Drain-to-Source On Resistance		$V_{GS} = 4.5 \text{ V}, I_D = 540$	) mA	-	0.4	0.55	Ω
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 500 mA		-	0.5	0.7	
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 350 mA		-	0.7	0.9	
Forward Transconductance	9FS	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 540 mA		-	1.0	-	S
CHARGES AND CAPACITANCES	•						
Input Capacitance	C <sub>ISS</sub>			-	80	150	pF
Output Capacitance	C <sub>OSS</sub>	V <sub>GS</sub> = 0 V, f = 1.0 MHz, V	<sub>DS</sub> = 16 V	-	13	25	
Reverse Transfer Capacitance	C <sub>RSS</sub>			-	10	20	
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 10 V; I <sub>D</sub> = 540 mA		-	1.5	2.5	nC
Threshold Gate Charge	Q <sub>G(TH)</sub>			-	0.1	-	
Gate-to-Source Charge	Q <sub>GS</sub>			-	0.2	-	
Gate-to-Drain Charge	$Q_{GD}$			-	0.35	-	
SWITCHING CHARACTERISTICS, V <sub>GS</sub> = V	Note 4)				-		
Turn-On Delay Time	t <sub>d(ON)</sub>			-	6.0	-	ns
Rise Time	t <sub>r</sub>	$V_{GS}$ = 4.5 V, $V_{DD}$ = 10 V, $I_D$ = 540 mA, $R_G$ = 10 $\Omega$		-	4.0	-	
Turn-Off Delay Time	t <sub>d(OFF)</sub>			_	16	_	
Fall Time	t <sub>f</sub>			-	8.0	-	
DRAIN-SOURCE DIODE CHARACTERISTIC	s			<u> </u>	<u> </u>	-	-
Forward Diode Voltage	.,	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C	-	0.7	1.2	V
	V <sub>SD</sub>	$I_S = 350 \text{ mA}$	T <sub>J</sub> = 125°C	-	0.6	-	
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = 0 \text{ V}, d_{ISD}/d_t = 100 \text{ A}/\mu\text{s}$	, I <sub>S</sub> = 350 mA	-	6.5	_	ns
	do / lob/ t // / 0						

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Surface–mounted on FR4 board using 1 in. sq. pad size (Cu. area = 1.127 in sq [1 oz] including traces).

3. Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.

4. Switching characteristics are independent of operating junction temperatures.

#### NTZD3154N

#### TYPICAL PERFORMANCE CURVES (T<sub>J</sub> = 25°C unless otherwise noted)

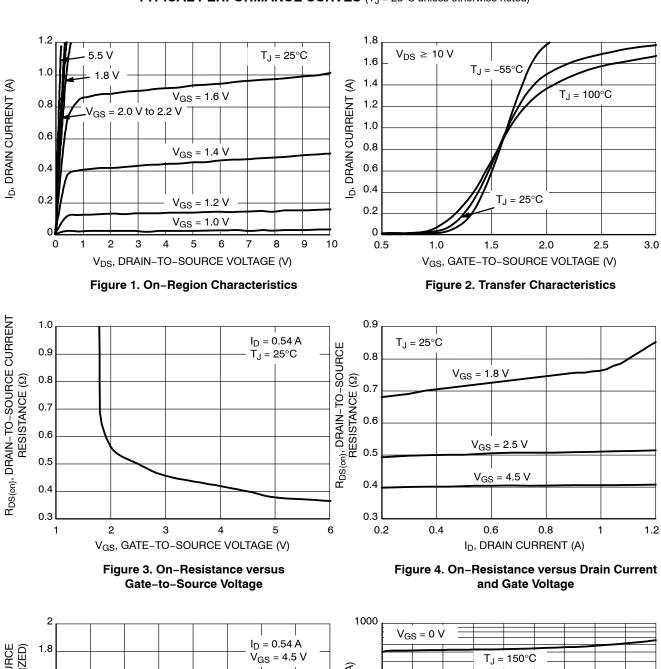


Figure 5. On–Resistance Variation with Temperature

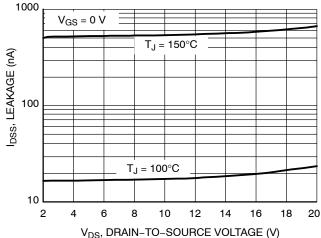
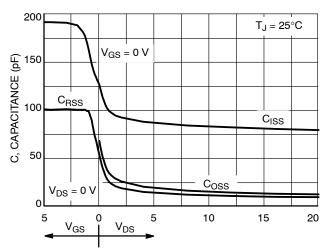


Figure 6. Drain-to-Source Leakage Current versus Voltage

#### NTZD3154N

### TYPICAL PERFORMANCE CURVES ( $T_J = 25^{\circ}C$ unless otherwise noted)

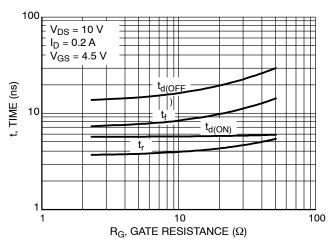


V<sub>DS</sub>, DRAIN-TO-SOURCE VOLTAGE (V) GATE-TO-SOURCE VOLTAGE (V)  $\mathsf{Q}_\mathsf{T}$  $V_{DS}$  $V_{GS}$  $\mathsf{Q}_{\mathsf{GS}}$  $Q_{\text{GD}}$  $I_D = 0.54 A$  $T_J = 25^{\circ}C$ V<sub>GS</sub>, ( 0 0.2 0.4 0.6 8.0 1.2 1.4 1.6 Qg, TOTAL GATE CHARGE (nC)

GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (V)

Figure 7. Capacitance Variation

Figure 8. Gate-to-Source and Drain-to-Source Voltage versus Total Charge



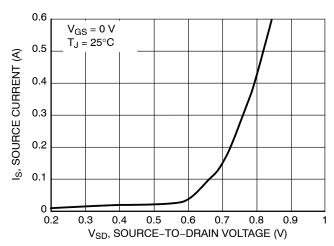


Figure 9. Resistive Switching Time Variation versus Gate Resistance

Figure 10. Diode Forward Voltage versus Current

#### **ORDERING INFORMATION**

Device	Package	Shipping
NTZD3154NT1G		
NTZD3154NT1H	1	4000 / Tarra & David
NTZD3154NT2G	SOT-563 (Pb-Free)	4000 / Tape & Reel
NTZD3154NT2H	(. 2 1.33)	
NTZD3154NT5H		8000 / Tape & Reel

#### **DISCONTINUED** (Note 5)

NTZD3154NT5G	SOT-563 (Pb-Free)	8000 / Tape & Reel
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<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>5.</sup> **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on <a href="https://www.onsemi.com">www.onsemi.com</a>.





STYLE 4:

PIN 1. COLLECTOR 2. COLLECTOR 3. BASE

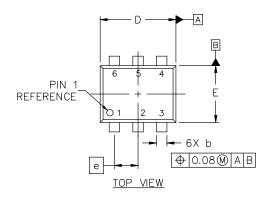
4. EMITTER
5. COLLECTOR
6. COLLECTOR

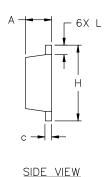
#### SOT-563-6 1.60x1.20x0.55, 0.50P CASE 463A ISSUE J

**DATE 15 FEB 2024** 

#### NOTES:

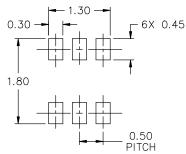
- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.





DIM NDM. MIN. MAX. 0.50 0.55 0.60 Α 0.17 0.22 0.27  $\subset$ 0.08 0.13 0.18 D 1.50 1.60 1.70 Ε 1.10 1.20 1.30 9 0.50 BSC Н 1.50 1.60 1.70 0.20 0.30 L 0.10

MILLIMETERS



STYLE 1:	STYLE 2:	STYLE 3:
PIN 1. EMITTER 1	PIN 1. EMITTER 1	PIN 1. CATHODE 1
2. BASE 1	2. EMITTER 2	2. CATHODE 1
3. COLLECTOR 2	3. BASE 2	3. ANODE/ANODE 2
4. EMITTER 2	4. COLLECTOR 2	4. CATHODE 2
5. BASE 2	5. BASE 1	5. CATHODE 2
6. COLLECTOR 1	6. COLLECTOR 1	6. ANODE/ANODE 1

STYLE 6: PIN 1. CATHODE 2. ANODE

3. CATHODE

4. CATHODE 5. CATHODE

6. CATHODE

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

3. ANDDE

4. ANDDE 5. CATHODE

6. CATHODE

STYLE 5: PIN 1. CATHODE 2. CATHODE

### GENERIC MARKING DIAGRAM\*



XX = Specific Device Code
M = Month Code
• = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 10:	STYLE 11:
PIN 1. CATHODE 1	PIN 1. EMITTER 2
2. N/C	2. BASE 2
3. CATHODE 2	3. COLLECTOR 1
4. ANODE 2	4. EMITTER 1
5. N/C	5. BASE 1
6. AN□DE 1	6. COLLECTOR 2

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