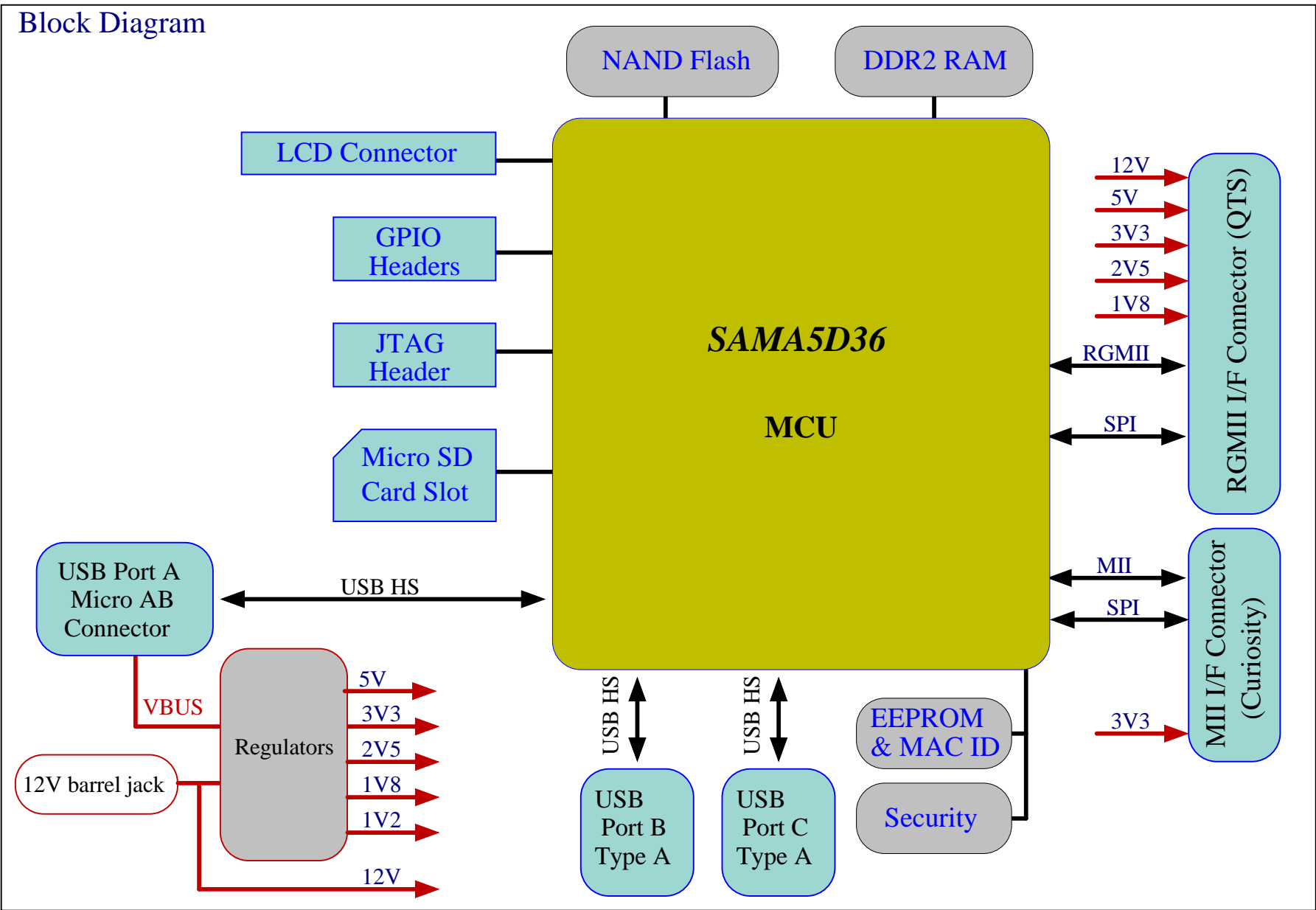
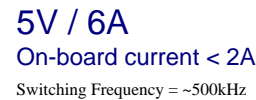


Ethernet Switch FDB Base Board with Atmel SAMA5D36

Table of Contents	
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5	SDRAM
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Revision History			
Revision	Date	Revision Summary	Author
A	11/16/2016	Initial release	Carl Johnson
B	09/08/2017	Errata fixed, 12V added, new minor features	Carl Johnson
B1	10/02/2017	Reversed Rt/Rb for 3V3 regulator divider.	Carl Johnson
C	12/07/2017	Replaced oscillators with crystals for 12MHz and 32.768kHz, revised legend	
1	03/20/2018	Released	Jennifer Hancock





SAMA5D36 Power Sequencing Requirements

Power up:
All 1V2 rails should come up at least 1ms after the other power rails.
Reset should be release after 1V2 comes up.

Power down:
Voltage rails can be removed in any sequence.

