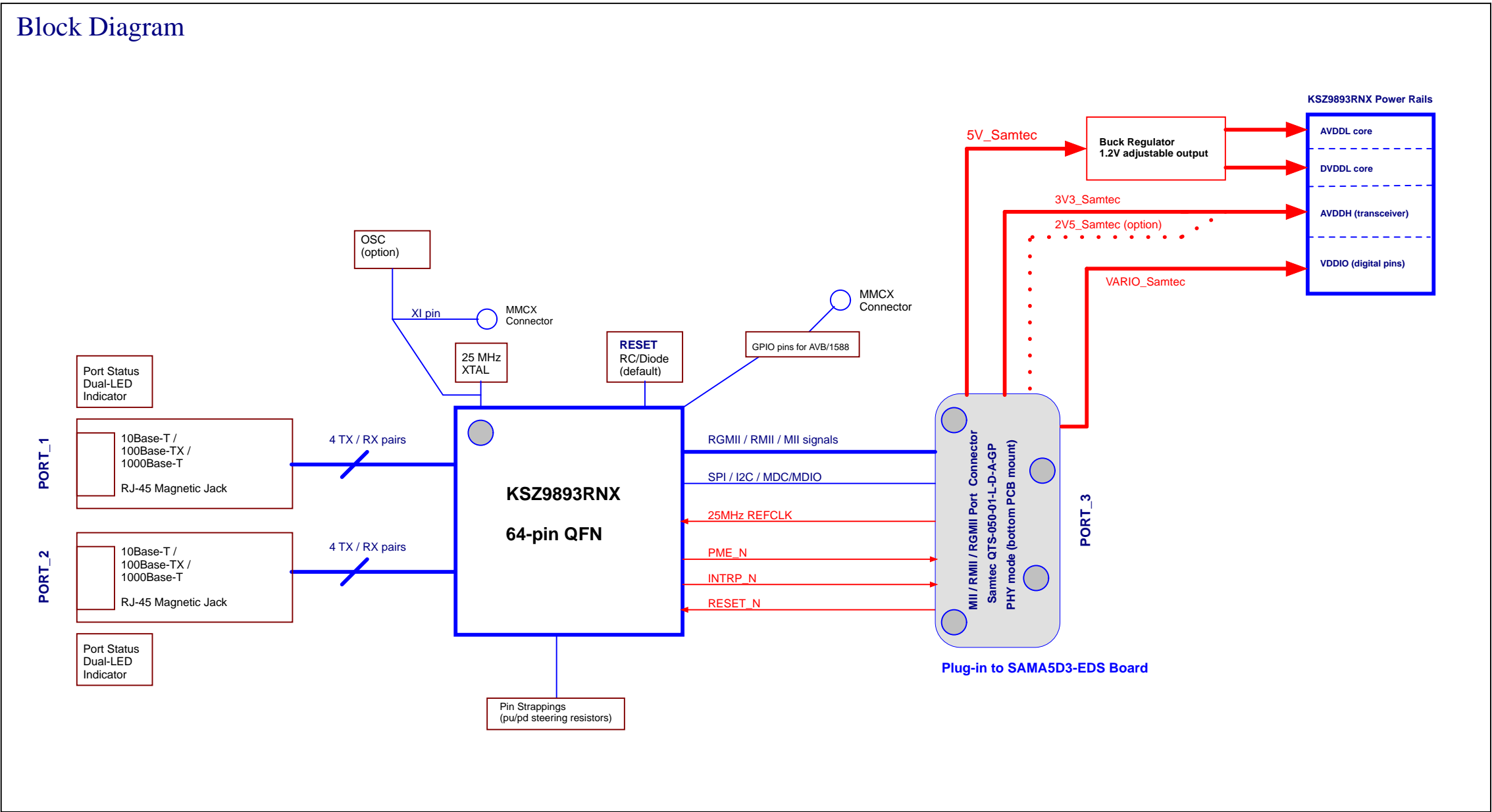
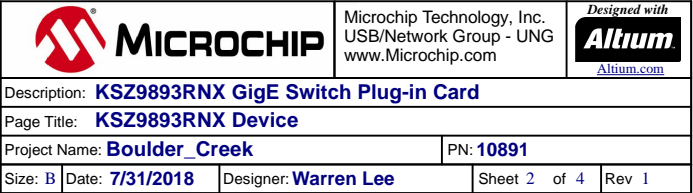
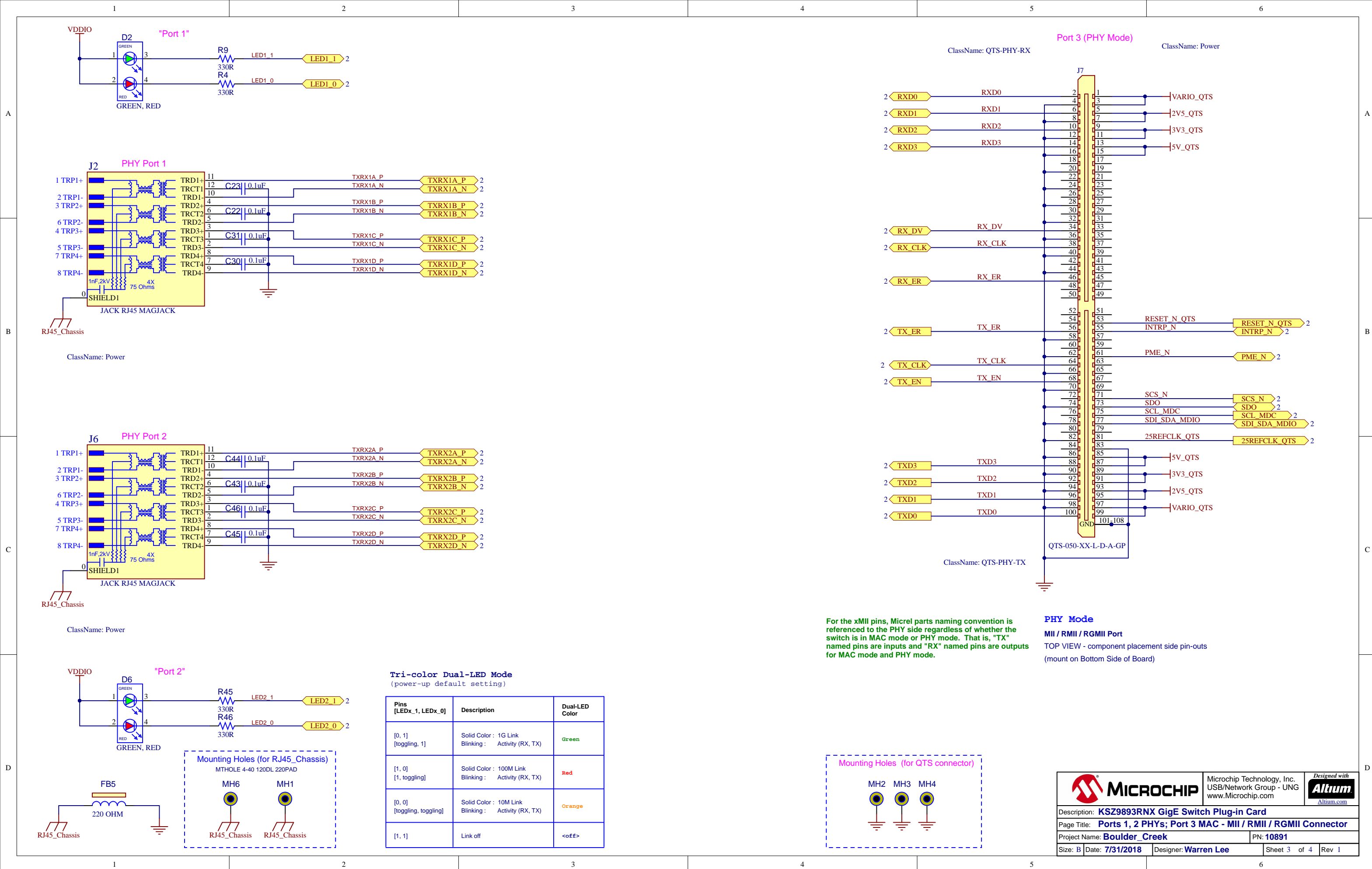


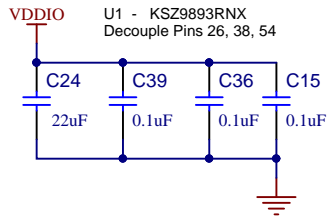
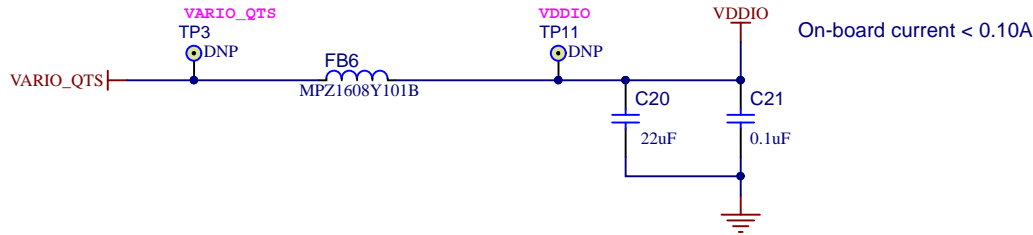
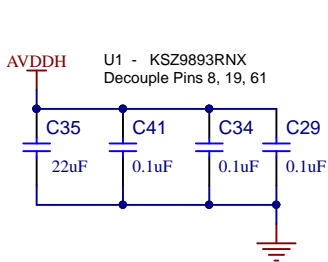
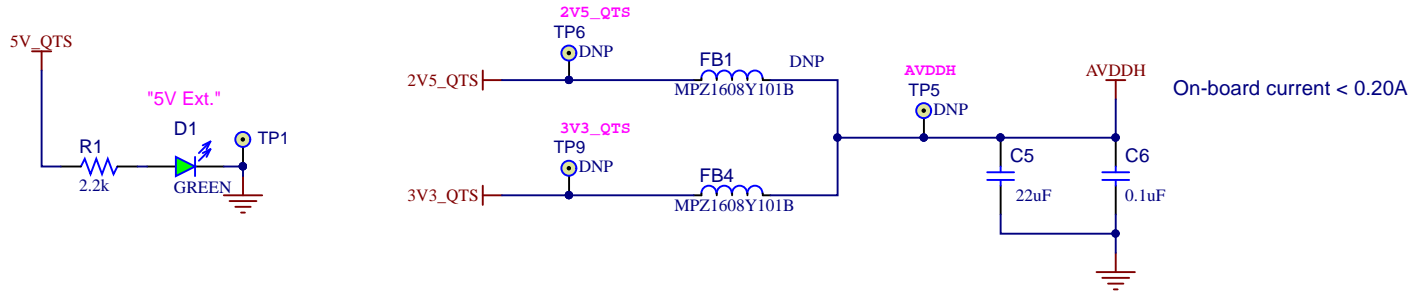
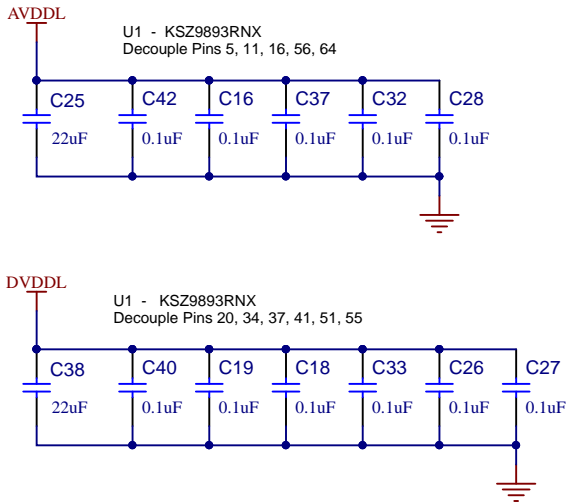
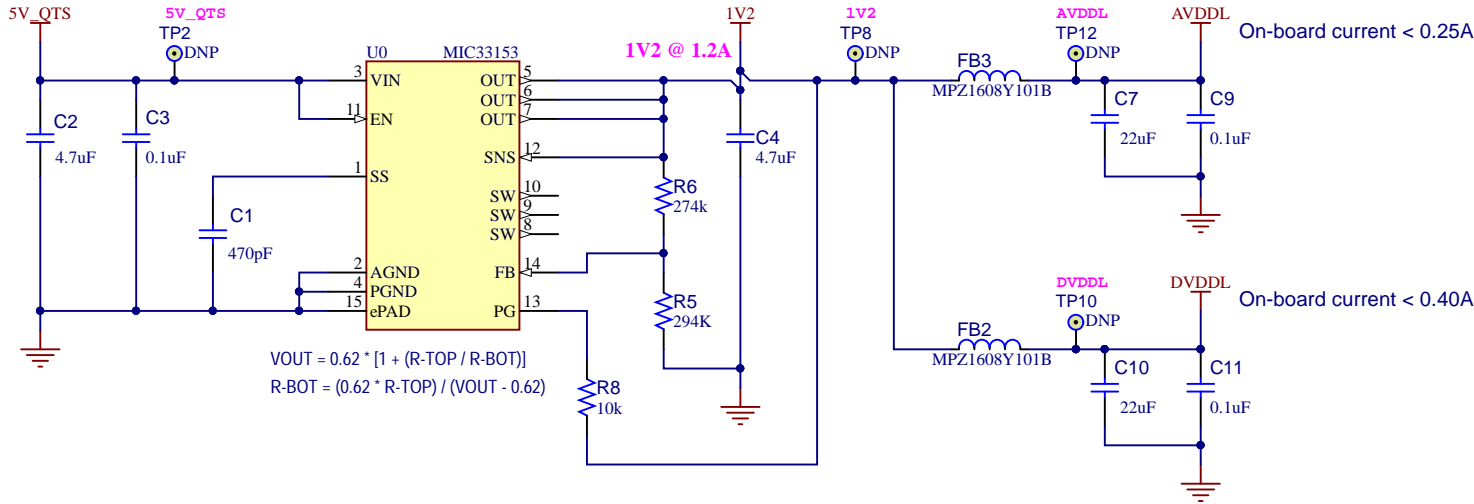
KSZ9893RNX 3-Port Gigabit Ethernet Switch Plug-in Card

Table of Contents				Revision History			
Sheet	Description			Revision	Date	Revision Summary	Author
1	Title Page & Block Diagram			00A	7/12/2017	Initial Release	Warren Lee
2	KSZ9893RNX Device			1	4/6/2018	BOM update to MCL components; Some minor text clean-up	Warren Lee
3	Ports 1, 2 PHYs; Port 3 MAC - MII / RMII / RGMII Connector				5/3/2018	Change Y1 from OSC to XTAL	Warren Lee
4	Board Power I/O and Regulators						

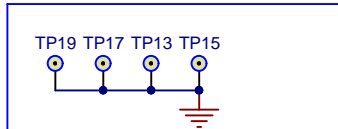




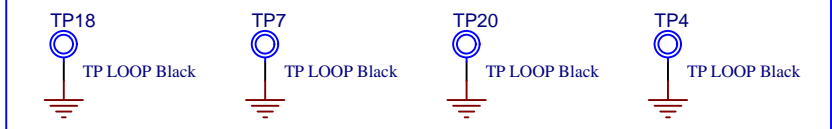




GND Test Pads





GND Test Points



Fiducials



		Microchip Technology, Inc. USB/Network Group - UNG www.Microchip.com		Designed with 	
Description: KSZ9893RNX GigE Switch Plug-in Card					
Page Title: Board Power I/O and Regulators					
Project Name: Boulder_Creek				PN: 10891	
Size: B	Date: 7/31/2018	Designer: Warren Lee	Sheet 4 of 4	Rev 1	