

Ethernet Switch FDB Base Board with Atmel SAMA5D36

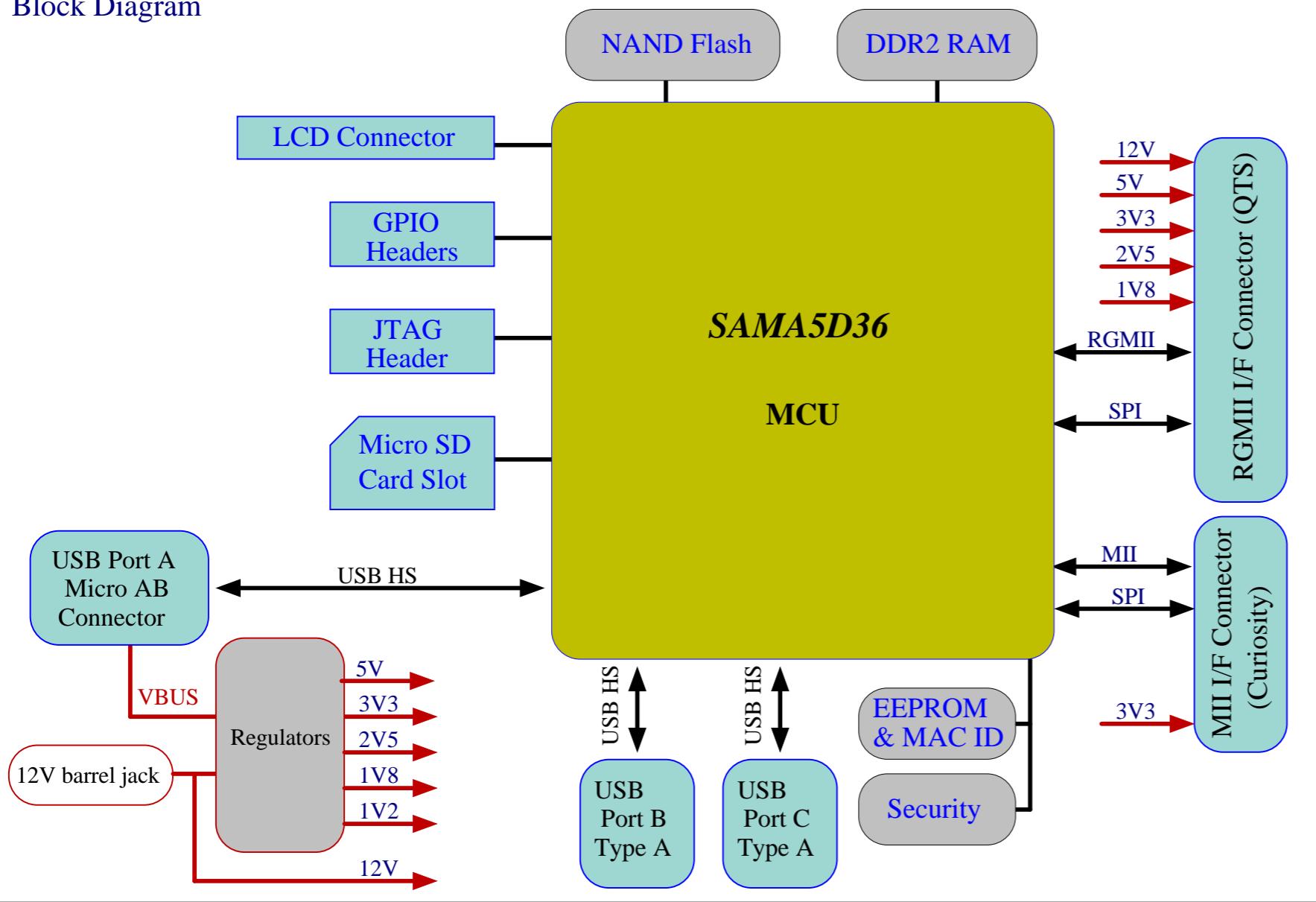
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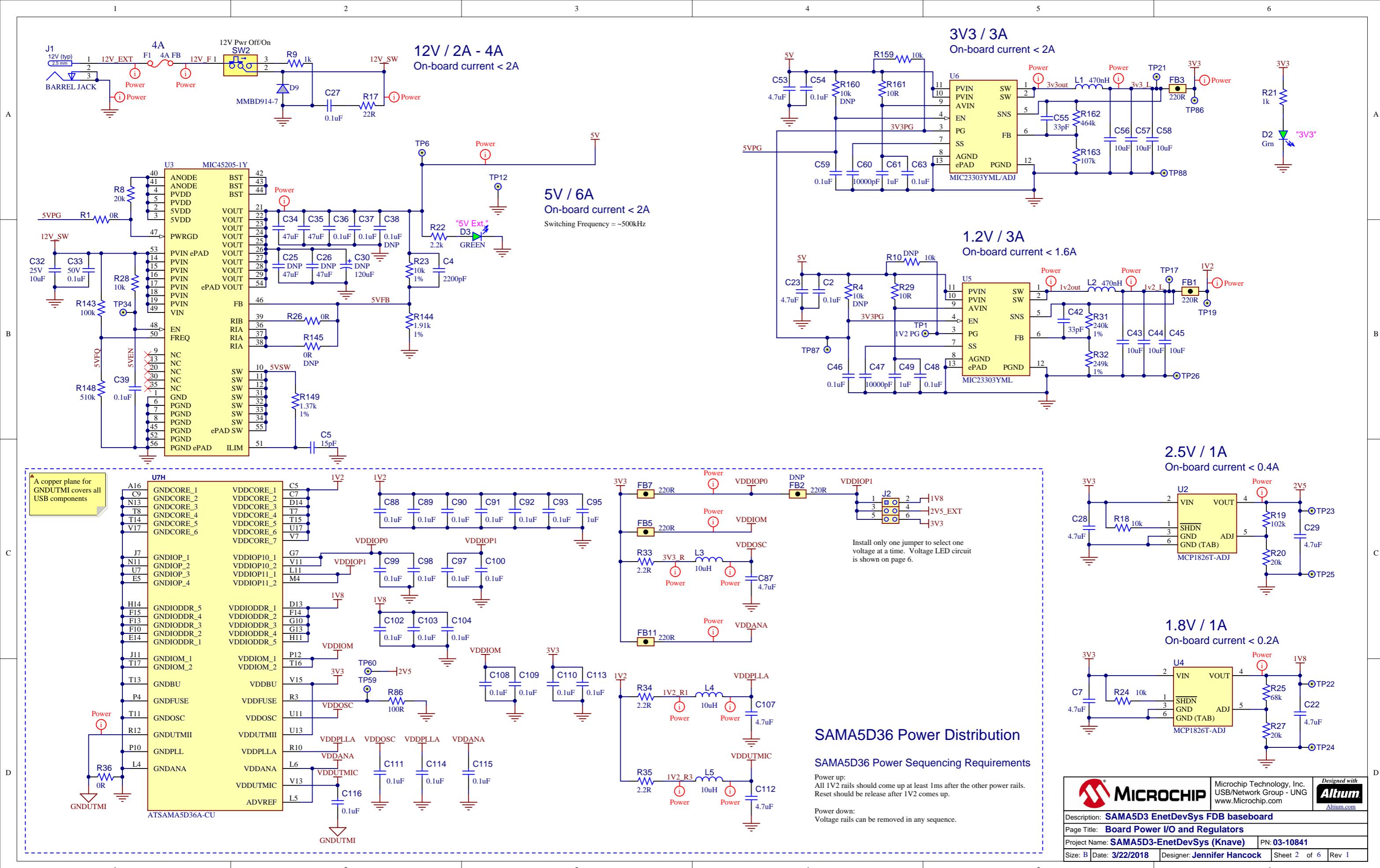
Sheet	Description
1	Title Page & Block Diagram
2	Power - Regulation and MCU
3	SOC Main - Flash, SD card, USB
4	RGMII and MII, EEPROM, Security, LCD
5	SDRAM
6	SOC GPIO

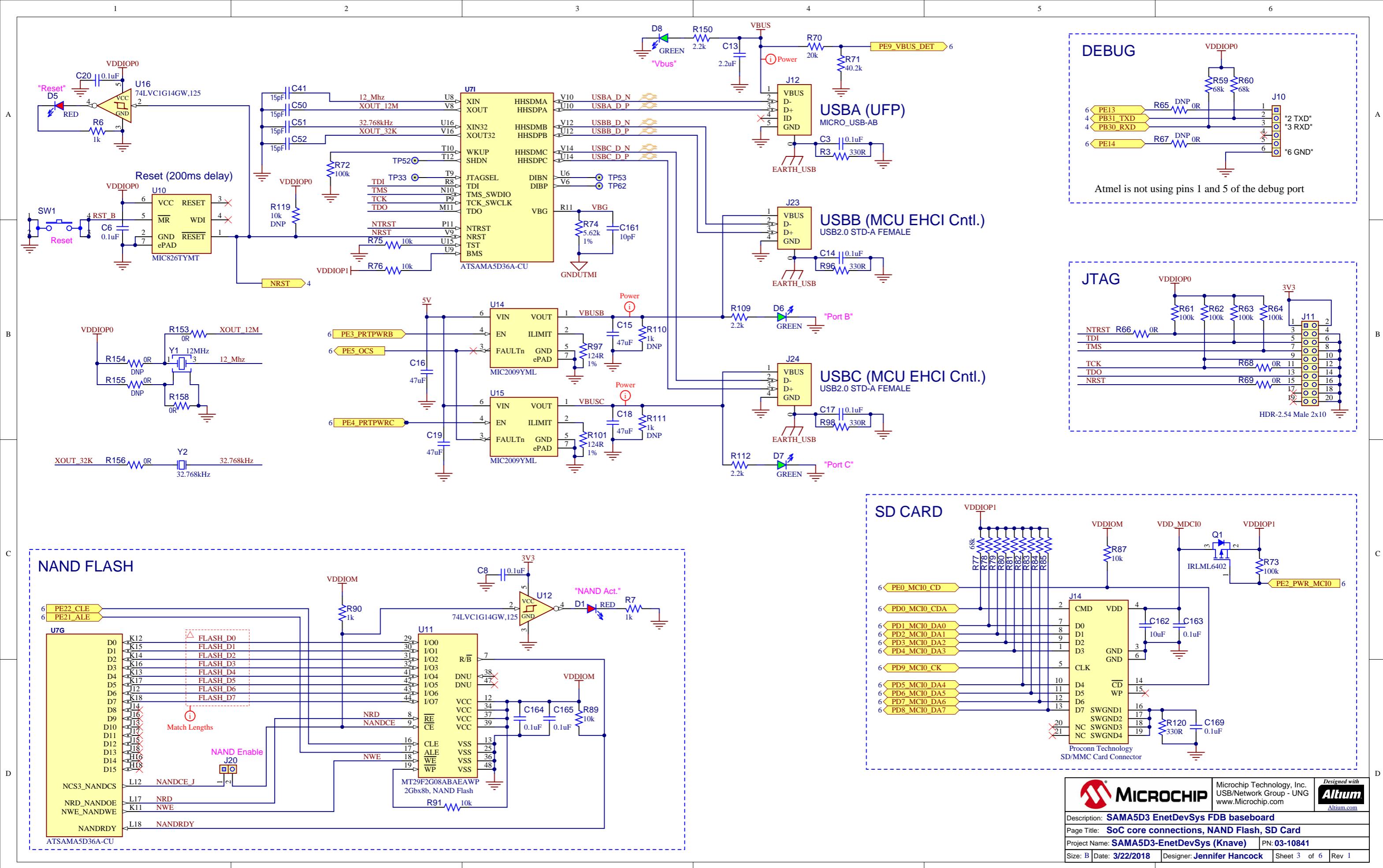
Revision History

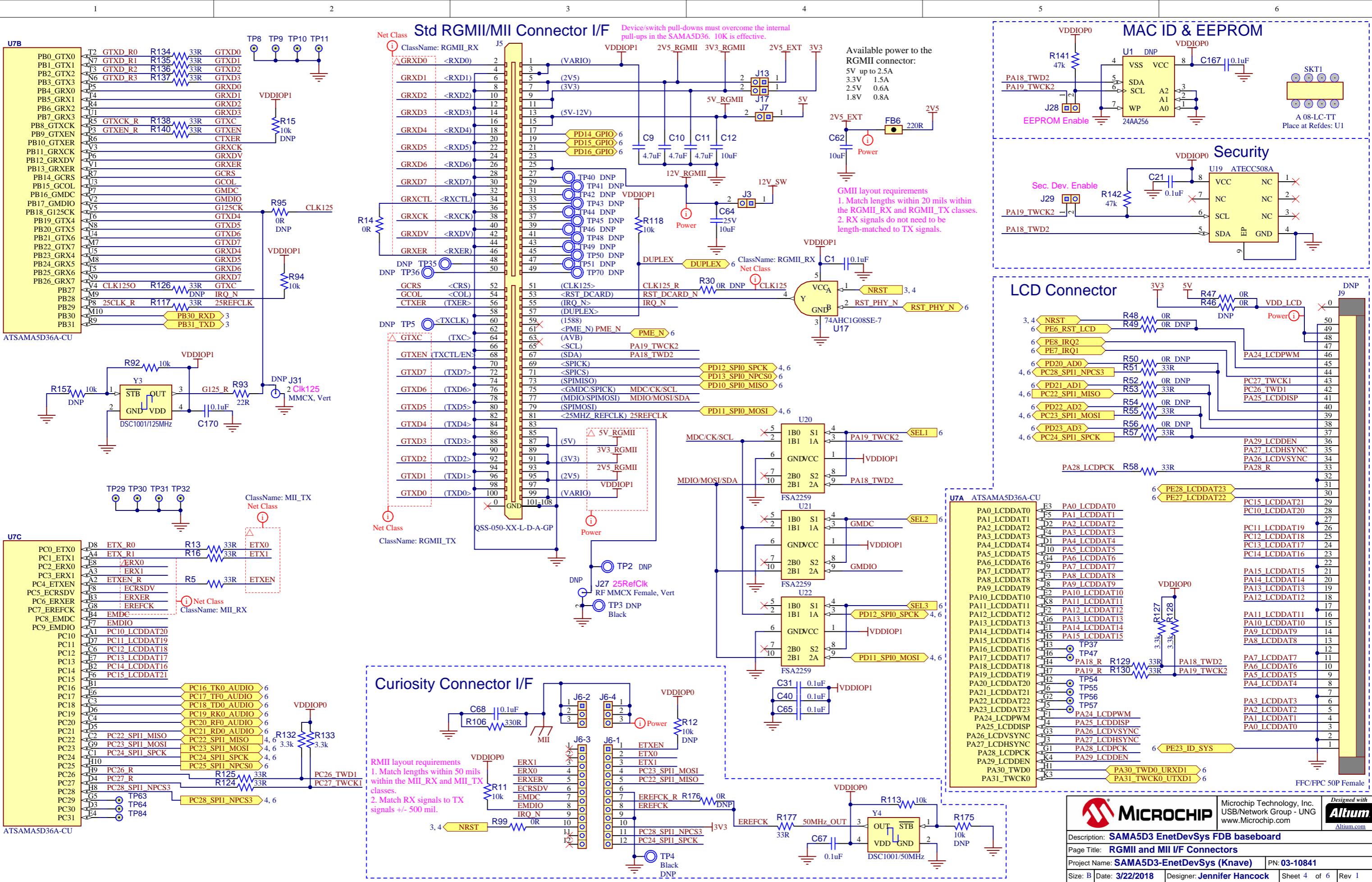
Revision	Date	Revision Summary	Author
A	11/16/2016	Initial release	Carl Johnson
B	09/08/2017	Errata fixed, 12V added, new minor features	Carl Johnson
B1	10/02/2017	Reversed Rt/Rb for 3V3 regulator divider.	Carl Johnson
C	12/07/2017	Replaced oscillators with crystals for 12MHz and 32.768kHz, revised legend	
1	03/20/2018	Released	Jennifer Hancock

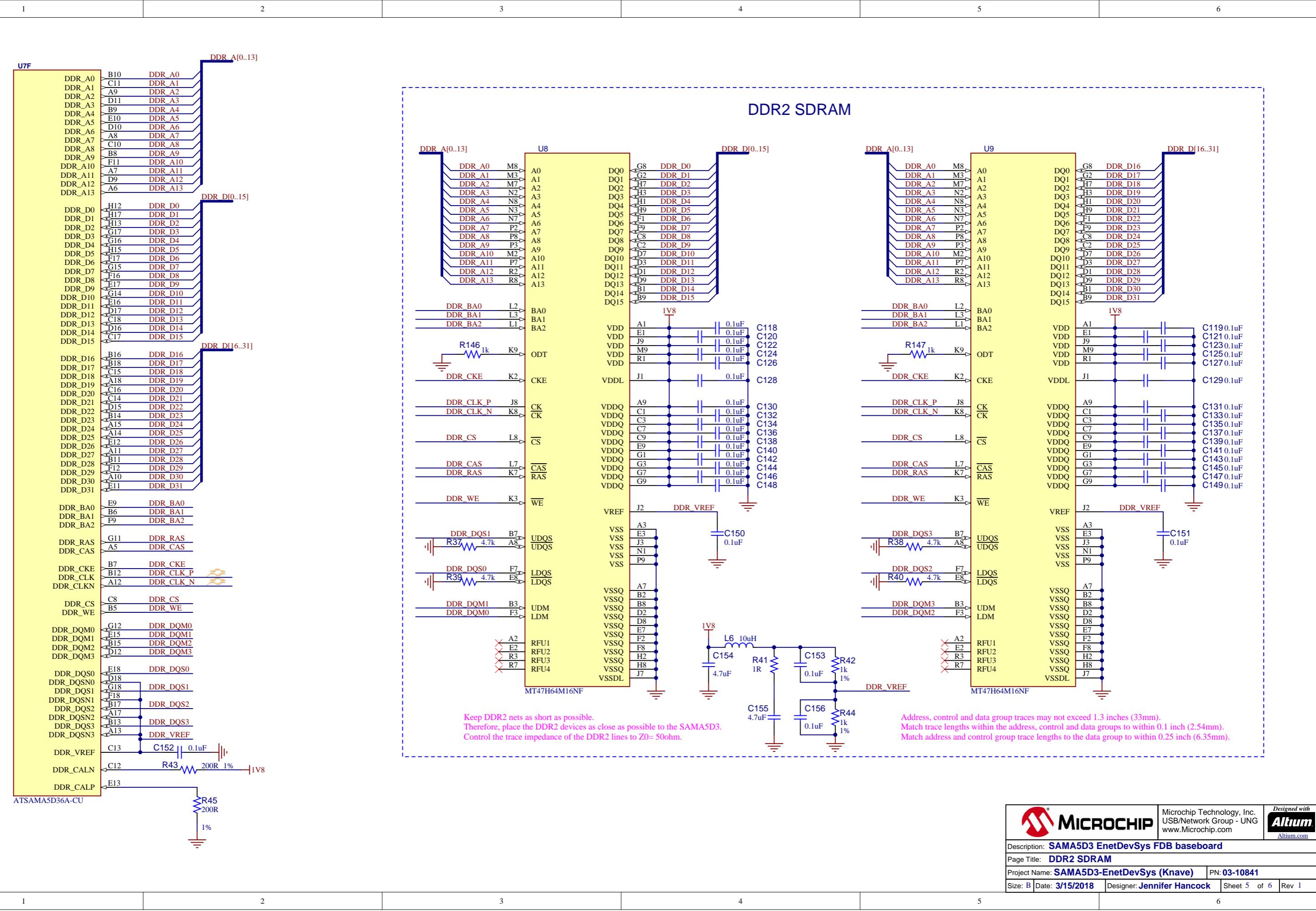
Block Diagram

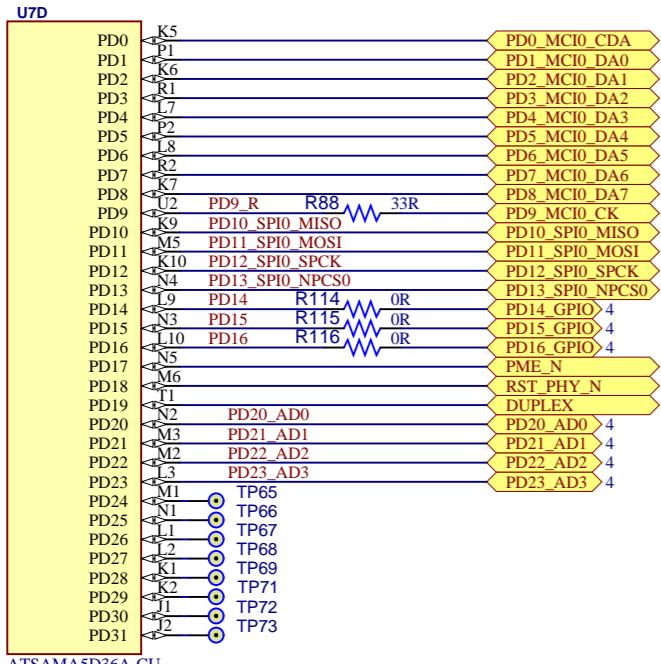
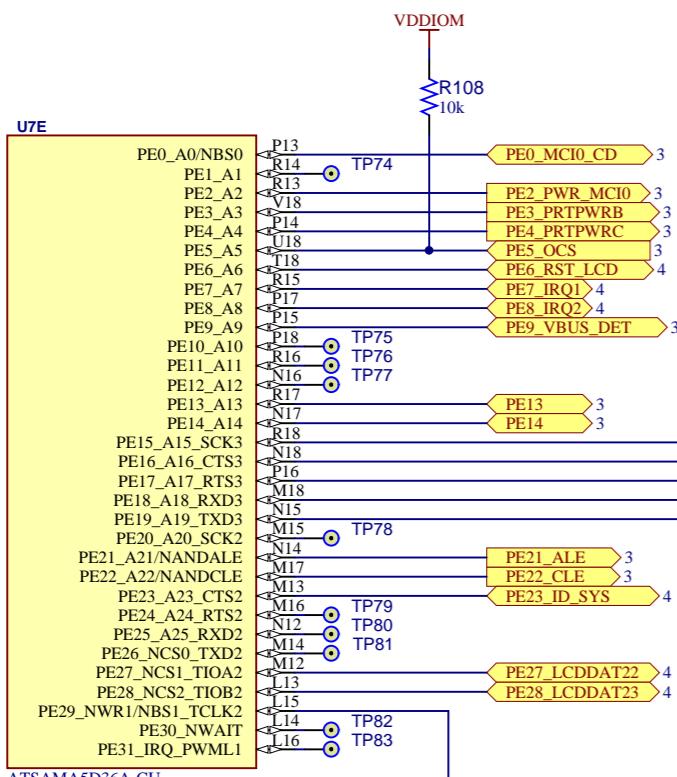
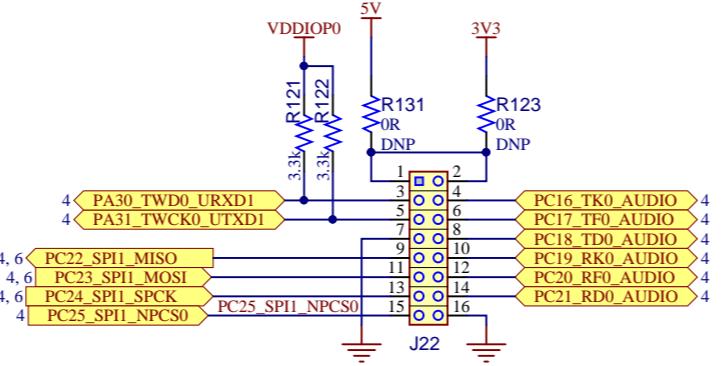
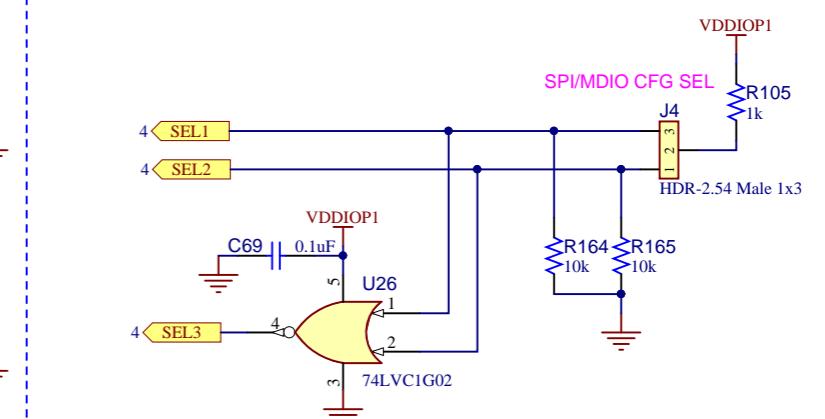
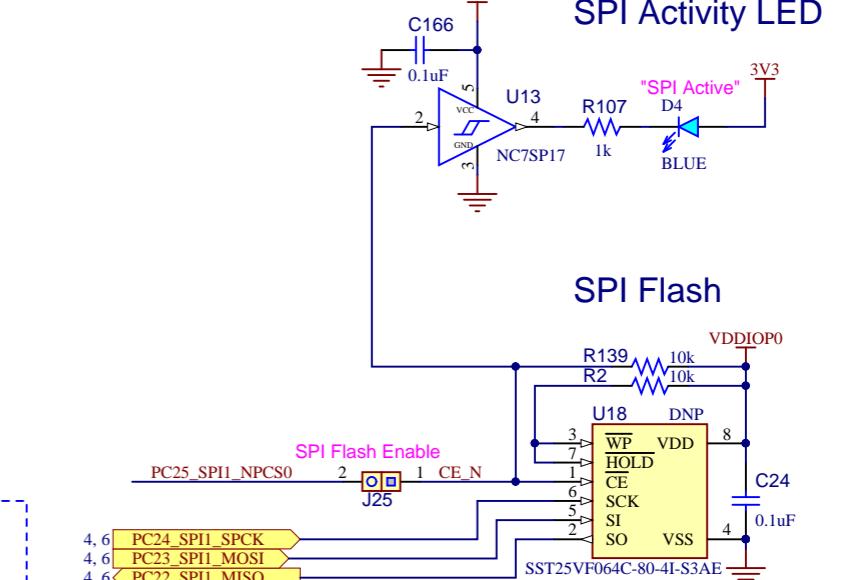
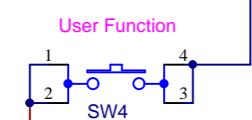
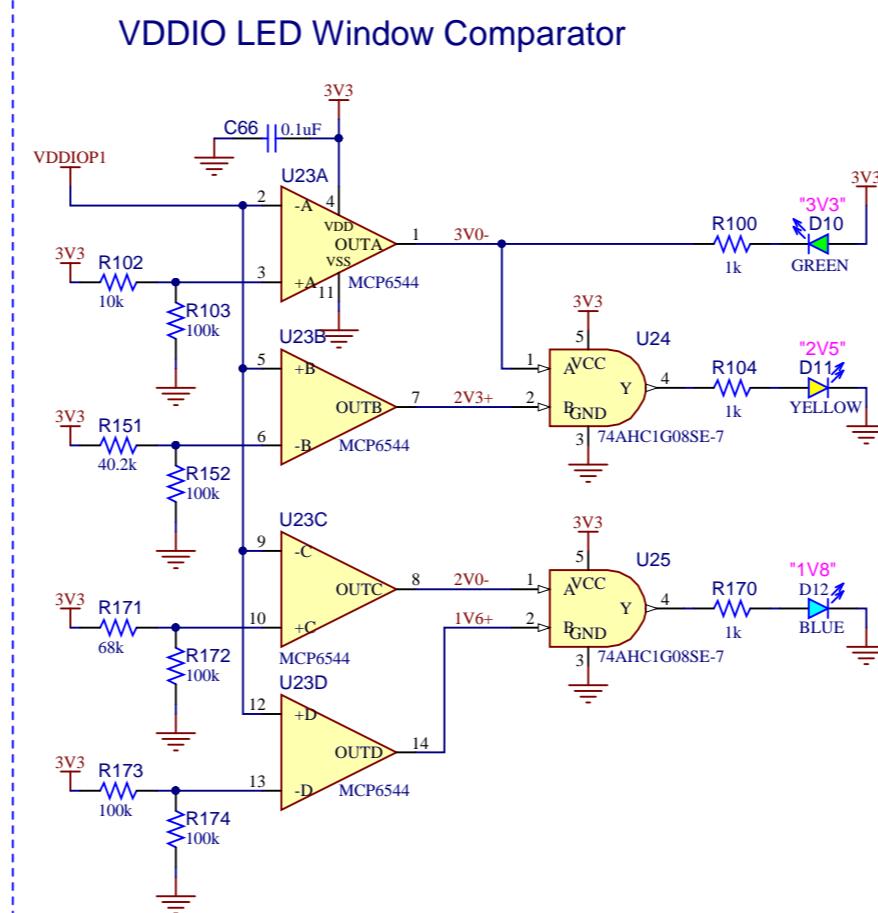




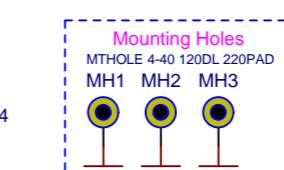






**I/O Connector #1****I/O Connector #2**

Fiducials



	MICROCHIP	Microchip Technology, Inc.
		USB/Network Group - UNG
		www.Microchip.com
		Designed with
		Allium
		Allium.com

Description: SAMA5D3 EnetDevSys FDB baseboard

Page Title: SoC GPIO, LCD Connector, misc headers

Project Name: SAMA5D3-EnetDevSys (Knave)

PN: 03-10841

Size: B Date: 3/22/2018 Designer: Jennifer Hancock Sheet 6 of 6 Rev 1