ENT-AN1249-1.0.1

Software Release Notification

Ocelot Unmanaged

Microsemi

Power Matters.™

Contents

1   Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

1.1   Revision 1.0

Revision 1.0 was published in September 2017. It was the first publication of this document.

2 Release Notification

This notification announces the availability of a new software release. For information about specific changes, see the release notes that accompany the software package.

Note: This software package is under MIT license and can be downloaded directly after accepting the terms and conditions given at the downloading web page.

This notification covers the VSC6825SDK Ocelot Unmanaged Application Software Package for 8051-based Ocelot Unmanaged Enterprise Switches.

3   Release Information

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Supported Chipset:
     Ocelot (VSC7511, VSC7512)


Supported Boards:
     Four types (F11, F10P, F5, F4P) based on Reference Board PCB121.
          . 'D' means device chip port.
          . 'x' means unused port.


     - F11 : 11-port Giga switch. (8*1G Copper + 2*2.5G SFP + 1*1G Copper)
                 --------------------------------------------------
                 | 2(D5) | 4(D7) | 6(D1) | 8(D3) | 10(D10, 2.5G) |
                 --------------------------------------------------
                 | 1(D4) | 3(D6) | 5(D0) | 7(D2) |  9( D8, 2.5G) |    Copper (daughter board):
                 --------------------------------------------------                 11(D9)
     - F10P: 10-port Giga switch + a PCIe inteface.
        (8*1G Copper + 1*2.5G SFP + 1*1G SFP + 1*PCIe)
                 --------------------------------------------------
                 | 2(D5) | 4(D7) | 6(D1) | 8(D3) | 10(D9,   1G) |
                 --------------------------------------------------
                 | 1(D4) | 3(D6) | 5(D0) | 7(D2) |  9(D8, 2.5G) |   PCIe: (D10)
                 --------------------------------------------------
     - F5  : 5-port Giga switch. (4*1G Copper + 1*1G Fiber)
                 ----------------------------------------
                 | x | x | 2(D1) | 4(D3) | Fiber: 5(D10) |
                 ----------------------------------------
                 | x | x | 1(D0) | 3(D2) |   x           |
                 ----------------------------------------
     - F4P : 4-port Giga switch + a PCIe inteface. (4*1G Copper + 1*PCIe)
                 ------------------------
                 | x | x | 2(D1) | 4(D3) |
                 ------------------------
                 | x | x | 1(D0) | 3(D2) |    PCIe: (D10)
                 ------------------------


Supported PHYs:
      - MINI (VSC7511, VSC7512): F11/F10P/F5/F4P internal PHY port (1-4)
      - ELISE (VSC8514): F11/F10P external PHY port (5-8)
      - COBRA (VSC8211, VSC8221): F11 NPI port (11)


The following SFP Modules have been verified by our SQA:
     - SPT-SFP+C1,    10G SFP+ DAC 1M Passive Cable
     - FTLF8519P3BNL, Finisar - 1000Base-SX 850nm MM 550m
     - SFP-SX-M2002,  Excom   - 1000Base-SX 850nm MM 550m
     - SFP-LH-S1010,  Excom   - 100Base-LX 1310nm SM 10km
     - SFP-SX-M1002,  Excom   - 100Base-SX 1310nm MM  2km
     - PS: Copper SFP module is unsupported.


LEDs Definition: Based on SGPIO
     - System LED
         Blink GREEN when normal operation, Blink RED when port loop is detected.
     - Port LEDs
```

```
        Light GREEN-Activity when 1/2.5G link-up state, YELLOW(GREEN+RED)-Activity
        when 10M/100M link-up state.


Requirements:
    - Console baudrate: 115200(8-N-1)
    - Serial NOR Flash size 64 bytes at least.
    - Make sure your Keil compiler tool support uVision4 since the project file is based
      on it.
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