Richard Homan

Microcomputer Design

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March 20, 2024

Design Report #9

I. Time: 16 Hours

II. Accomplishments

    A. Completed soldering board

    B. Found power and serial issues with board, rectified by cutting traces

    C. Board serial successfully recognized by Mac and Windows machines

    D. Successfully programmed CPLD with test program

III. Planned Work

    A. Write CPLD logic

    B. Write serial test program

    C. Burn serial test program onto ROM and test functionality

    D. Combine serial and written monitor program, burn to ROM, and test

IV. Project Completion

    A. Hardware: 98%; Assembly complete, actively looking out for mistakes during software testing

    B. Software: 80%; Monitor program 95% done, serial recognized and able to accept characters

V. Help from Lab Tech

    A. n/a