

Richard Homan

Microcomputer Design

Dr. Joonwan Kim

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Design Report #8

I. Time: 5 Hours

II. Accomplishments

A. Began soldering board

III. Planned Work

A. Complete board assembly

B. Program CPLD logic

C. Test burn EEPROM

IV. Project Completion

A. Hardware: 95%; Assembly almost complete

B. Software: 75%; Monitor program 95% done

V. Help from Lab Tech

A. n/a