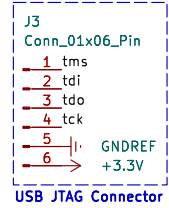
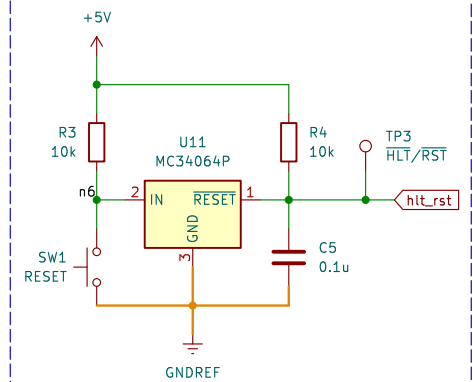


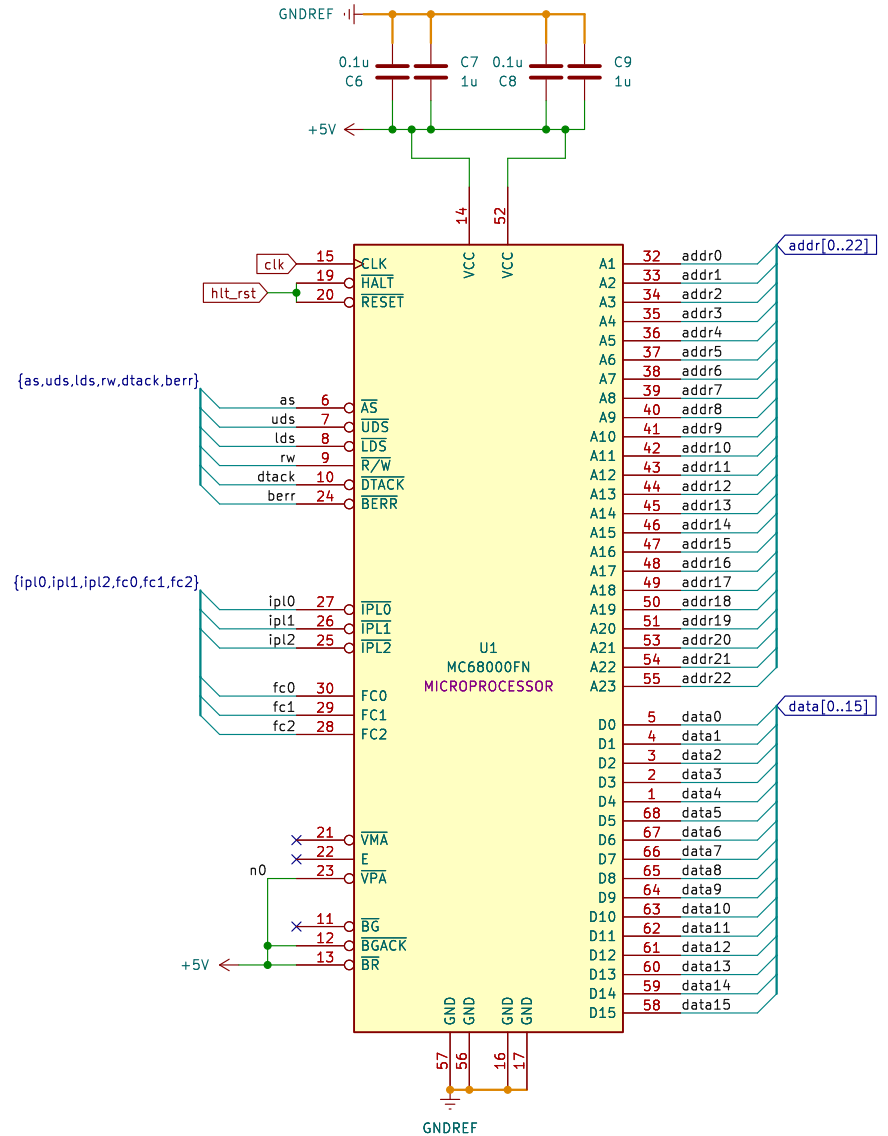
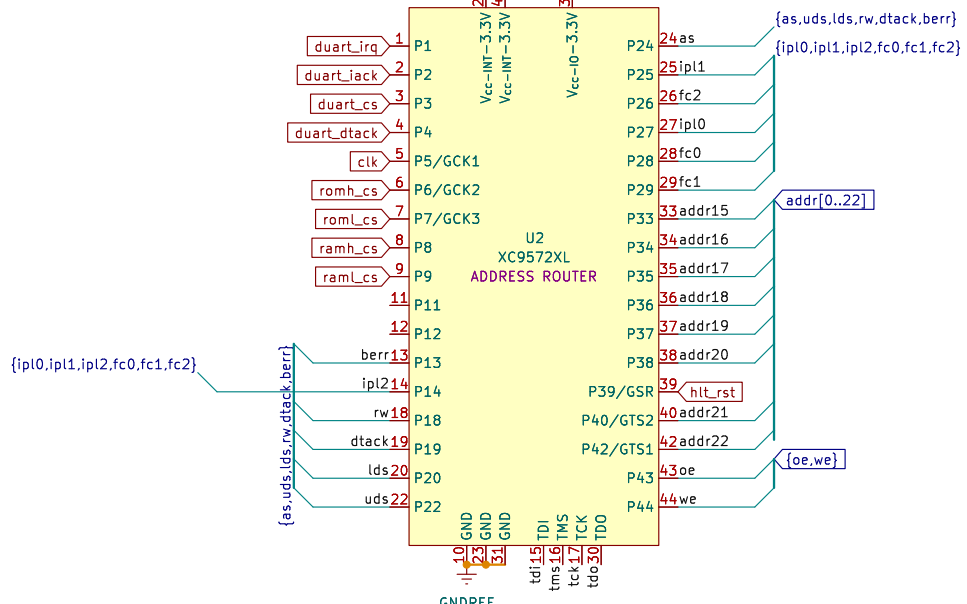
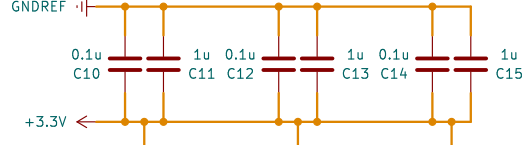
Clock Generator



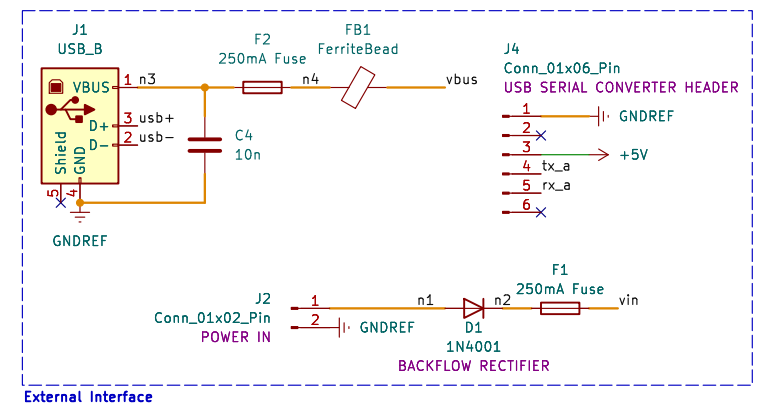
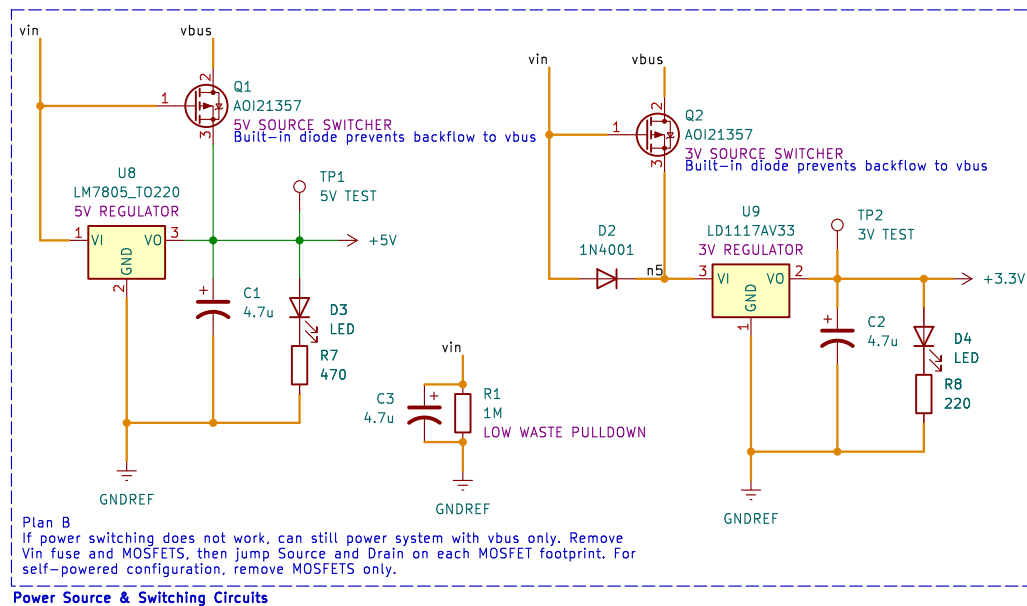
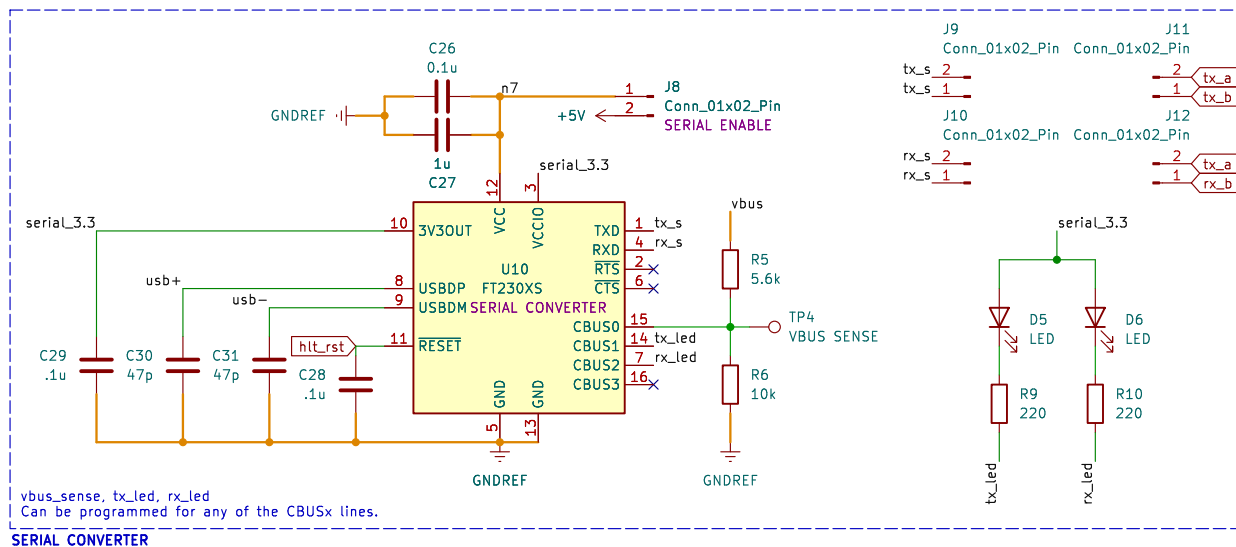
USB JTAG Connector



Reset Circuit



GCK=Global clock
GTS=Global output enable
GSR=Global set/reset CPLD control signals
GTS 1 and 2 do the same thing



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Sheet: /Power+I0/
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Rev: 0.4

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