

Richard Homan

Microcomputer Design

Dr. Joonwan Kim

February 21, 2024

Design Report #6

I. Time: 22 Hours

II. Accomplishments

A. Finished monitor program on simulator

III. Planned Work

A. Receive and assemble board

B. Program CPLD logic

C. Test burn EEPROM

IV. Project Completion

A. Hardware: 90%; PCB ordered, parts recieved

B. Software: 75%; Monitor program 95% done

V. Help from Lab Tech

A. 2/15/23 - Basic checkup