Richard Sirokman

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Cloud DevOps and Quality Assurance Engineer

- Implements full end to end development, test, and operations process
- Ensures pipelines are tested at each step to reduce cost and escape of bugs
- Architects AWS solution using the latest technologies, including serverless

Skills

- Amazon Web Services Certified Solutions Architect, Certified Developer Associate
- AWS Services: S3, Lambda, DynamoDB, CodePipeline, Codebuild, Git, EC2, IAM
- Software Test Planning, Execution, Automation for REST APIs
- Exploratory Testing, Bug Tracking, Integration and Acceptance Testing
- Agile Process, Scrum, Continuous integration, Kanban, Jira, Confluence
- Java, Python, C, C++, Bash, Shell, Postman, JavaScript, React, Chai, Mocha
- Unix, Linux, Solaris, Windows, Mac OSX, npm, pip, node.js

Experience

Test Automation and QA Engineer Aug 2011- Mar 2019

Oracle

- Testing of Orchestration Service for Cloud Services such as Azure, and Linux Servers
- Wrote tests in Java for Agile/Scrum team in large microservices project
- Troubleshooting tests/ failures, design tests, work with developers to fix problems
- Testing REST endpoints using curl, JSON, Postman, RestAssured
- Create Test Plans, track test results, file and resolve bugs
- Work with developers and QA on remote Agile team spread across the globe

Software Test Engineer and Consultant 2000 - 2011

LSI Logic, Sun Microsystems, StorageTek, HP

- Managed testing for Windows Exchange-based email server
- Tested web-based data center hardware management program
- Implemented test planning and bug tracking systems for small engineering team
- Performed daily build regression testing in Dev team for disk RAID product
- Created and managed Automated regression tests in Python
- Created test plans, executed tests for RAID SAN and NAS products
- Managed QA team to execute tests and created automation testing software

Education

Bachelor of Science in Computer Engineering, Graduated 1999

Kansas State University, Manhattan, KS

Coursework: TCP/IP Networking, Robotics, Artificial Intelligence, Analog Electronics, Amplifier Design, Electromagnetic Theory, Operating Systems, C/C++, OpenGL, Digital Design, Microcontrollers, TCP/IP Networking, Physics, Geography, Writing

Honors: Dean of Engineering Scholarship, Eta Kappa Nu Electrical Engineering Fraternity, Golden Key Honor Society