

Richie Patel

Vapi, Gujarat

✉ richiepatel30@gmail.com

☎ (+91) 9016453372

🌐 linkedin.com/in/richie-patel45

EXPERIENCE

ISWDP Cohort 4 - TCAD Training (Ongoing)

IISC Bangalore in collaboration with
Samsung and Synopsys.

Feb 2025-, Virtual

- Hands-on experience in TCAD for semiconductor device modeling.
- Designed and simulated diodes, BJTs, and MOSFETs with realistic process variations.
- Developed CMOS structures and automated design using scripting.

Space Applications Centre, ISRO.....

Research Intern

Jan 2024 - Apr 2024, Ahmedabad

Design of custom MIPI CSI-2 Protocol and its Applications Testing

- Proficiency in VHDL and FPGA programming flow.
- Designed and verified custom MIPI CSI-2 protocol using Libero.
- Testing of application notes of Microchip Polarfire SoC video kit.
- Led collaborative problem-solving with Microchip personnel and co-guide to implement the design successfully.

Techno IT Hub.....

Web Designing Intern

Aug 2023, Ahmedabad

- Created a clone website (Front-end) of the Amazon web page using HTML, CSS, and JavaScript.
- Created a few web pages like Pomodoro timer, calculator, Notes, and Task webpage.

Projects

- Digital Electronics circuit simulation using Xilinx & Libero SoC like State Machines, Decoder, Encoder, Tx & Rx of Protocol.
- Simulated various electronics circuit on Softwares like Multisim, Matlab, Collab, Blynk IOT, Arduino.

Achievements

- Samsung India Fellowship for ISWDP course of TCAD simulator.
- Consistently ranking in the top 10 % of the batch.
- Received a letter of Recommendation from Group Director of SAC, ISRO Ahmedabad during my final year Internship
- Achieved a top 20 finish in the VOSAP International Hackathon for Assistive Technology.

Extracurriculars

- Worked with the Sponsorship Team at Prakalpa Club, engaging with 50+ companies to secure 20% sponsorship conversion rate.
- Spearheaded marketing initiatives for Innovator Club, resulting in a 25% surge in event registrations.
- Actively contributed to community development through NSS Club, participating in Blood Donation Camps and Rang Amazi.

SUMMARY

Electronics Engineer with hands-on experience in RTL design, FPGA-based development, and digital circuit verification. Strong foundation in VHDL, Verilog, and semiconductor device modeling through ISRO & IISc training. Passionate about ASIC design and logic synthesis.

SKILLS

Soft Skills	Written & Verbal Communication, Problem-solving, Leadership Skills
Software	Microsoft Office : Word, Excel, Powerpoint
Languages	VHDL, Python, C, C++, HTML, CSS

EDUCATION

L.D. College of Engineering

Ahmedabad

Bachelor of Engineering - Electronics & Communication

CGPA: 8.98/10.00May 2024

Upasana Lions School

Vapi

HSC - PCM & Higher Secondary

94.53 PercentileMay 2020

SSC - Secondary

99.49 PercentileMay 2018

CERTIFICATIONS

- IBM AI Fundamentals Badge
- IBM AI Foundations in Business
- PCB Designing Workshop using Ki-Cad.
- Foundations of Project Management

LANGUAGES

- Gujarati (Native)
- English
- Hindi