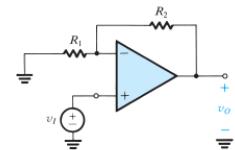


Op Amp Discovery



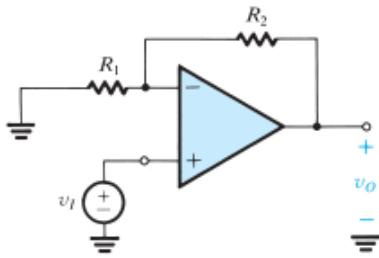
Circuit Examples for Guided Learning

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1 Introduction

Guided Learning is like a buffet table: **take as much or as little as you need!**



Choose one of three paths to discovery.

1. *Follow a walk-through of circuits to observe op amp functions and behaviors.*
2. *Borrow ideas from circuit examples and follow-through on your own.*
3. *Explore circuits entirely on your own with self-directed learning.*

The examples are intended to help you

- ✓ *build your circuit discovery and learning skills.*
- ✓ *explore op amps in a fun, engaging way.*

2 Four Key Steps

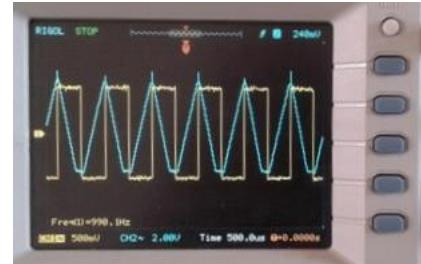
The topics walk-through an op amp circuit with 4 basic steps.

1. **Define It** - Draw Schematic, choose gains, signal levels, etc.
2. **Design It** - Calculate component values, signal levels.
3. **Build It** - Find a way to create it (sketch circuit, install parts on Discovery Bd.)
4. **Test & Learn It** - Plan setup, make measurements, verify levels, observe, connect to theory.

3 What You'll Need

The Op Amp Discovery Bd provides the Quick Proto-Board, Supplies and Signal Source. You'll need a few additional items to learn and explore.

- Oscilloscope
 - Observing waveforms provides key insight into how circuits work.
 - Suggested: 2 channels with >1MHz Bandwidth.
 - Budget Scopes (hand-held or USB type) are available from \$40 to \$100.
- DMM
 - A low-budget alternative to a scope is a basic Digital Multi-Meter (DMM) available from \$10 and up.
 - Select the DC input source on the Discovery Bd and observe the op amp's behavior with the DMM.
 - See Section 10 for more info.
- Paper Pad & Calculator
 - Let your pencil flow freely onto the paper with schematics, equations and waveforms.
 - The paper is your sandbox to play with ideas - *brainstorm, gain clarity, try and adjust.*



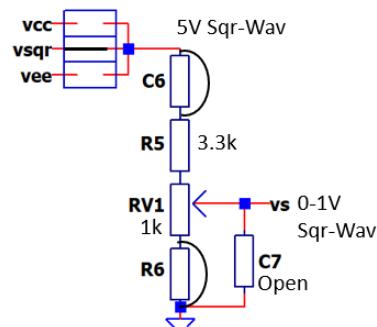
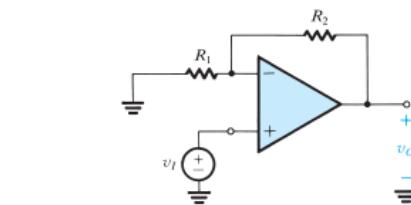
4 Op Amp Circuit Collection

Use the guided examples any way you like!

- Just follow your interest and curiosity!
- No rigid order or method.

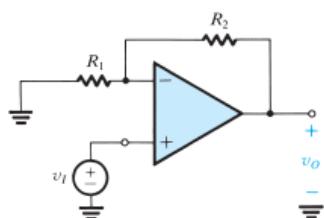
Default Signal Source

- DC Coupled, Square-Wave, 5kHz
- Trimpot adjusts Vs from 0 - 1.2V
- Default signal setup (unless noted)
 - C6 = short
 - R5 = 3.3k
 - RV1 = 1k trimpot
 - R6=short
 - C7 = open



4.1 Non-Inverting Amplifier

1. Define It – Draw Schematic and choose gains, signal levels, etc.



Let's amplify a signal by $K = +3$.

$$K = V_{out}/V_s = R_1/R_2+1 = 3$$

Choose $V_s = 0.5V$

2. Design It – Calculate component values and signal levels.

Choose $R_1 = 10k$,

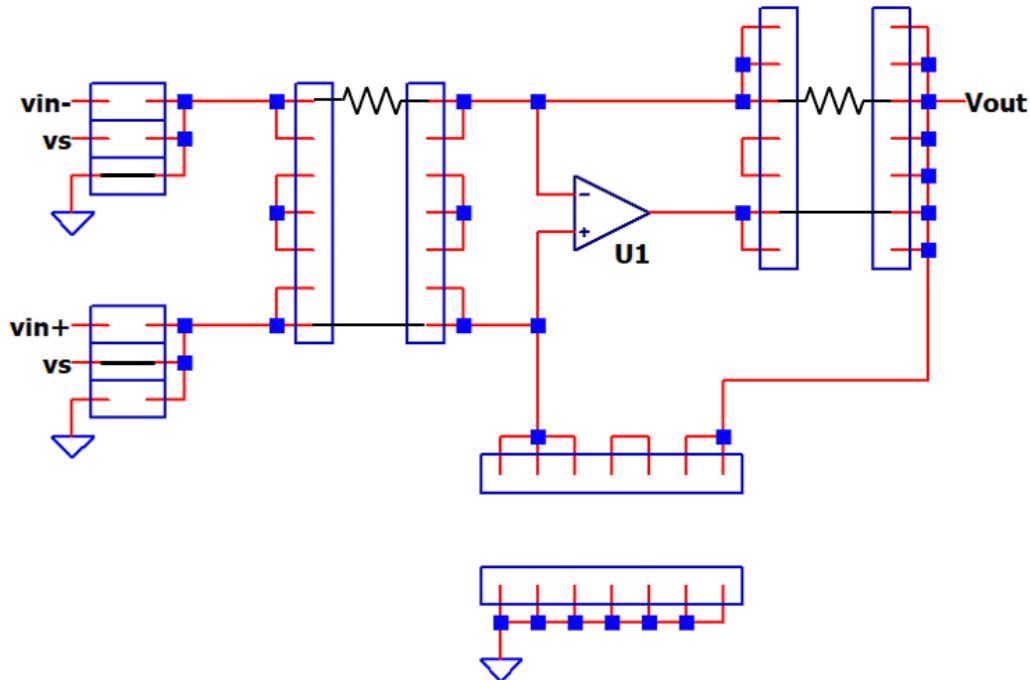
$$\text{Calc } R_2 = (K-1) * R_1 = 20k$$

$$V_{out} = V_s * K$$

$$= 0.5V * 3$$

$$= 1.5V$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 1 to VS (Jumper at J3,4).

Connect Probe 2 to VOUT (R2 at Vout side).

Verify a unipolar square wave at VS.

Adjust RV1 for VS = 0.5V peak.

Did Vout achieve the goal?

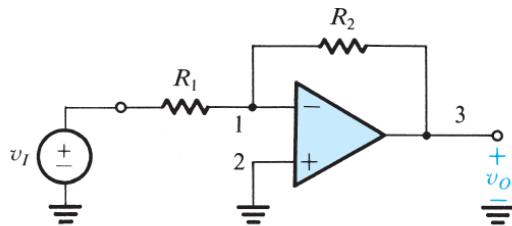
Move Probe 2 to U1's negative input (R1 at U1 side).

Does U1.in- follow VS?

What happens if you double R1 or R2?

4.2 Inverting Amplifier

1. Define It – Draw Schematic, choose gains, signal levels, etc.



Let's amplify a signal by -2.

$$V_s = +1V$$

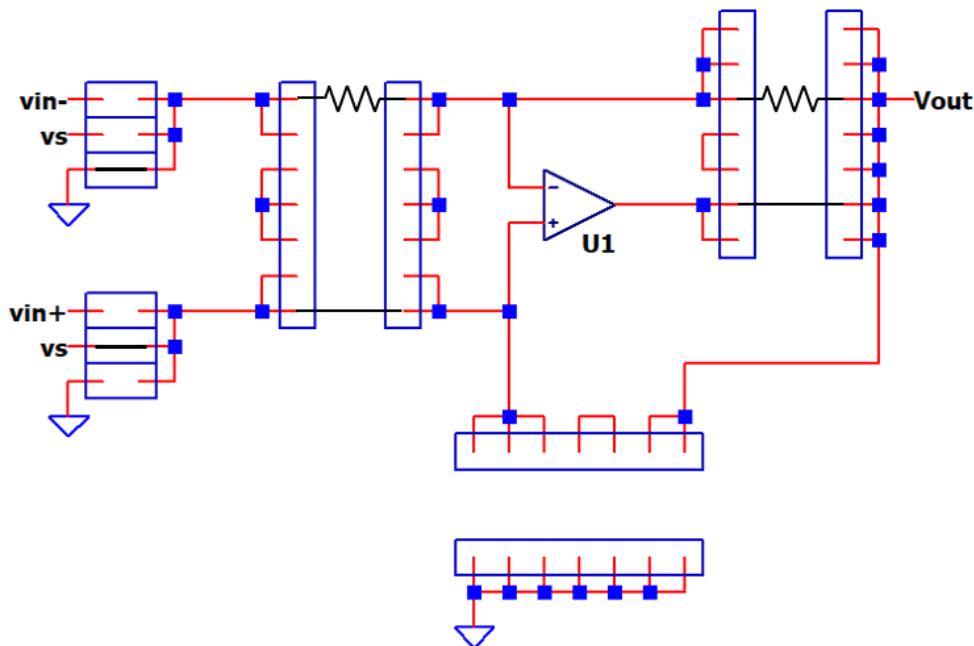
$$K = V_{out}/V_s = -R_2/R_1 = 2$$

2. Design It – Calculate component values and signal levels.

Choose $R_1 = 10k$,
Calc $R_2 = K * R_1 = 20k$

$$\begin{aligned} V_{out} &= V_s * K \\ &= 1V * 2 \\ &= -2V \end{aligned}$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).
Connect Probe 1 to VS (R1 at VS side).
Connect Probe 2 to VOUT (R2 at Vout side).

Verify a square wave at VS.

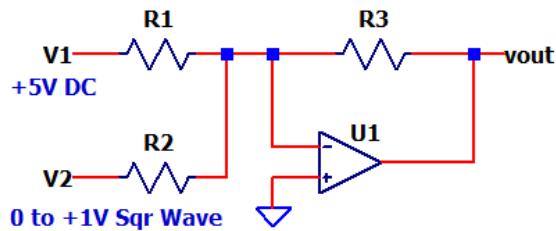
Adjust RV1 for $V_s = 1V$ peak.
Did Vout achieve the goal?

Move Probe 2 to U1's neg input (R1 at U1 side).
Does U1.in- follow GND (0V)?

What happens if you double R1 or R2?

4.3 Summing Amplifier

1. Define It – Draw schematic, choose gains, signal levels, etc.



What happens when a DC signal (V_{cc}) is summed with a Square Wave (V_S)?

$$V_1 = V_{cc} = +5VDC$$

$$K_1 = V_{out} / V_{cc} = -R_3/R_1 = -1/10$$

$$V_2 = V_{sqr} = 0 \text{ to } +1V \text{ (Sqr Wave)}$$

$$K_2 = V_{out} / V_{sqr} = -R_3/R_2 = -1$$

2. Design It – Calculate component values and signal levels.

Choose $R_3 = 10k$

Calc $R_2 = R_3 / -K_1 = 100k$

Calc $R_1 = R_3 / -K_2 = 10k$

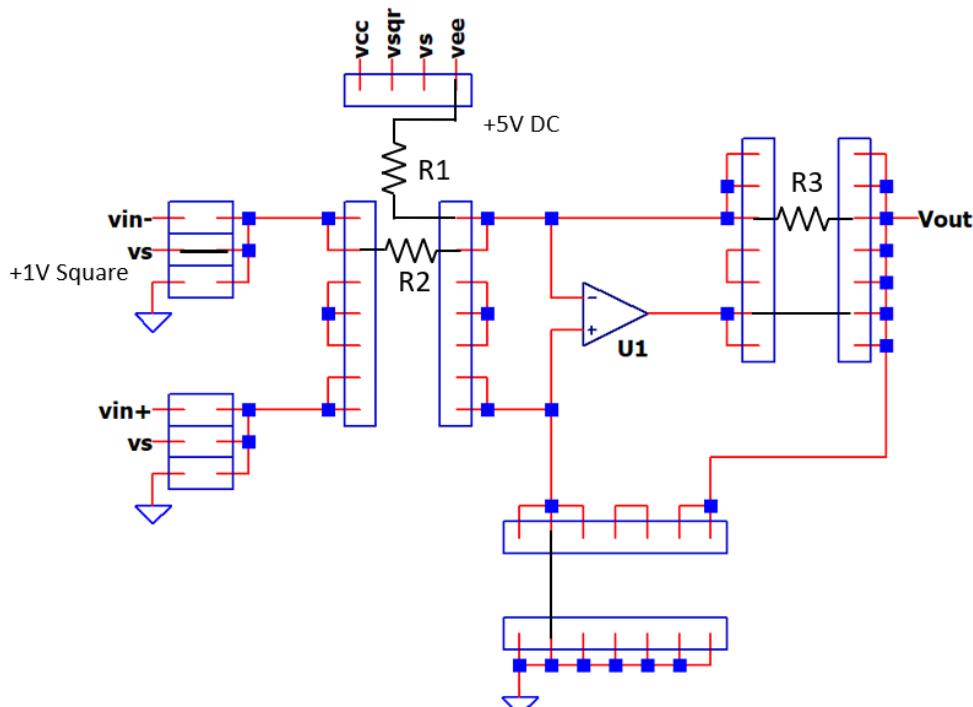
$$V_{out} = V_{cc} * K_1 + V_{sqr} * K_2$$

$$= V_{cc} * -0.1 + V_{sqr} * (-1)$$

= -0.5 DC level + (0 to -0.5V) Sqr wave

Note: RV_1 adjusts Square Wave amplitude

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 1 to VS (R1 at VS side).

Connect Probe 2 to VOUT (R3 at Vout side).

Adjust RV_1 for $VS=0V$ peak.

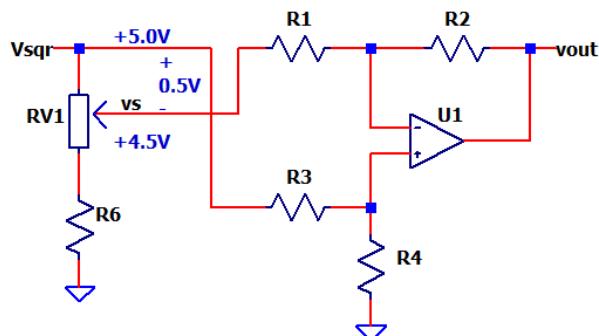
Slowly adjust RV_1 for $VS=1V$ peak.

Does the Square Wave get summed with V_{out} ?

What if R_2 were halved or doubled?

4.4 Differential Amplifier

1. Define It – Draw Schematic, choose gains, signal levels, etc.



Can the amp sense the difference between
Vsqr = 5V and Vs = 4.5V?

$$V_{in+} = V_{sqr} = +5V$$

$$V_{in-} = V_s = 4.5V$$

$$V_{in} = (V_{sqr} - V_s) = 0.5V$$

$$K = V_{out} / V_{in} = R_2 / R_1 = 1$$

2. Design It – Calculate component values and signal levels.

Choose $R_1 = R_3 = 10k$

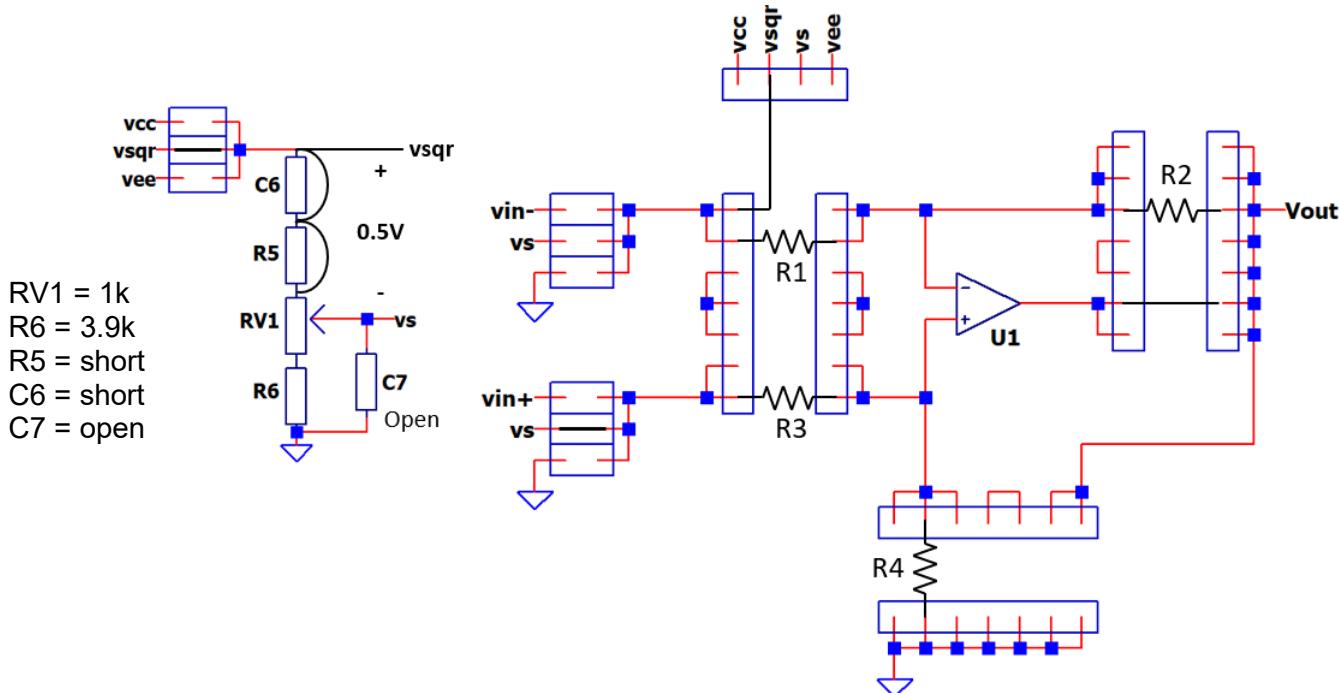
Calc $R_2 = R_4 = R_1 \cdot K = 10k$

$$V_{out} = (V_{in+} - V_{in-}) * K$$

$$= 0.5V * 1$$

$$= 0.5V$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 1 to VS (R3 at Vs side).

Connect Probe 2 to VOUT (R2 at Vout side).

Adjust RV1 for $V_s = +4.5V$.

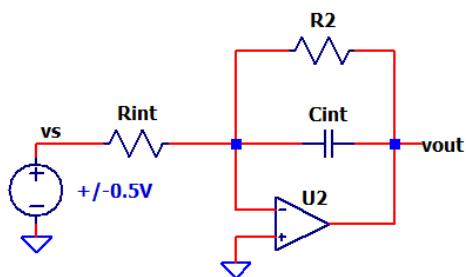
V_{sqr} should be +5V.

Does Vout achieve its goal?

What happens if you halve or double R2 and R4?

4.5 Integrator

1. Define It – Draw Schematic, choose gains, signal levels, etc.



Can the integrator generate a ramp from a square wave input?

$V_s = \pm 0.25V$ (Sqr Wave AC coupled)
 $dT = 100\mu s$ (Square wave half-cycle)

2. Design It – Calculate component values and signal levels.

Choose $R_{int} = 100k$, $C_{int} = 100pF$

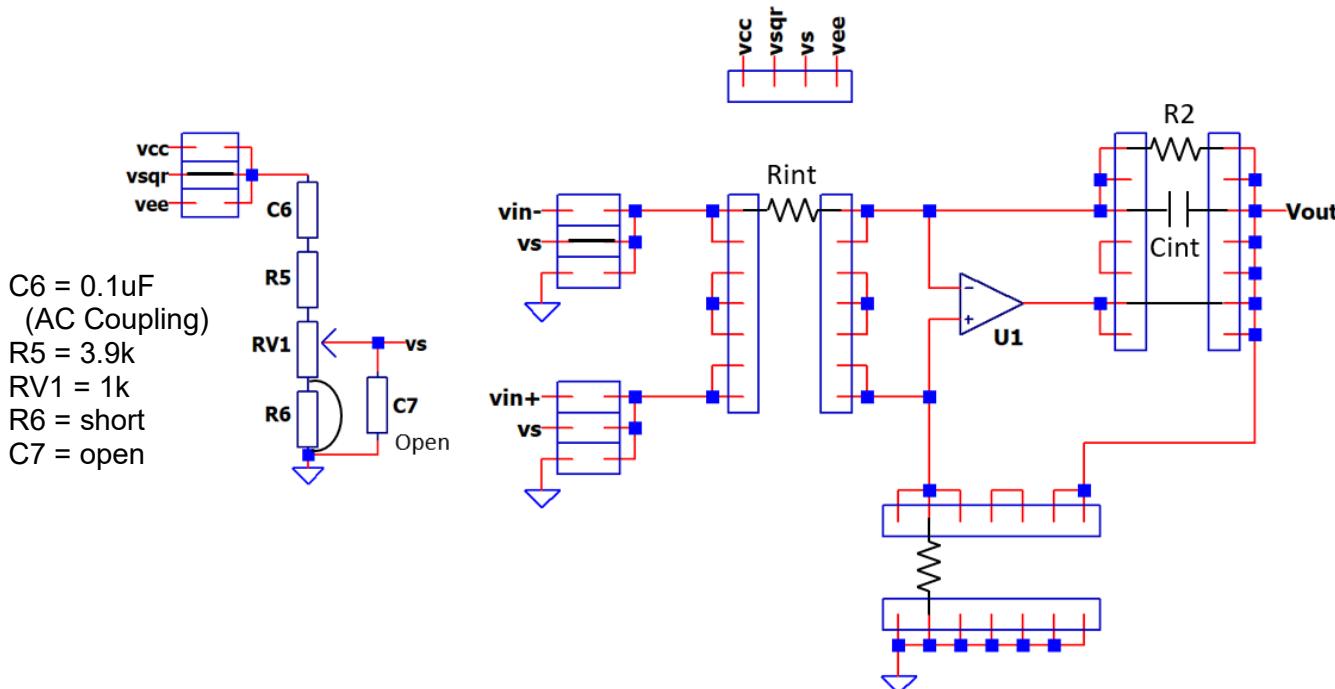
Choose $R_2 = 4.7M$

(R_2 makes finite DC gain, keeps offset low.)

$$dV_{out} = V_s / (R_{int} \cdot C_{int}) \cdot dT \\ = 2.5V$$

$$V_{out} = \pm 1.25V$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

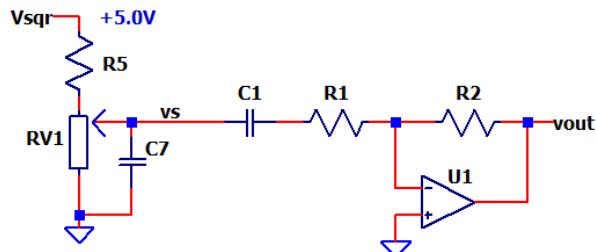
Power up the circuit (connect 9V Battery).
 Connect Probe 1 to VS (Rint at Vs side).
 Connect Probe 2 to VOUT (Cint at Vout side).

Adjust RV1 for $V_s = \pm 0.5V$
 Does Vout ramp as expected?

What happens if you increase Rint or Cint?

4.6 Differentiator

1. Define It – Draw Schematic, choose gains, signal levels, etc.



Can the differentiator find the rate-of-change of a triangle wave input?

Vs = "Triangle" Wave

Triangle wave approximated by a Square-Wave passed thru LP Filter with long time-constant (Tau).

2. Design It – Calculate component values, signal levels.

Choose $R_5 = 10k$, $C_7 = 0.1\mu F$

$$\text{Tau} = R_5 \cdot C_7 = 0.001\text{s}$$

Choose $C_1 = 1nF$, $R_2 = 100k$

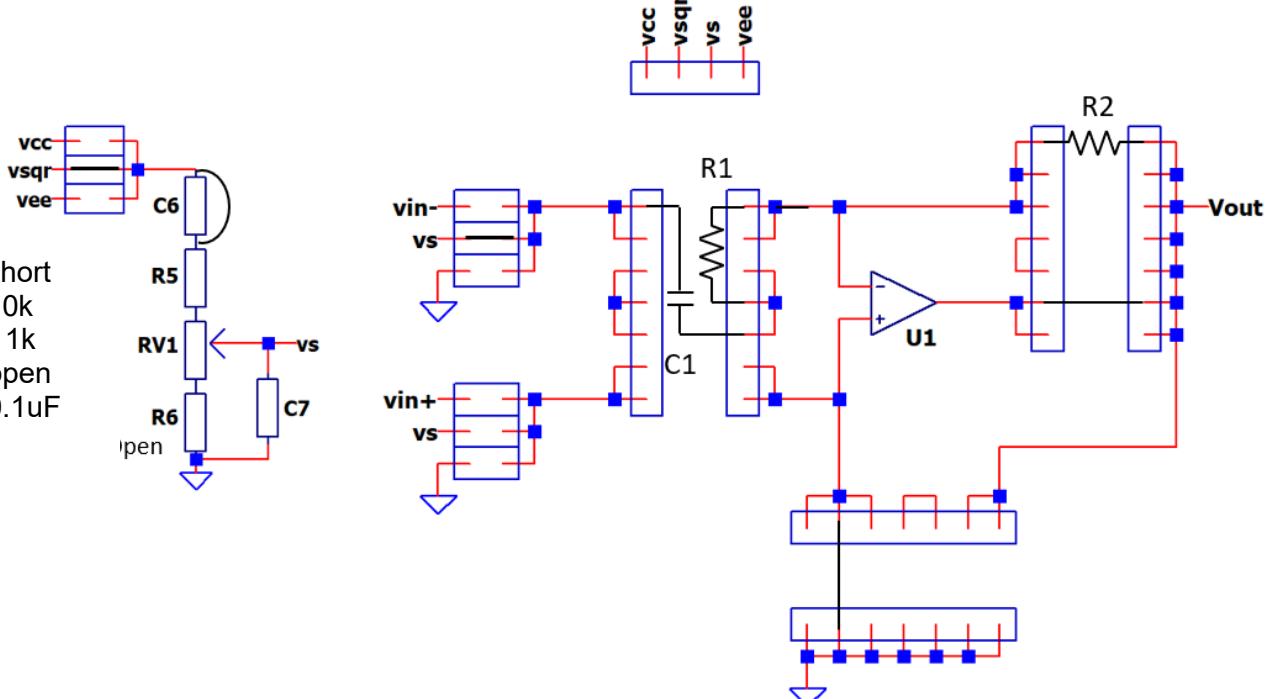
$$dV/dt = (Vsqr/2)/\text{Tau} = 2.5V/0.001s = 2500V/s$$

Choose $R_1 = 4.7k$ (keeps amp stable.)

$$V_{out} = R_2 \cdot C_1 \cdot dV/dt = 0.25V$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)

$C_6 = \text{short}$
 $R_5 = 10k$
 $RV_1 = 1k$
 $R_6 = \text{open}$
 $C_7 = 0.1\mu F$



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Adjust RV1 fully CW (max signal)

Connect Probe 1 to VS (C1 at Vs side).

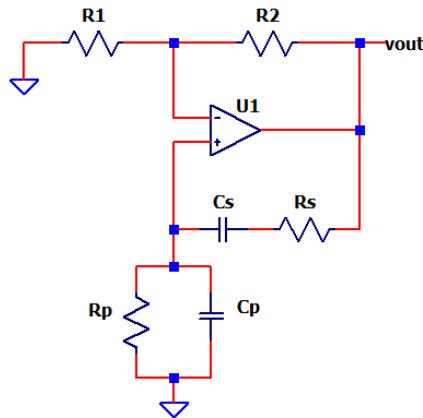
Does Vout show slope as expected?

Connect Probe 2 to VOUT (C2 at Vout side).

What happens if R2 increases?

4.7 Wien-Bridge Oscillator

1. Define It – Draw Schematic, choose gains, signal levels, etc.



Can you produce a Sine-Wave output with
 $T_{per} = 300 \text{ us}$?

$$f_0 = 1/(2\pi R C)$$

2. Design It – Calculate component values, signal levels.

Requires gain $K > 3$ for osc.

Choose $R1 = 4.7\text{k}$,

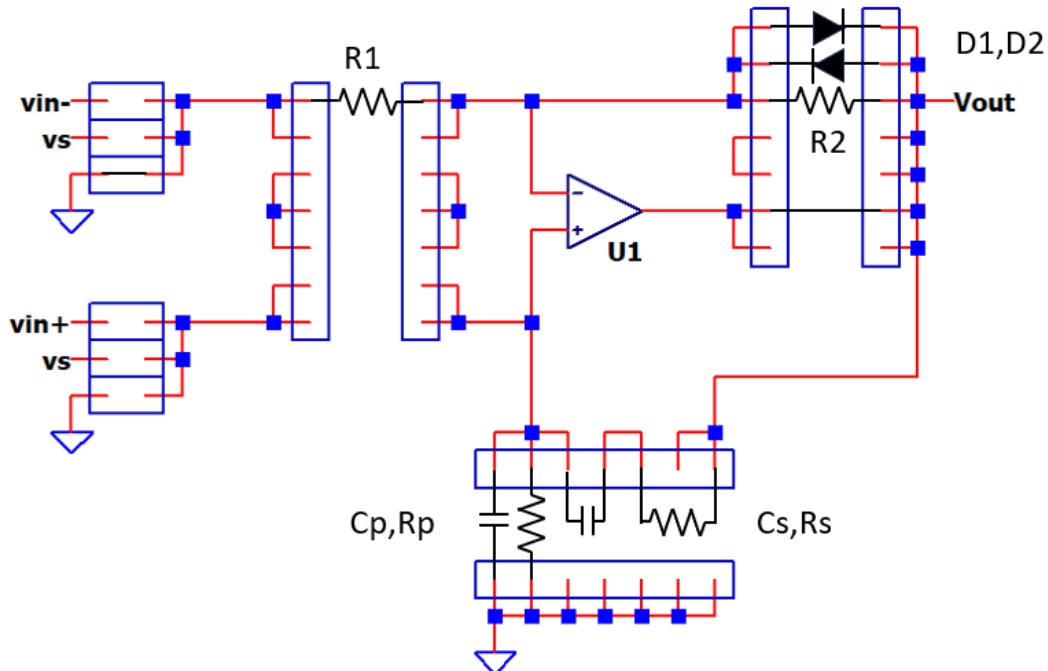
Calc $R2 = (K-1)R1 = 9.4\text{k}$, round up to 10k

Choose $C = 1\text{nF}$, $f_0 = 3.3 \text{ kHz}$

Calc $R = 1/(2\pi C f_0)$

$$= 47.8\text{k} \text{ (use } 47\text{k)}$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

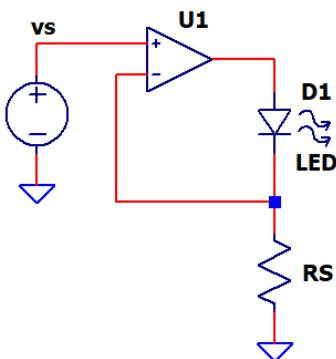
Connect Probe 1 to VOUT (C2 at Vout side).

Does Vout show the sinewave as designed?

How is Vout related to Vdiode?

4.8 V-to-I Converter (LED Drive)

1. Define It – Draw Schematic, choose gains, signal levels, etc.



How can VS be converted into a current through the LED?

$$I_o = V_s / R_s$$

2. Design It – Calculate component values, signal levels.

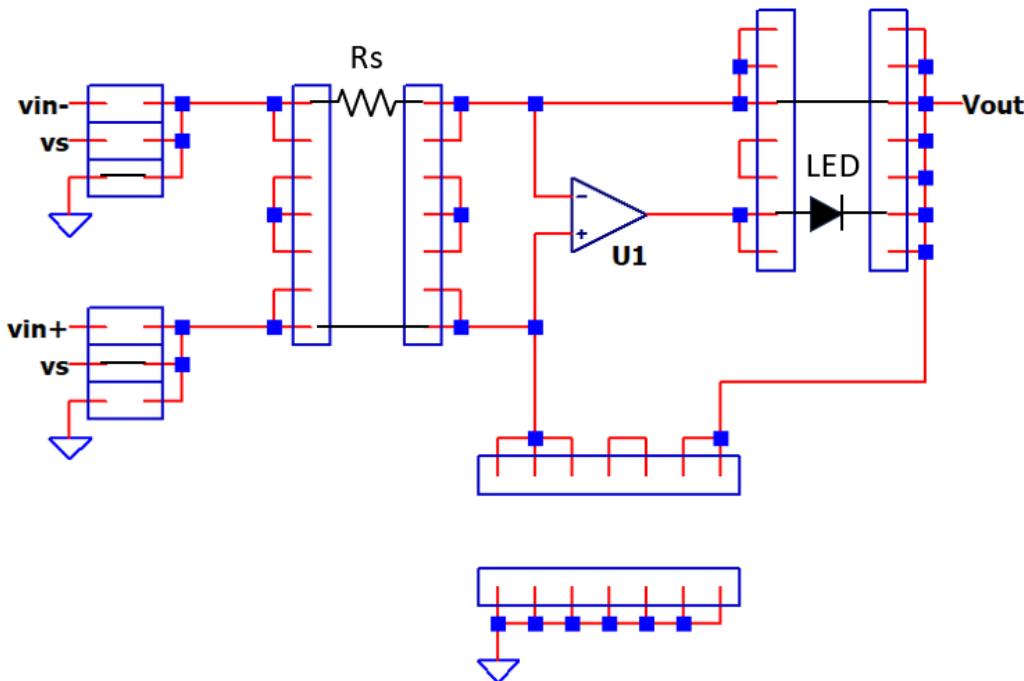
Choose $I_o = 2\text{mA}$ at $V_s = 1\text{V}$

Calc $R_s = V_s / I_o = 500 \text{ ohms}$

Choose 510 if available

$$\begin{aligned} \text{Calc } I_o &= 1\text{V} / 510 \\ &= 1.98 \text{ mA} \end{aligned}$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 1 to VS (Jumper).

Connect Probe 2 to RS (U1 side).

Move Probe 2 to LED anode (U1.vout).

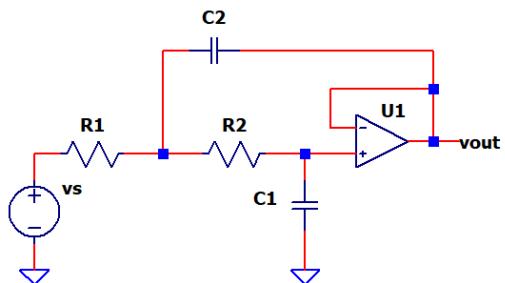
How much LED voltage is needed above the RS voltage?

Adjust RV1 for $V_s = 1\text{V}$ max (Square Wave)
Does the RS voltage follow V_s so that $I_o = V_s / R_s$?

What happens to LED current (and brightness) if $R_s = 1k$?

4.9 Sallen-Key Low-Pass Filter

1. Define It – Draw Schematic, choose gains, signal levels, etc.



How much can the amplitude of a square wave (5Vp-p, 5kHz) be reduced using a 2-pole filter?

$$f_c = 1 / (2 \cdot \pi \cdot R \cdot C)$$

$$H \approx (f_c/f_{osc})^2$$

2. Design It – Calculate component values, signal levels.

Choose $f_c = 333\text{Hz}$ and $C_1 = 10\text{nF}$

$$\text{Calc } C_1 = 1/(2\pi \cdot 10\text{nF} \cdot 333\text{Hz}) = 47.8\text{k}$$

Choose 47k

$$\text{Calc } H \approx (f_c/f_{osc})^2 \\ \approx 0.0044$$

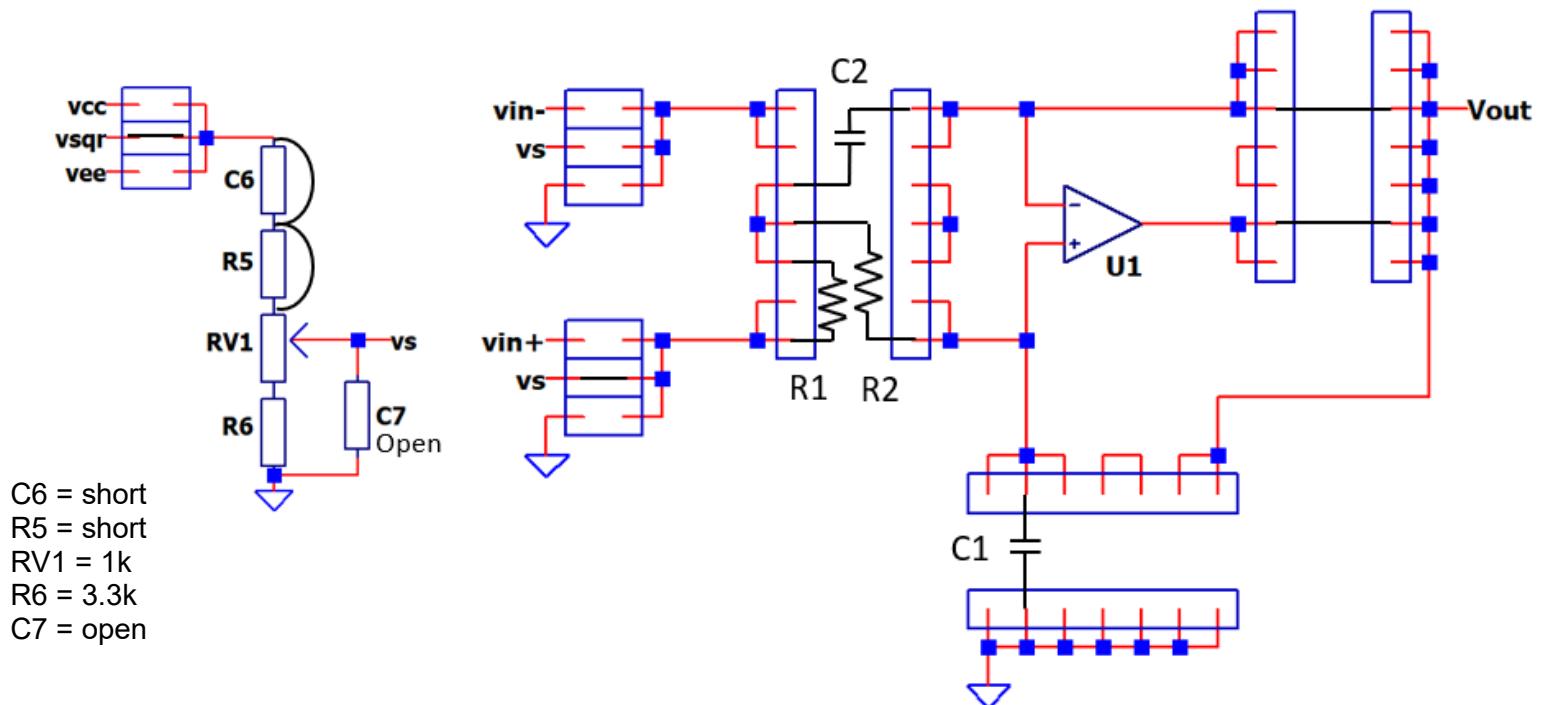
Calc expected output with

$V_S = 5\text{V}$, $f_{osc} = 5\text{k}$

$$V_{out} \approx V_S \cdot (f_c/f_{osc})^2$$

$$\approx 22 \text{ mVp-p}$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 1 to R1 (VS side).

Connect Probe 2 to Vout (Jumper).

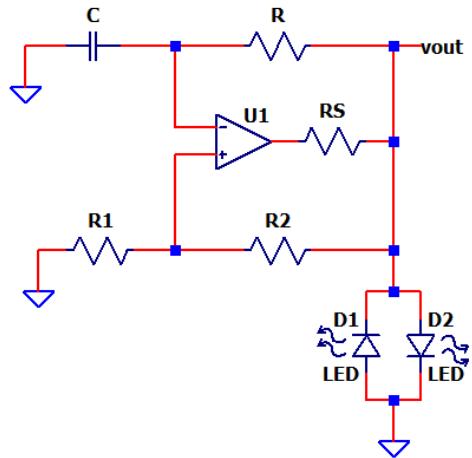
How much voltage (peak to peak) appears at Vout?

What is the DC level at Vout and why?

Adjust RV1 for max 5V Square wave.

4.10 RC Comparator Oscillator

1. Define It – Draw Schematic, choose gains, signal levels, etc.



What is the oscillation frequency of this Square-Wave output circuit?

$$f_0 = 1/(2 \cdot R_o \cdot C_o \ln((1+B)/(1-B)))$$

$$\text{where } B = R_1/(R_1+R_2)$$

2. Design It – Calculate component values, signal levels.

Choose handy components

$$R_o = 100k, C_o = 10nF$$

$$R_1 = 10k, R_2 = 10k$$

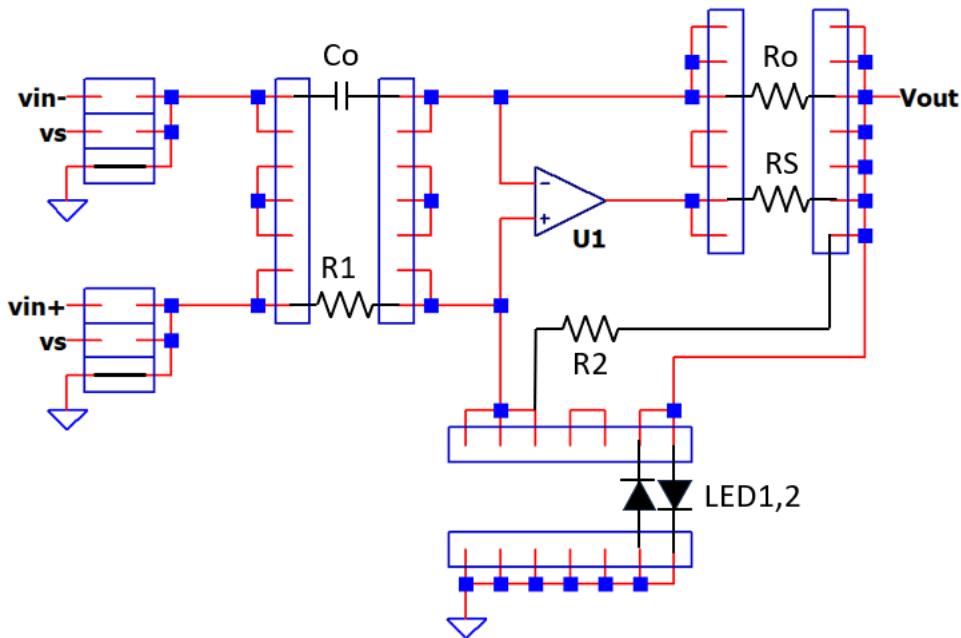
$$RS = 1k, \text{ LED1,2} = \text{pick two}$$

Calc osc frequency

$$f_0 = 1/(2 \cdot 100k \cdot 10nF \cdot \ln((1+0.5)/(1-0.5))) = 80 \text{ Hz}$$

$$\text{Calc } B = R_1/(R_1+R_2) = 0.5$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 1 to VOUT (Ro at Vout side).

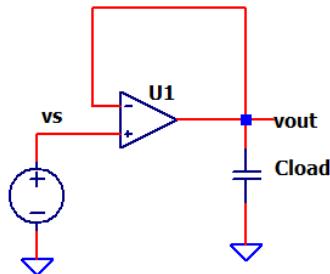
Does Vout show the sinewave as designed?

How is Vout related to Vdiode?

4.11 Driving a Capacitive Load

PART 1 - The Overshoot Problem

1. Define It – Draw Schematic, choose gains, signal levels, etc.

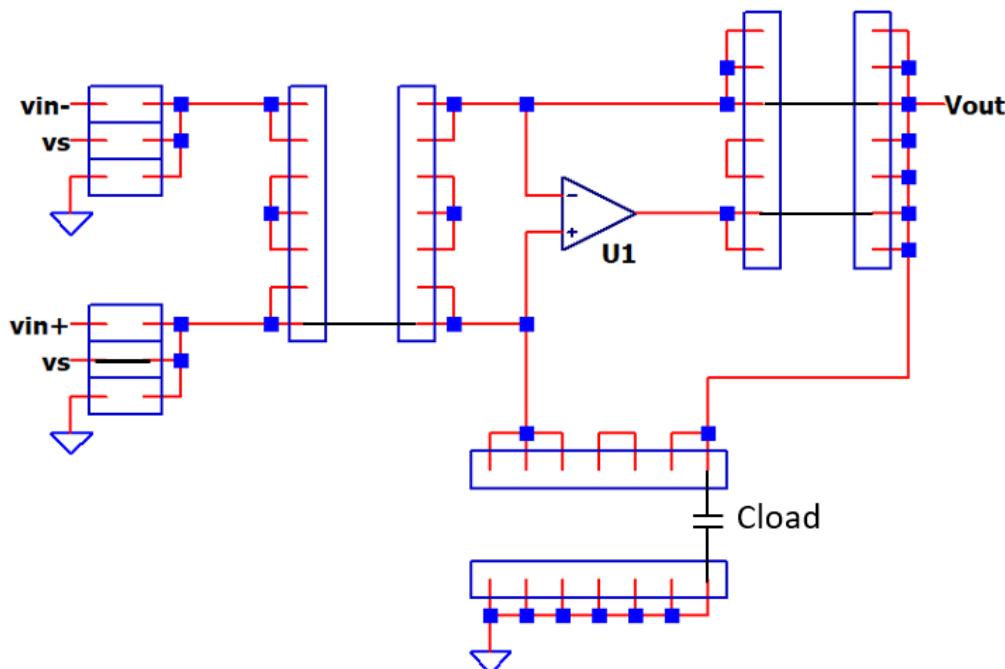


What is the overshoot when driving a $1\mu F$ capacitive load?

2. Design It – Calculate component values, signal levels.

Choose $C_{load} = 1\mu F$ or $0.1\mu F$ to observe the effects of a capacitive load.

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

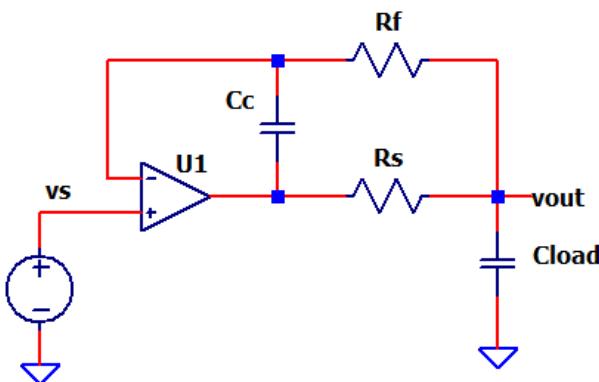
Power up the circuit (connect 9V Battery).
Connect Probe 1 to VS (Jumper at U1.+).
Connect Probe 2 to Vout (Jumper).
Adjust RV1 for max 1V Square wave.

How much overshoot appears at V_{out} ?

How long V_{out} take to settle to within $\pm 0.1V$ of the final value?

PART 2 - The Overshoot Solution

1. Define It – Draw Schematic, choose gains, signal levels, etc.



Can we reduce the large overshoot when driving a 1uF capacitive load?

2. Design It – Calculate component values, signal levels.

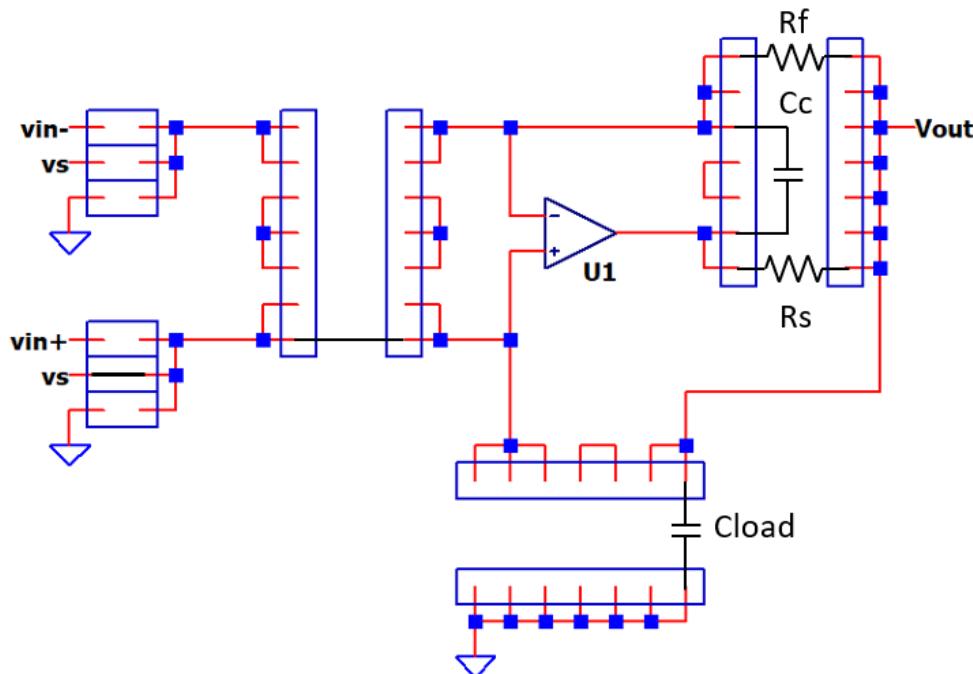
Choose $R_s = 100$ ohms

Choose $R_f = R_s \times 10$
= 1k

Solution by experiment!

Bump up CC incrementally: 100pF, 1nF, etc. until the overshoot reduces to an acceptable level.

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).
Connect Probe 1 to VS (Jumper at U1.in+).
Connect Probe 2 to Vout (Rf at Vout)

Adjust RV1 for max 1V Square wave.
Install CC = 100pF

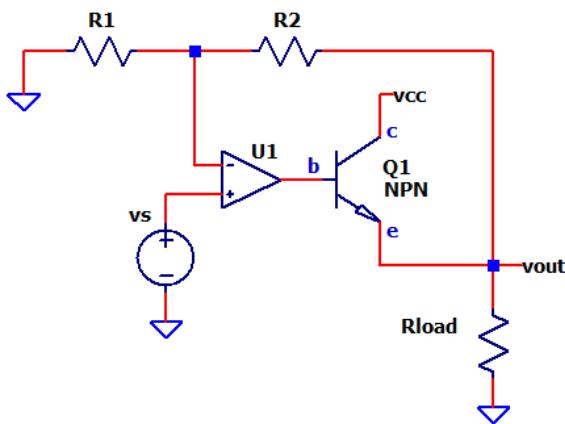
How much overshoot appears at Vout?
Any overshoot reduction over Part 1?

Install $C_c = 1nF$, then try $10nF$.

What value of C_c reduces overshoot without sacrificing too much settling time?

4.12 BJT Regulator

1. Define It – Draw Schematic, choose gains, signal levels, etc.



How can a BJT increase the output current drive of an op amp?

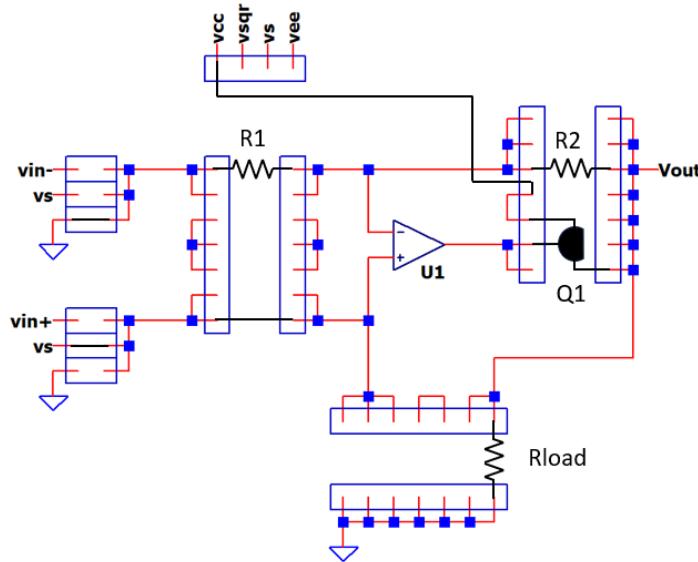
$$K = V_{out}/V_s = R_2/R_1 + 1$$

2. Design It – Calculate component values, signal levels.

Choose $V_s = 0.5V$, $R_1 = 10k$, $K = 2$
 $R_{load} = 1k$

$$\text{Calc } R_2 = (K-1) \cdot R_1 \\ = 10k$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 1 to Vs (Jumper).

Connect Probe 2 to Vout (R2 at Vout).

Move Probe 2 to (Q1.base).

How much voltage does the op amp need to apply to the base to achieve V_{out} ?

Adjust RV1 for $V_s = 1V$ max

How much current is flowing into R_{load} ?

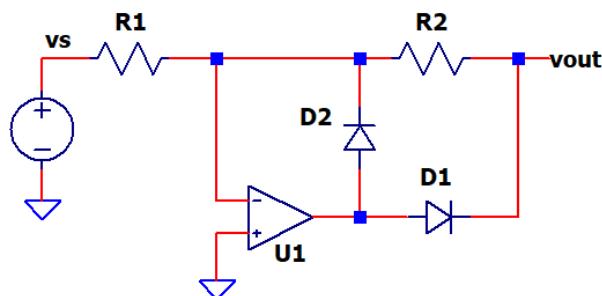
Reduce R_{load} to 100 ohms (increase I_{load}).

Does V_{out} hold at 1V?

How much voltage at Q1 base is needed now?

4.13 Half-Wave Rectifier

1. Define It – Draw Schematic, choose gains, signal levels, etc.



Let's pass the positive polarity of a bipolar square wave!

$$K = V_{out} / V_s = -R_2 / R_1 = -2$$

$$V_s = +/-0.5V$$

2. Design It – Calculate component values, signal levels.

Choose $R_1 = 10k$,
Calc $R_2 = -K * R_1 = 20k$

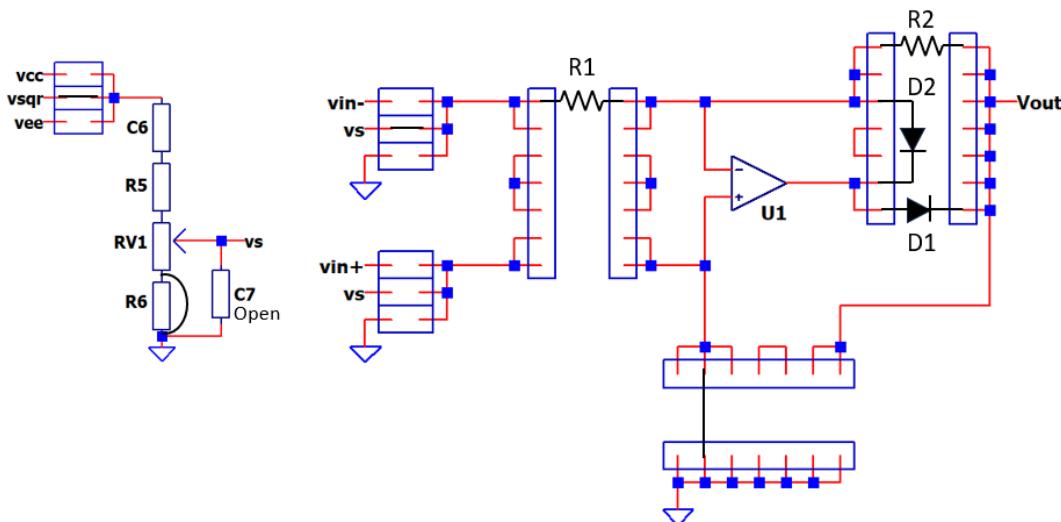
$$V_{out} = V_s * K$$

$$= -0.5V * -2$$

$$= +1V$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)

$C_6 = 0.1\mu F$
 $R_5 = 3.3k$
 $RV_1 = 1k$
 $R_6 = \text{short}$
 $C_7 = \text{open}$



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Which cycles does the rectifier pass?

Connect Probe 1 to VS (R1 at Vs side)

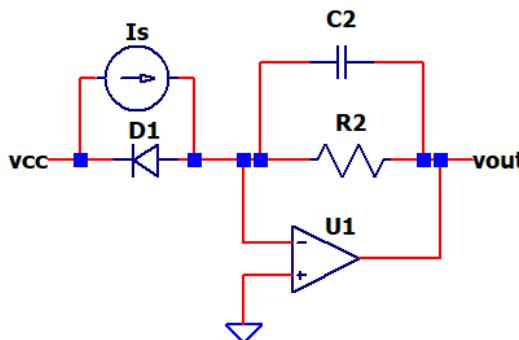
Connect Probe 2 to VOUT (R2 at Vout side).

What happens if you reverse both diodes D1 and D2?

Adjust RV1 for Vs = +/-0.5V (Square Wave)

4.14 I-to-V Converter (Photodiode)

1. Define It – Draw Schematic, choose gains, signal levels, etc.



How can the photodiode current be converted to a voltage?

$$V_o = I_s * -R_2$$

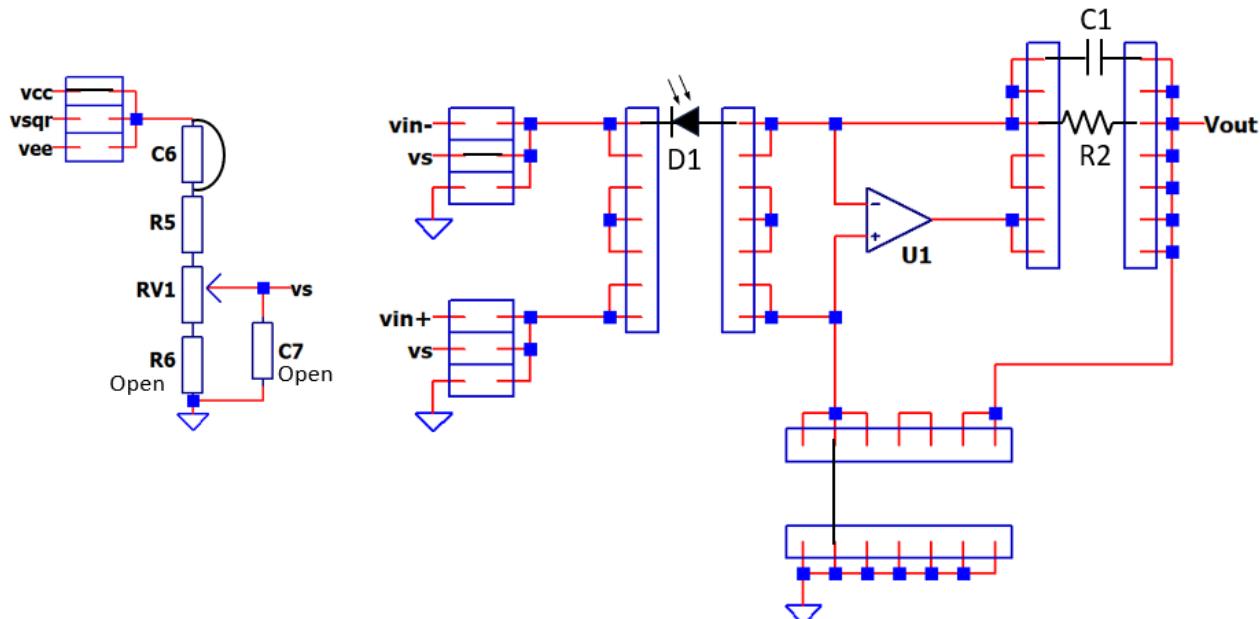
Photodiode: Vishay BV23F

2. Design It – Calculate component values, signal levels.

Choose $R_2 = 470k$

Ambient light level varies greatly.

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).
Connect Probe 1 to Vout (R2 at Vout side)

Does Vs provide +5V reverse bias to the photodiode?

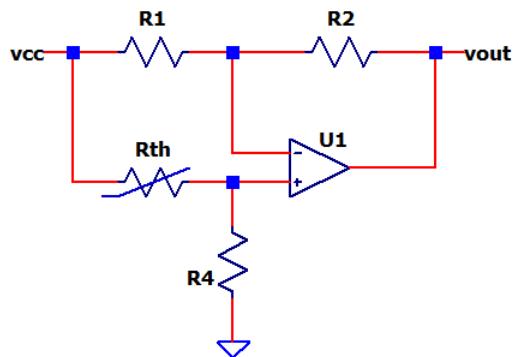
Where is Vout in ambient light?

How much current is flowing out of the photodiode?

What happens when you cover the photodiode with your finger?

4.15 Thermistor Preamp

1. Define It – Draw Schematic, choose gains, signal levels, etc.



What is the voltage change at Vout versus a temperature change?

$$V_{out} = V_{cc} * \frac{R_S}{(R_{th}+R_S)} * \left(\frac{R_2}{R_1} + 1\right) + V_{cc} * \left(-\frac{R_2}{R_1}\right)$$

2. Design It – Calculate component values, signal levels.

Choose $R_{th} = 10k @ 25C$

Sensitivity: $\Delta R_{th} / \Delta T = -4\% \text{ per } C$

Choose $R_S = R_1 = R_2 = 10k$

Calc V_{out} with $R_{th} = 10k @ 25C$

$$V_{out} = 5 * \left(\frac{1}{2}\right) * (+2) + 5 * (-1) = 0V$$

Calc V_{out} with $R_{th} = 9.96k @ 26C$

$$V_{out} = 5 * (0.501) * (+2) + 5 * (-1) = +0.1V$$

$$\Delta V_{out} / \Delta T = +0.1V / \text{degC}$$

3. Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)

4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

Power up the circuit (connect 9V Battery).

Connect Probe 2 to Vout (Jumper).

Room Temp: Test Thermistor in free air. (77F or 25C)

Elevated Temp Test: Hold thermistor between thumb and index finger. (90F or 32C)

Temp rise: $\Delta T = 32 - 25 = 7 \text{ deg C.}$

Voltage rise: $\Delta V_{out} = 7 \text{ deg C} * 0.1V/C = 0.7V$

What is V_{out} in free air?

What is V_{out} with sensor at an elevated temperature

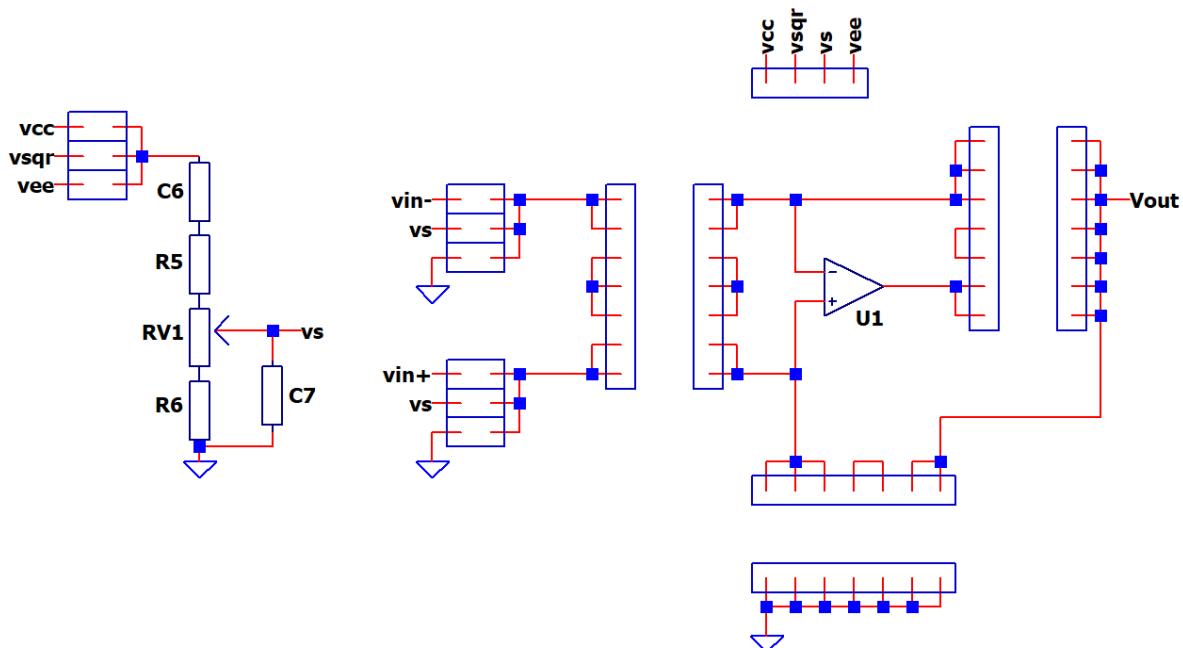
Did ΔV_{out} approximate the temp change by $0.1V/\text{deg C?}$

5 Discovery Sheets - Circuit:

1. Define It – Draw Schematic, choose gains, signal levels, etc.

2. Design It – Calculate component values, signal levels.

Build It - Find a way to create it (sketch circuit below, install parts on Discovery Bd.)



4. Test & Learn It – Plan setup, make measurements, verify levels, observe, connect to theory.

6 Op Amp Learning with a DMM Only

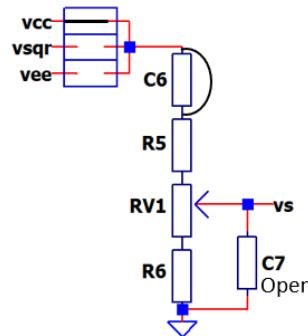
Learn and explore op amps with only budget Digital Multi-Meter (DMM)!

- Multimeters available for \$10 and up.
- Use manual ranging to observe voltage changing.



Select a DC signal for the input to VS.

- Remove JP1 for Square Wave
- Install JP2 for VCC (+5V)
or JP3 for VEE (-5V)
- Install short across C6 for DC coupling.

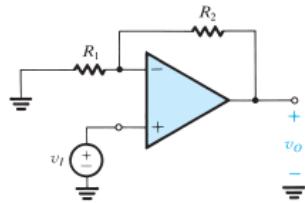


Explore 10+ topologies with just a DMM.

Op Amp Topology	Comment
Non-Inverting Amplifier Inverting Amplifier Summing Amplifier Differential Amplifier Unity-Gain Buffer	Select VCC or VEE as DC source. Vary the input with RV1. Observe Vout with a Volt Meter Function.
V-to-I Converter (LED Drive)	RV1 controls brightness
Wien-Bridge Oscillator RC Bridge Oscillator	Design for low frequency 1 to 5 Hz. Watch LEDs (D1,D2) flash.
Thermistor Preamp	Touch temp sensor, observe Vout change.
I-to-V Converter (Photodiode)	Cover light sensor, observe Vout change.
BJT Regulator	Vary Rload, check Vout stability.

7 The Power of Play

- Key to analog success:
play, play, play with circuits!



- Analog circuits appear more inviting (less intimidating) when playing / tinkering.
- Learning and passion grow when you
 - Ask “what if” questions
 - Play with component values
Predict outcome!
 - Get energized with small discoveries and learning.

