

Sheet1

	ESP32	CH340G	USB-C
NON-GPIOS	EN	V3	GND
	IO0	VCC	SBU2
	TXD0	R232/CND	SBU1
	RXD0	UD+	TX2+
	USB_D-	UD-	TX2-
	USB_D+	XI	RX2+
	3.3V	XO	RX2-
	GND	TXD	TX1+
		RXD	TX1-
		$\overline{\text{CTS}}$	RX1+
		$\overline{\text{DSR}}$	RX1-
		$\overline{\text{RI}}$	D+
		$\overline{\text{DCD}}$	D+
		$\overline{\text{DTR}}$	D-
		$\overline{\text{RTS}}$	D-
		GND	CC2
			CC1
			VBUS
			SHIELD