|  |  |  |  |
| --- | --- | --- | --- |
| **BCD\_To\_EX3 Project Status (07/29/2025 - 03:09:50)** | | | |
| **Project File:** | bcd\_ex3.xise | **Parser Errors:** | No Errors |
| **Module Name:** | BCD\_To\_EX3 | **Implementation State:** | Synthesized |
| **Target Device:** | xc3s400-4ft256 | * **Errors:** | No Errors |
| **Product Version:** | ISE 14.6 | * **Warnings:** | [6 Warnings (6 new)](D://ms/bcd_ex3/_xmsgs/*.xmsgs?&DataKey=Warning) |
| **Design Goal:** | Balanced | * **Routing Results:** |  |
| **Design Strategy:** | [Xilinx Default (unlocked)](Xilinx%20Default%20(unlocked)?&DataKey=Strategy) | * **Timing Constraints:** |  |
| **Environment:** | [System Settings](D://ms/bcd_ex3/BCD_To_EX3_envsettings.html) | * **Final Timing Score:** |  |

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| --- | --- | --- | --- | --- |
| **Device Utilization Summary (estimated values)** | | | | [**[-]**](?&ExpandedTable=DeviceUtilizationSummary(estimatedvalues)) |
| **Logic Utilization** | **Used** | **Available** | **Utilization** | |
| Number of Slices | 2 | 3584 | 0% | |
| Number of 4 input LUTs | 4 | 7168 | 0% | |
| Number of bonded IOBs | 8 | 173 | 4% | |

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| --- | --- | --- | --- | --- | --- | --- |
| **Detailed Reports** | | | | | | [**[-]**](?&ExpandedTable=DetailedReports) |
| **Report Name** | **Status** | **Generated** | **Errors** | **Warnings** | **Infos** | |
| [Synthesis Report](D://ms/bcd_ex3/BCD_To_EX3.syr) | Current | Tue 29. Jul 03:09:49 2025 | 0 | [6 Warnings (6 new)](D://ms/bcd_ex3/_xmsgs/xst.xmsgs?&DataKey=Warning) | 0 | |
| Translation Report |  |  |  |  |  | |
| Map Report |  |  |  |  |  | |
| Place and Route Report |  |  |  |  |  | |
| Power Report |  |  |  |  |  | |
| Post-PAR Static Timing Report |  |  |  |  |  | |
| Bitgen Report |  |  |  |  |  | |

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| **Secondary Reports** | | | [**[-]**](?&ExpandedTable=SecondaryReports) |
| **Report Name** | **Status** | **Generated** | |

**Date Generated:** 07/29/2025 - 03:09:50