```
module hexdigit (
  2
3
4
5
6
7
                                                              [3:0] in,
[6:0] out);
                        i nput
                        output reg
                        always @(in) begin
                            case (in)

4' b0000: out = 7' b1000000;

4' b0001: out = 7' b1111001;
  8
                             4' b0010: out = 7' b0100100;
  9
                              4' b0011: out = 7' b0110000;
10
                             4' b0101: Out = 7' b0110000;

4' b0100: Out = 7' b0011001;

4' b0101: Out = 7' b0010010;

4' b0111: Out = 7' b0000010;

4' b0111: Out = 7' b1111000;

4' b1000: Out = 7' b0010000;

4' b1010: Out = 7' b0010000;
11
12
13
14
15
16
                             4' b1001: out = 7' b0010000;
4' b1010: out = 7' b0001000;
4' b1011: out = 7' b0000011;
4' b1100: out = 7' b1000110;
4' b1101: out = 7' b0100001;
4' b1111: out = 7' b000110;
4' b1111: out = 7' b0001110;
17
18
19
20
21
22
23
                              endcase
24
                      end
25
             endmodul e
26
27
```

28