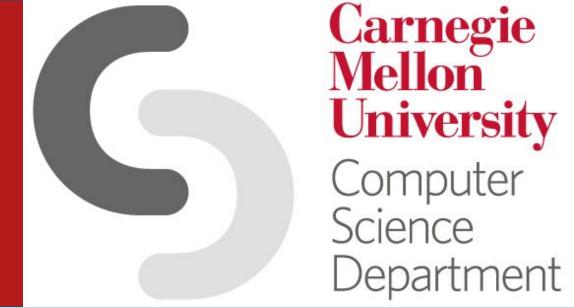
Accelerating GPT-2 Inference with TVM

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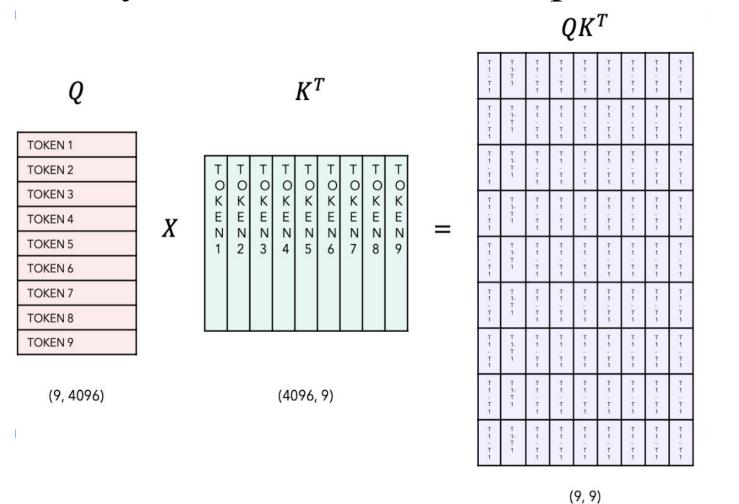


Autoregressive Inference

Under this setting, a decoder-only transformer's computation is mainly matrix-matrix, and matrix-vector multiplications.

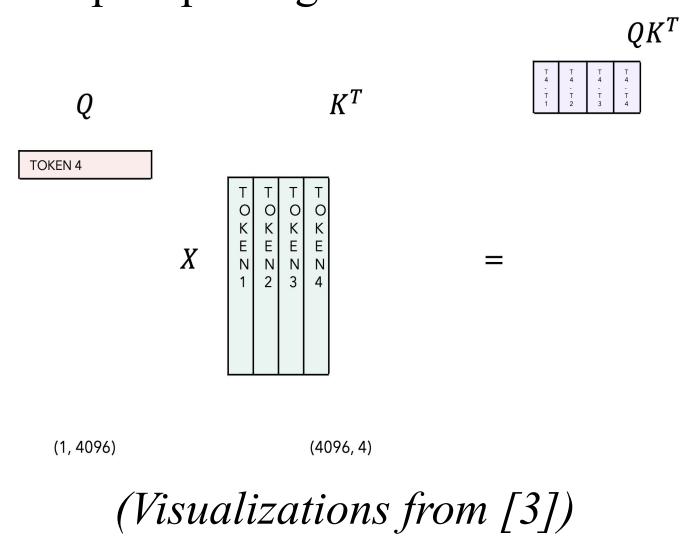
Phase 1: Prefill

Model's input (prompt) has multiple tokens, hence making the workload mainly matrix-matrix multiplication



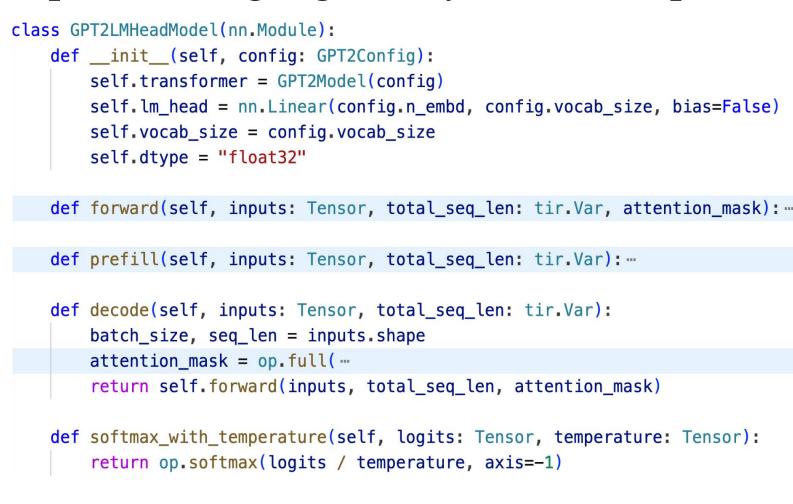
Phase 2: Decode

Model's input is generated token from previous step, hence a single token with matrix-vector multiplications. Keeps repeating until conditions met.



Optimizing GPT-2 Inference in TVM

Step 1. Define the workload in domain specific language w/ symbolic shapes.



Step 2. Optimize with TVM [1]. Among the steps: operator-level optimizations.

dl.ApplyDefaultSchedule(

dl.gpu.Matmul(),

```
dl.gpu.GEMV(),
                         dl.gpu.Reduction(),
                         dl.gpu.GeneralReduction(),
                         dl.gpu.Fallback(),
                            Prefill Speedup
                                           Decode (tokens/sec)
           Prefill (tokens/sec)
                                                              Decode Speedup
          70.0
Baseline
          3321.4
                            47.4x
GEMV
```

60.7

17.3x

29.4x

Table: Performance gain obtained from each operator-level optimization

63.1x

Matmul optimizes **matrix-matrix** multiplication \rightarrow gives speedup to **prefill**. **GEMV** optimizes matrix-vector multiplication \rightarrow gives speedup to decode. **Reduction** optimizes ops like softmax / LayerNorm \rightarrow gives speedup to decode.

Case Study: How TVM Optimizes GEMV

Reduction

Workload of GEMV is essentially $(N, K) \otimes (K) \rightarrow (N)$

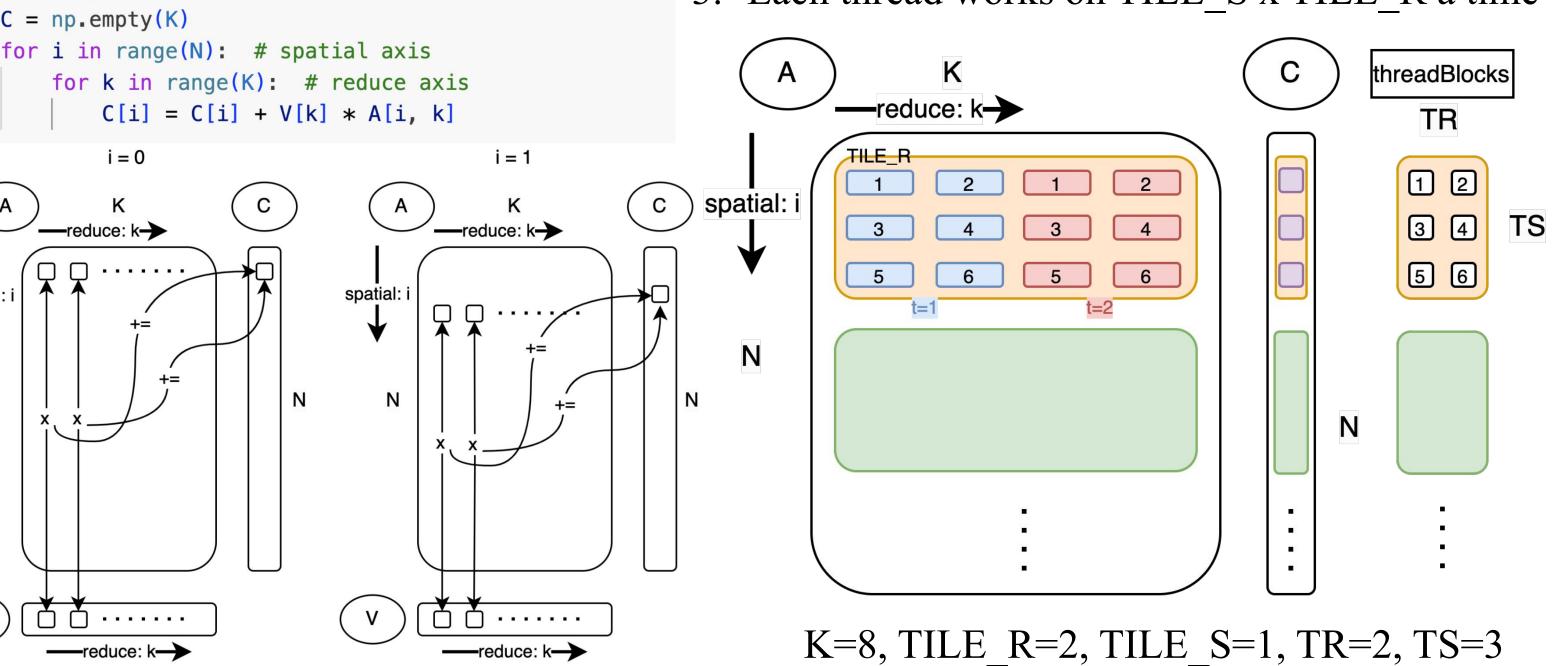
def NK_gemv(A: np.ndarray, V: np.ndarray, C: np.ndarray): # A: (N, K), V: (K), C: (N) C = np.empty(K)for i in range(N): # spatial axis for k in range(K): # reduce axis C[i] = C[i] + V[k] * A[i, k]

Sequential execution of a GEMV workload.

Parallelizing GEMV:

4419.0

- 1. N rows \rightarrow groups of TS rows, 1 group per block
- 2. Each block spawns TS x TR threads
- 3. Each thread works on TILE S x TILE R a time



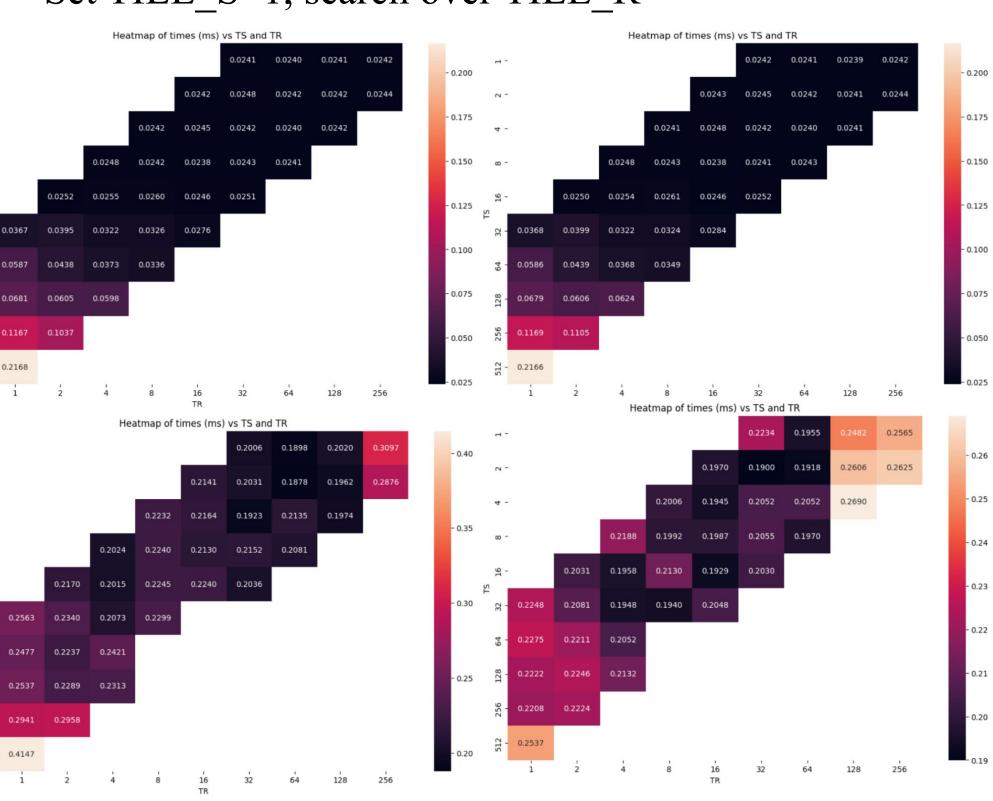
Tuning GEMV to Optimize Decode

Search space:

Make TILE R multiples of 8; consider two schedules:

Sch. 1: Load vectors over N, compute over K Set TILE R=8, search over TILE S

Sch. 2: Load vectors over K, computer over N Set TILE S=1, search over TILE R



How TS and TR affect GEMV given a set pair of TILE_S and TILE_R. Top figures are from RTX 4090; bottom from M2 Ultra. Left figures are from schedule 1; right figures from schedule 2.

	Decode (tokens/sec)
RTX 4090 - Untuned	338.614
RTX 4090 - Tuned	343.402
M2 Ultra - Untuned	117.239
M2 Ultra - Tuned	117.477

References

Comparison of GPT-2 inference with tuned/untuned GEMV.

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