

SDRR

LIDO

Variant: Development
Sample Type: B-Sample

07/04/2022

Version and Revision

0 .2

Sch. Freezed

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DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes .

DESIGN NOTE:
Example text for cautionary
design notes.


DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

TOP VIEW

BOTTOM VIEW

1	2	3	4	5	6	7	8																						
A	ID002 - Block Diagram							A																					
B	BLOCK DIAGRAM - HERARCHICAL DESIGN							B																					
C								C																					
D								D																					
1	2	3	4	5	6	7	8																						
						<table><tr><td colspan="2">Title: *</td><td colspan="2">Dott (emTransit B.V.)</td></tr><tr><td colspan="2">Date: 11/04/2022</td><td>Engineer: FG</td><td>Part Number: *xxxxx</td></tr><tr><td colspan="2">Size: A3</td><td>Sheet 2 of 18</td><td>Version: 0</td></tr><tr><td colspan="2">Project: LIDO</td><td colspan="2">File: LIDO-HW.002.BlockDiagram.SchDoc</td></tr><tr><td colspan="2"></td><td>Revision: .2</td><td>Rev. date: *Param</td></tr></table>		Title: *		Dott (emTransit B.V.)		Date: 11/04/2022		Engineer: FG	Part Number: *xxxxx	Size: A3		Sheet 2 of 18	Version: 0	Project: LIDO		File: LIDO-HW.002.BlockDiagram.SchDoc				Revision: .2	Rev. date: *Param	<div><div></div><div>Westerdok Van Diemenstraat 292 1013 CR, Amsterdam The Netherlands</div></div> <div></div>	
Title: *		Dott (emTransit B.V.)																											
Date: 11/04/2022		Engineer: FG	Part Number: *xxxxx																										
Size: A3		Sheet 2 of 18	Version: 0																										
Project: LIDO		File: LIDO-HW.002.BlockDiagram.SchDoc																											
		Revision: .2	Rev. date: *Param																										

ID003 - Revision History

Index

Date

HISTORY

1

29/04/2022

2

04/05/2022

3

17/05/2022


- ▲

Draft schematic wiht symbo only ready for first reviews within team and Vladimir (external)
- ▲

V0.1 - Corrections based on the first sch. review meeting.
- ▲

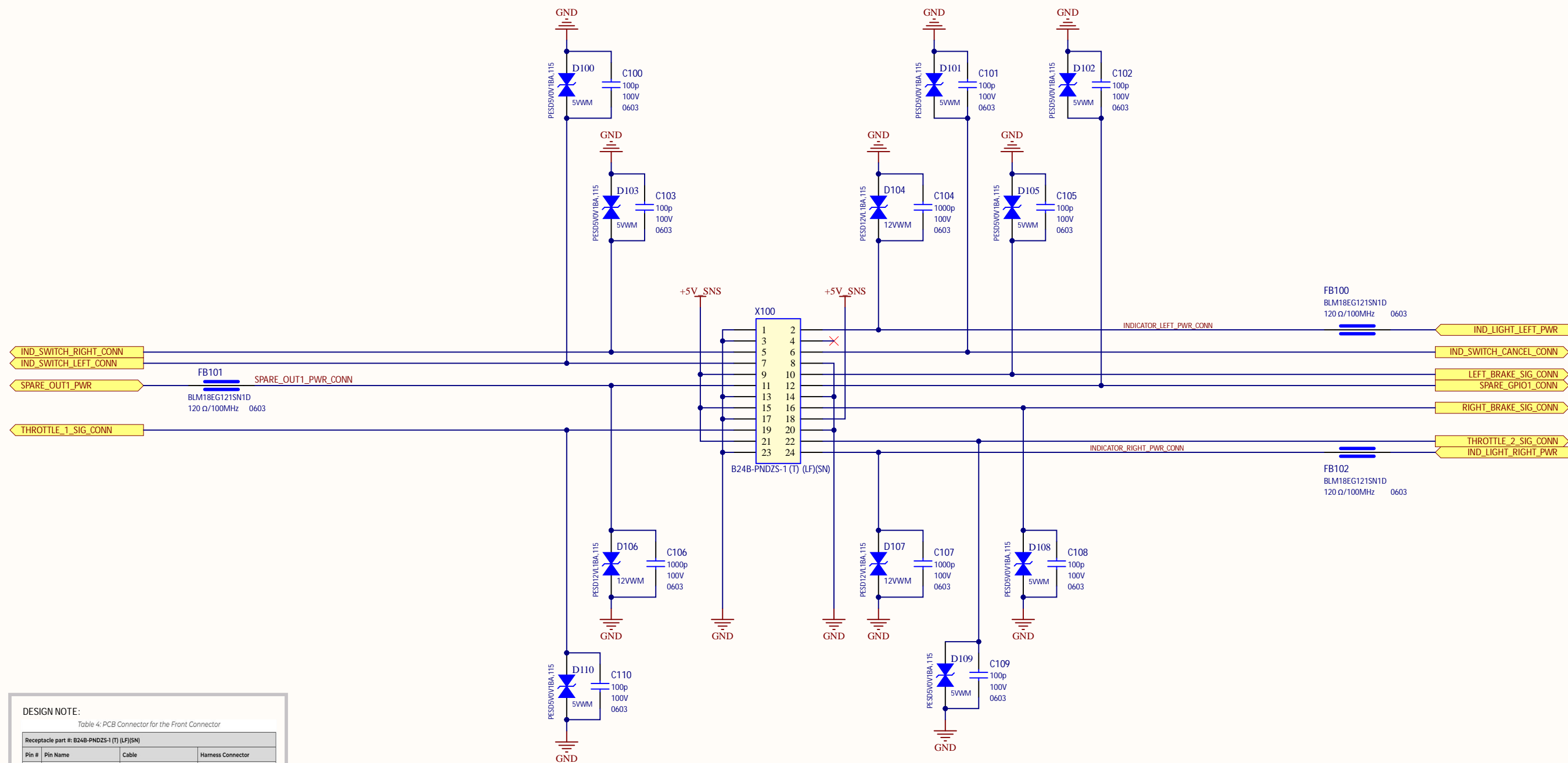
V0.2 - Vladimir's comments/suggestions added to schematic.
Old symbol where replaced by real components (Symbol + Footprint) from the databsae library with the part numbers provided by Vladimir.
Some of the part numbers change due to no stock. main parameter remains.

1	2	3	4	5	6	7	8	
ID004 - Revision History								
A								A
B								B
C								C
D								D
1	2	3	4	5	6	7	8	

Title: *					Dott (emTransit B.V.)		
Date: 11/04/2022		Engineer: FG		Part Number: *xxxxx		Westerdok	
Size: A3		Sheet 4 of 18		Revision: .2		Van Diemenstraat 292	
Version: 0				Rev. date: *Param		1013 CR, Amsterdam	
Project: LIDO		File: LIDO-HW.004.RevisionHistory.SchDoc				The Netherlands	



ID100 - FRONT CONNECTOR



DESIGN NOTE:

Table 4: PCB Connector for the Front Connector

Receptacle part #: B24B-PNDZS-1 (T) (LF)(SN)			
Pin #	Pin Name	Cable	Harness Connector
1	IND_LIGHT_LEFT_GND	Left indicator	D-Z208FS (D series, 2 pin, female)
2	IND_LIGHT_LEFT_PWR		
3	IND_SWITCH_GND		
4	NC	Control switch	D-Z508FS (D series, 5 pin, female)
5	IND_SWITCH_RIGHT_CONN		
6	IND_SWITCH_CANCEL_CONN		
7	IND_SWITCH_LEFT_CONN	Left brake	D-Z308FS (D series, 3 pin, female)
8	LEFT_BRAKE_SIG_GND		
9	+5V_SNS		
10	LEFT_BRAKE_SIG	Spare pins (no cable)	N/A
11	SPARE_OUT1_PWR		
12	SPARE_GPIOT		
13	SPARE_GPIOT_GND	Right brake	F-Z309FS (F series, 3 pin, female)
14	RIGHT_BRAKE_GND		
15	+5V_SNS		
16	RIGHT_BRAKE_SIG	Throttle	F-Z609FS (F series, 6 pin, female)
17	THROTTLE_1_GND		
18	+5V_SNS		
19	THROTTLE_1_SIG	Right indicator	F-Z209FS (F series, 2 pin, female)
20	THROTTLE_2_GND		
21	+5V_SNS		
22	THROTTLE_2_SIG		
23	IND_LIGHT_RIGHT_GND		
24	IND_LIGHT_RIGHT_PWR		

Title: *

Date: 11/04/2022 Engineer: FG

Size: A3 Sheet 5 of 18 Version: 0

Project: LIDO

Part Number: *xxxxx

Revision: .2

Rev. date: *Param

File: LIDO-HW.100.FrontConnector.SchDoc

Dott (emTransit B.V.)

Westerdok

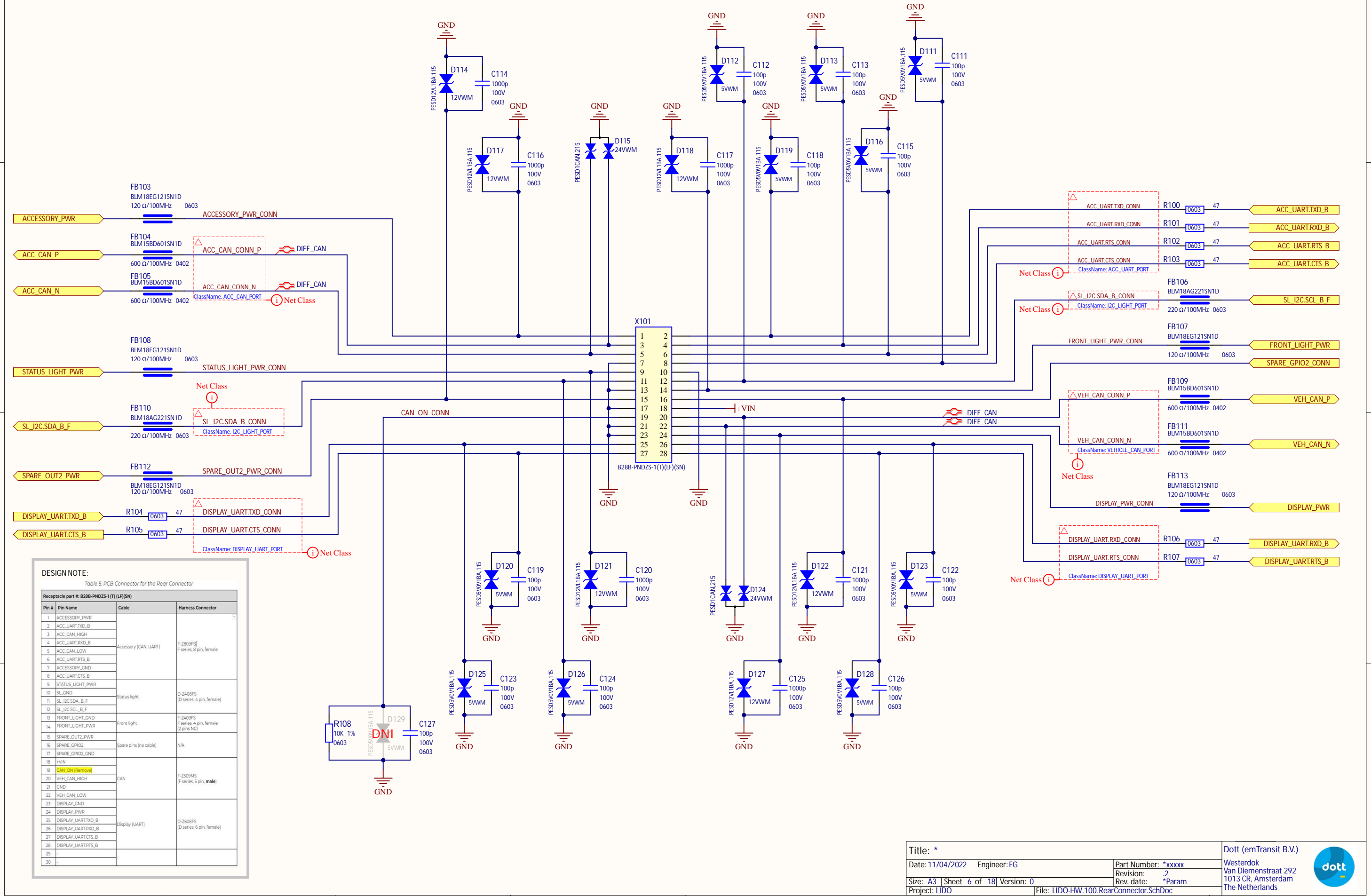
Van Diemenstraat 292

1013 CR, Amsterdam

The Netherlands

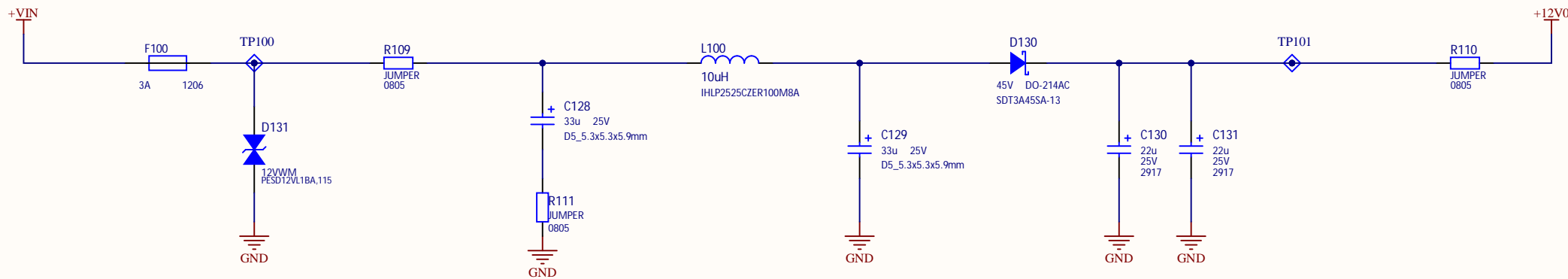


ID100 - REAR CONENCTOR

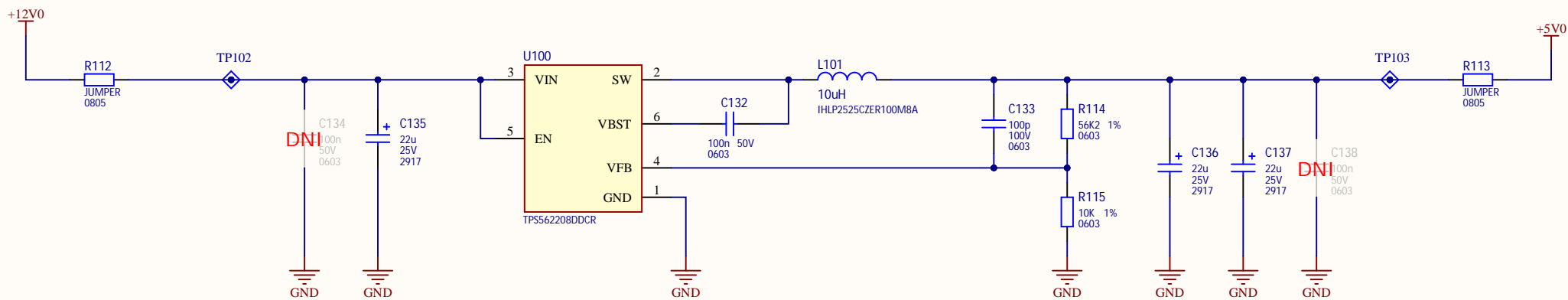


ID100 - INPUT VOLTAGE AND POWER SUPPLIES

INPUT VOLTAGE AND FILTER

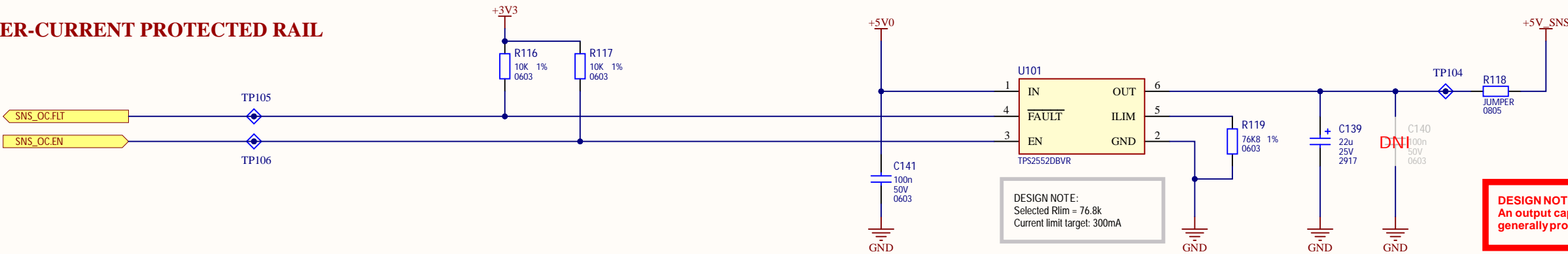


+5V POWER RAIL



TPS56220x LAYOUT NOTE:
4. Keep the SW trace as physically short and wide as practical to minimize radiated emissions.
5. Do not allow switching current to flow under the device.
6. A separate VOUT path should be connected to the upper feedback resistor.
7. Make a Kelvin connection to the GND pin for the feedback path.
8. Voltage feedback loop should be placed away from the high-voltage switching trace, and preferably has ground shield.

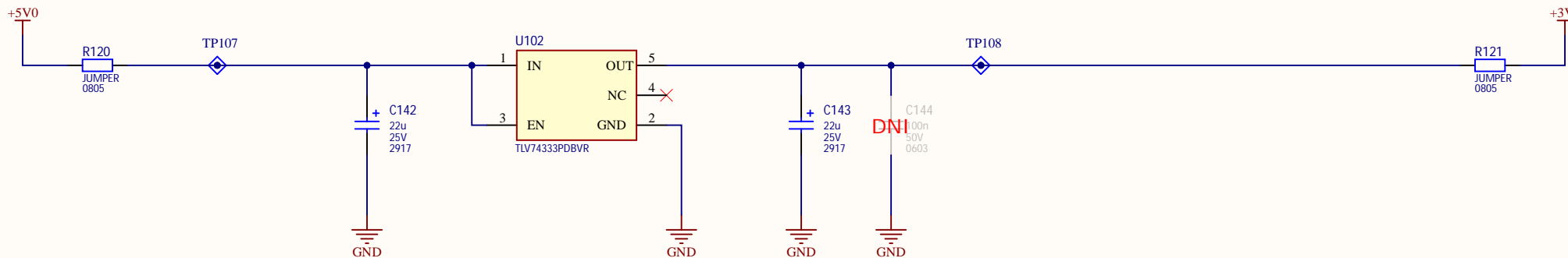
+5V OVER-CURRENT PROTECTED RAIL



DESIGN NOTE:
Selected Rlim = 76.8k
Current limit target: 300mA

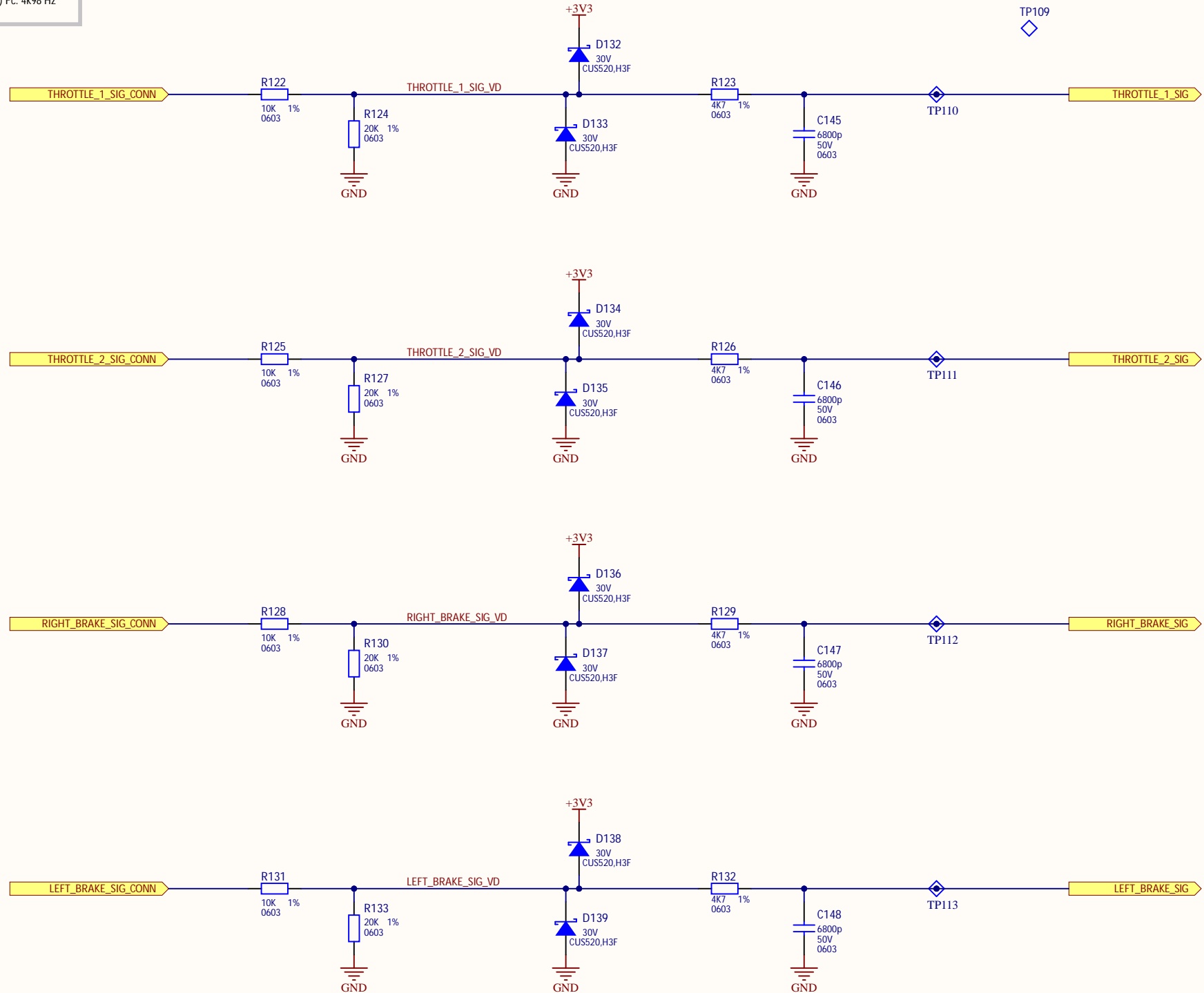
DESIGN NOTE:
An output capacitance of 1 μ F or larger generally provides good dynamic response.

+3V3 POWER RAIL



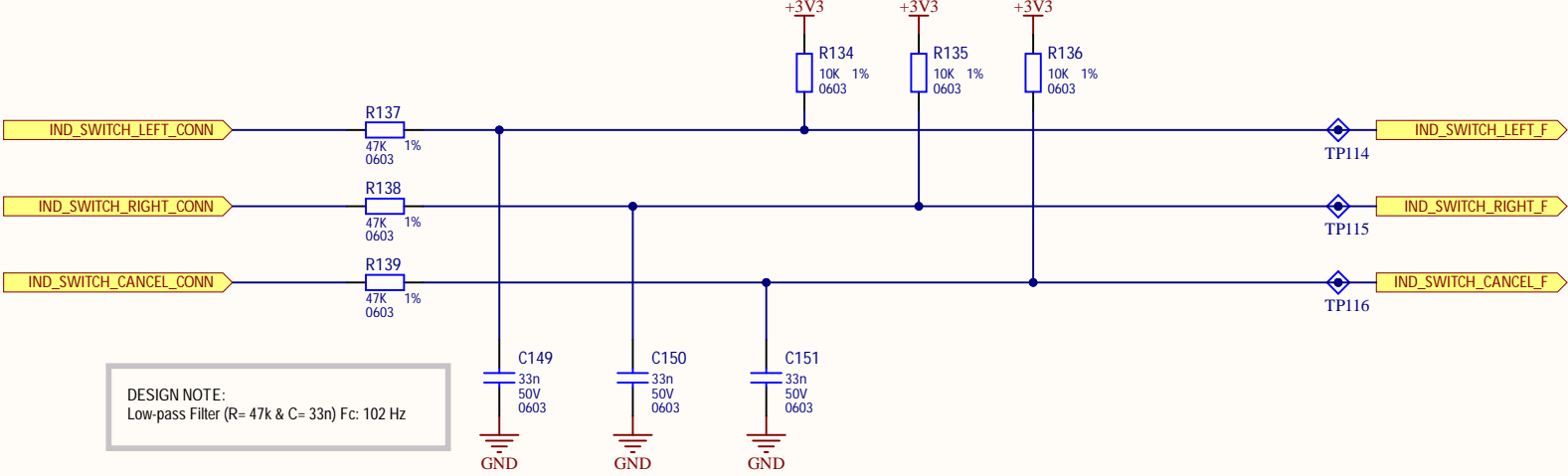
ID100 - INPUT STAGE

DESIGN NOTE:
Low-pass Filter (R= 4k7 & C= 6n8) Fc: 4k98 Hz

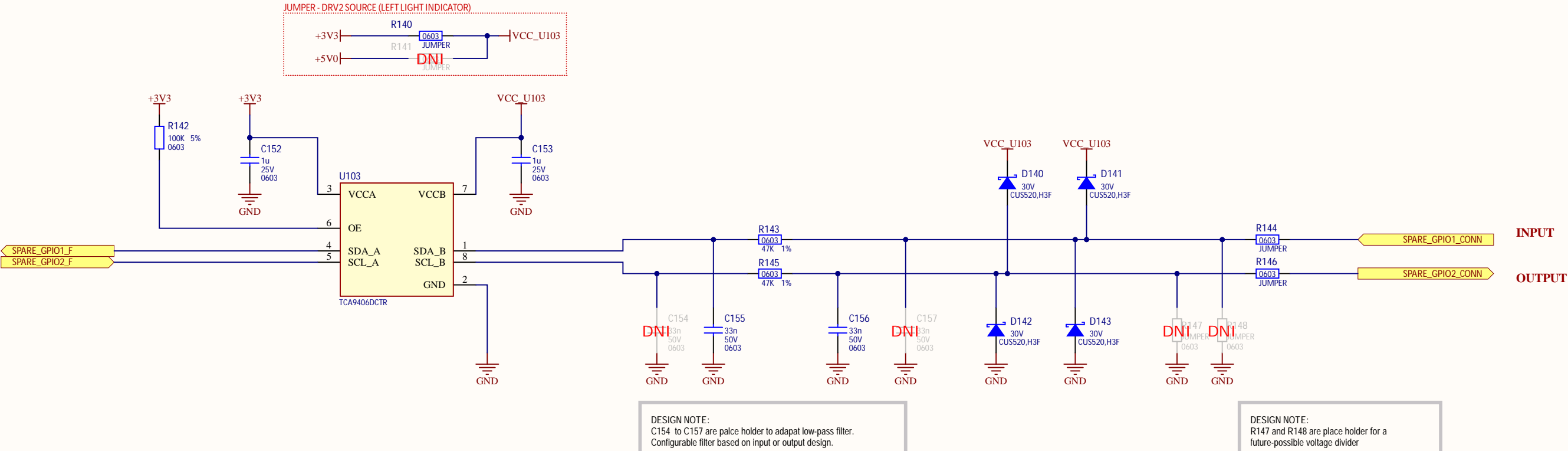


ID100 - INPUT STAGE

SENSOR SIGNALS



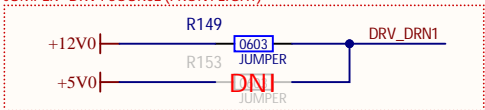
GENERIC GPIO



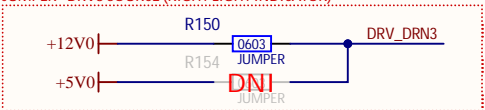
ID100 - OUTPUT STAGE

DRIVER VOLTAGE SELECTOR

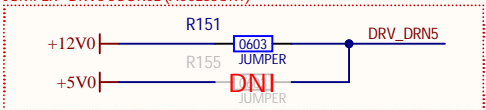
JUMPER - DRV1 SOURCE (FRONT LIGHT)



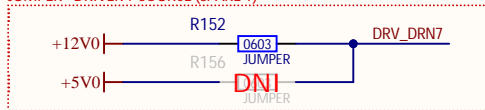
JUMPER - DRV3 SOURCE (RIGHT LIGHT INDICATOR)



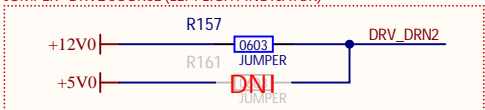
JUMPER - DRV5 SOURCE (ACCESSORY)



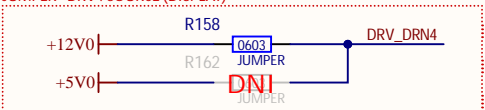
JUMPER - DRIVER 7 SOURCE (SPARE 1)



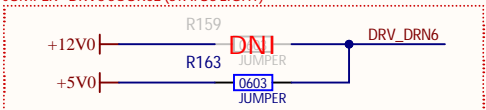
JUMPER - DRV2 SOURCE (LEFT LIGHT INDICATOR)



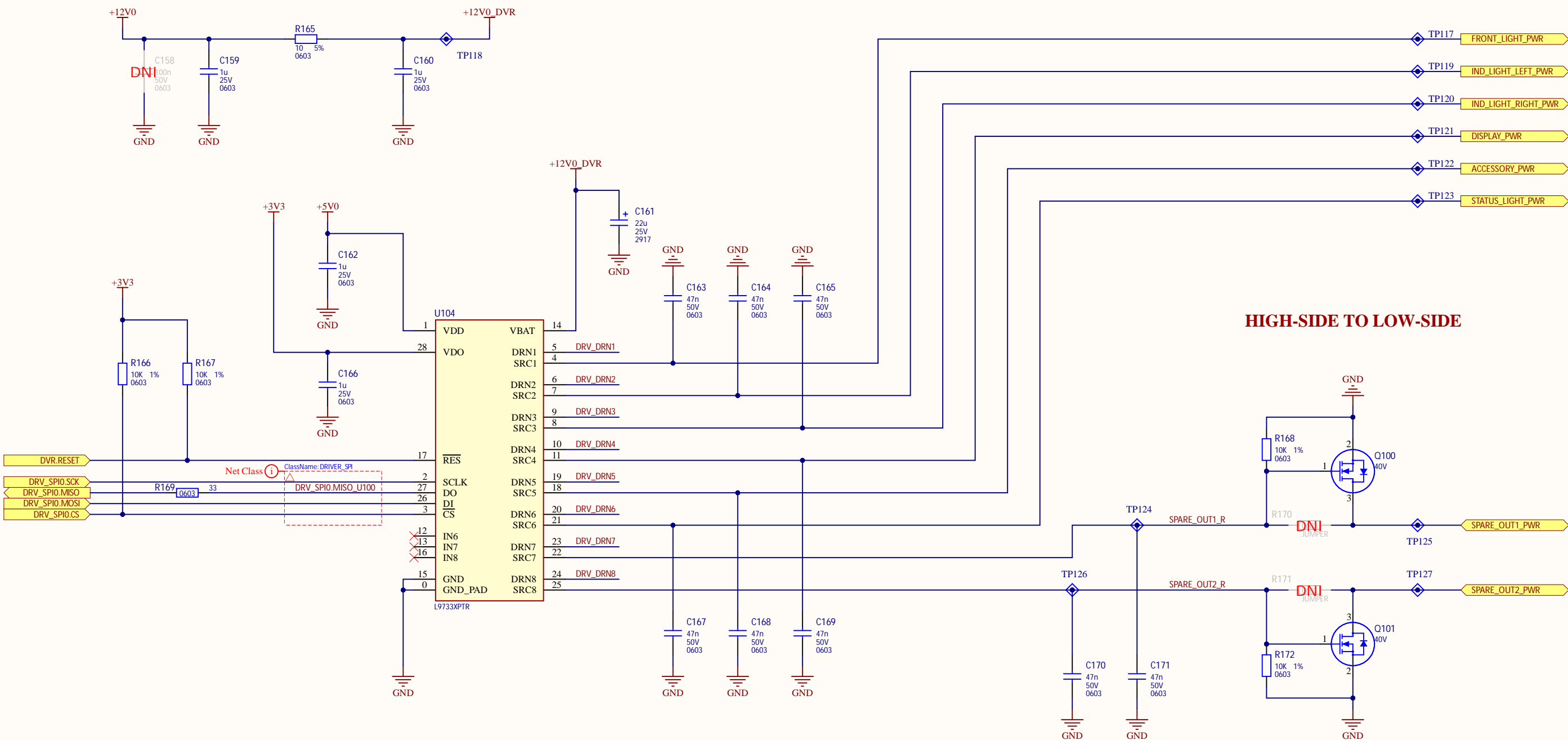
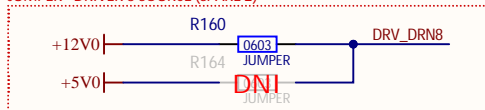
JUMPER - DRV4 SOURCE (DISPLAY)



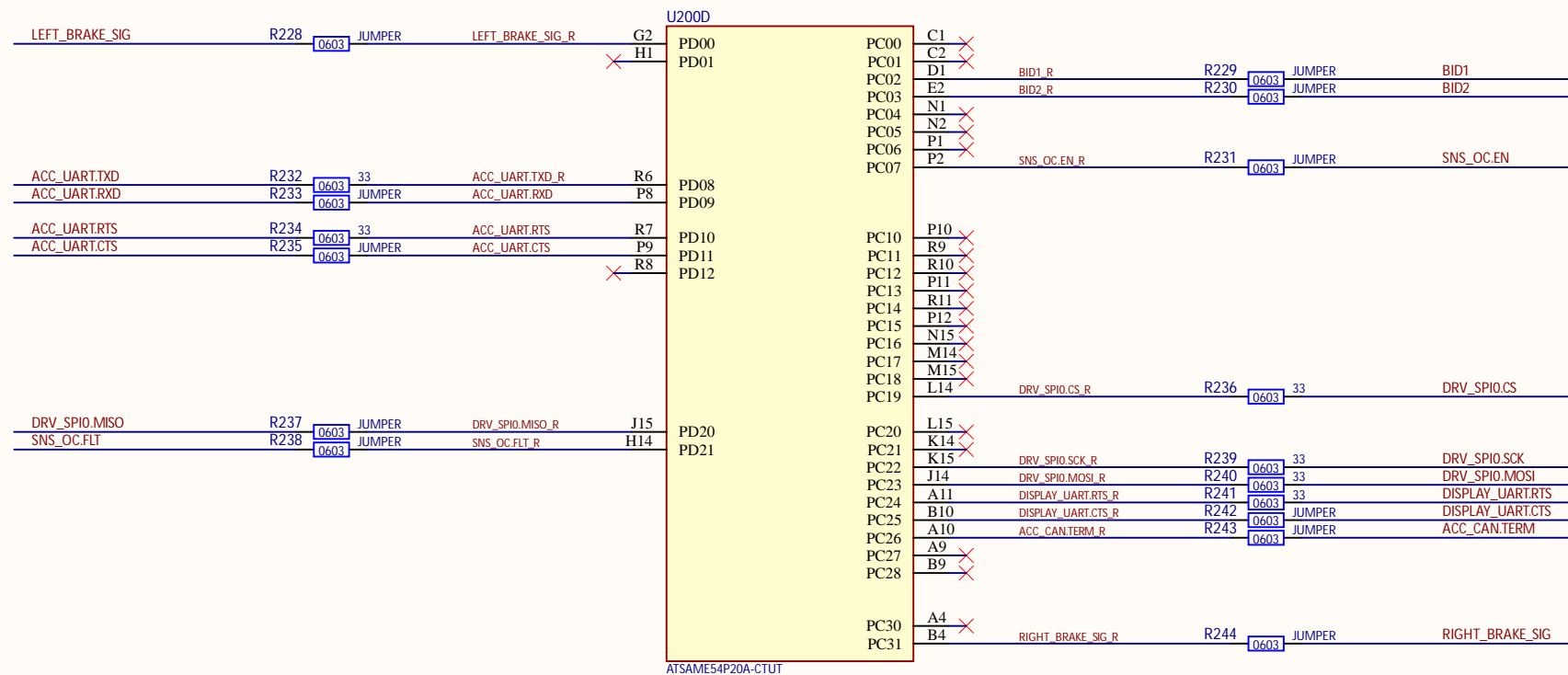
JUMPER - DRV6 SOURCE (STATUS LIGHT)



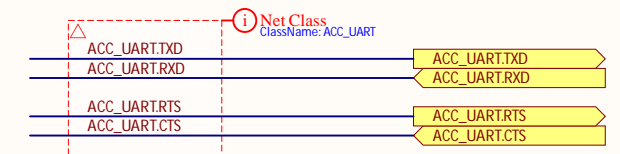
JUMPER - DRIVER 8 SOURCE (SPARE 2)



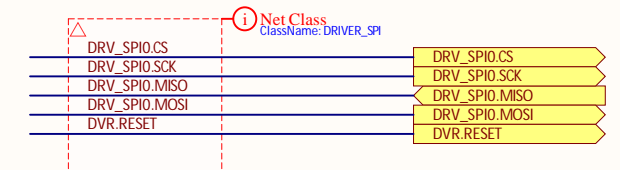
ID200 - MCU PERIPHERALS



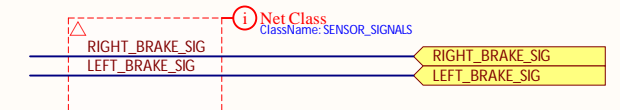
ACCESSORY UART



HIGH-SIDE DRIVER



INPUT SENSORS



ACCESSORY CAN



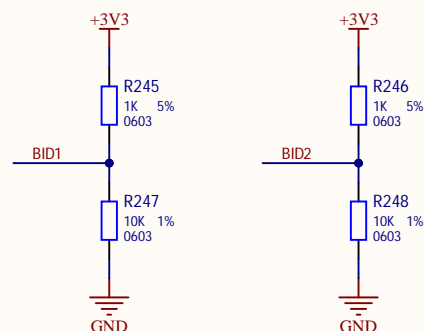
DISPLAY UART




OVER-CURRENT SWITCH



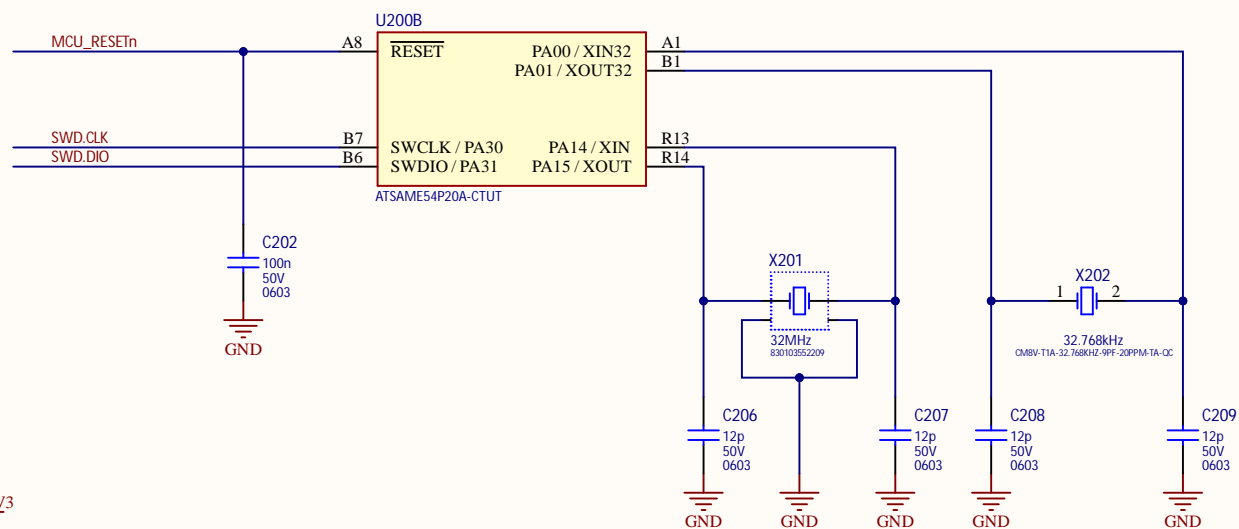
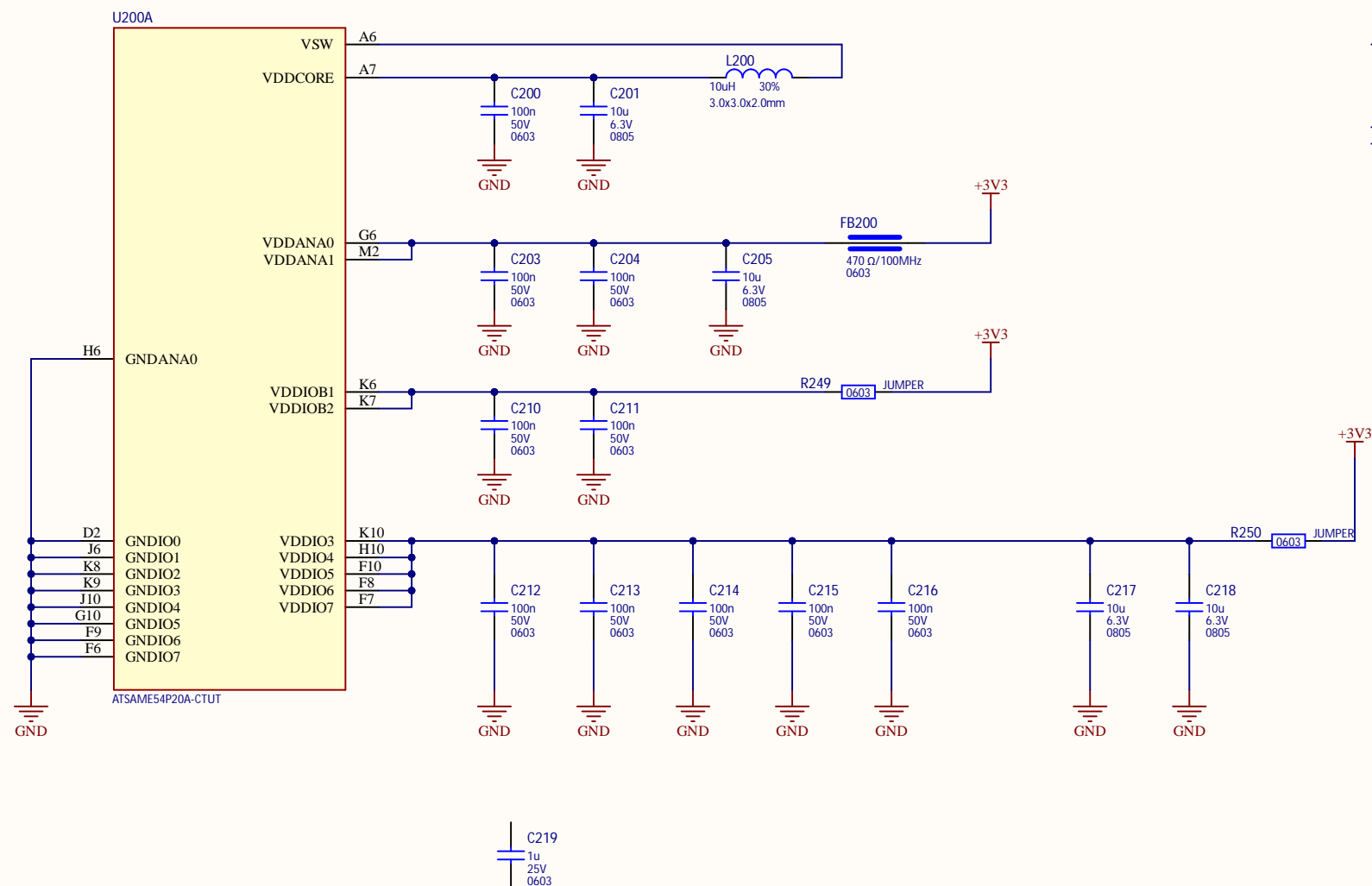
BOARD ID



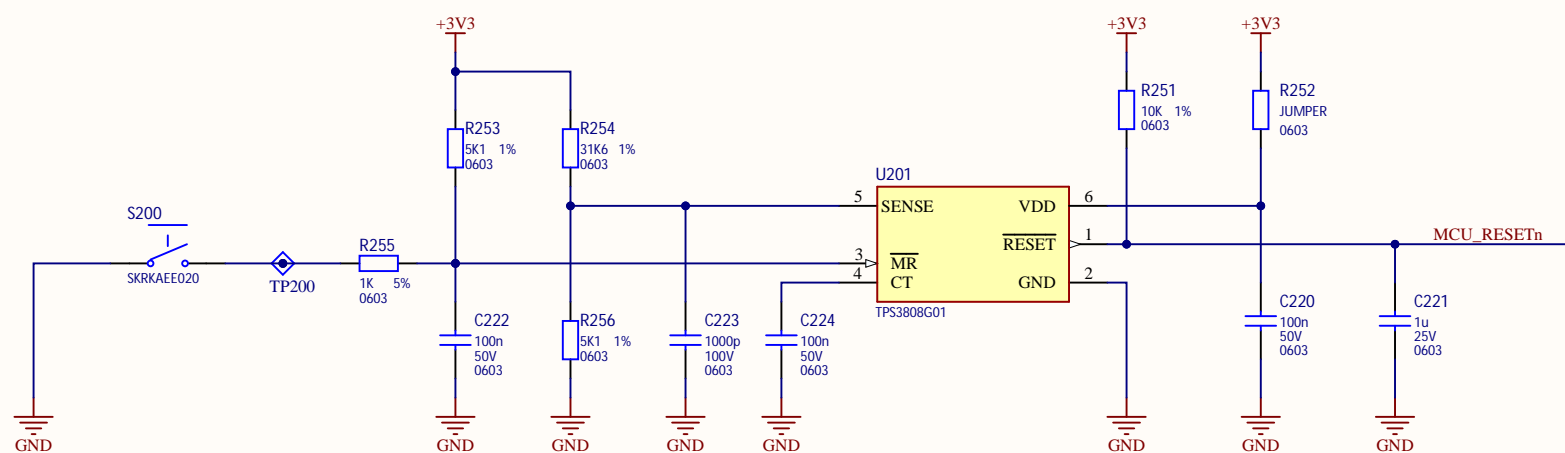
DESIGN NOTE:

Title: *			Dott (emTransit B.V.)		
Date: 11/04/2022	Engineer: FG	Part Number: *xxxxx	Westerdok		
		Revision: 2	Van Diemenstraat 292		
		Rev. date: *Param	1013 CR, Amsterdam		
Size: A3	Sheet 12 of 18	Version: 0	The Netherlands		
Project: LIDO		File: LIDO-HW.200.MCU			
		Peripherals: 2_SchDoc			

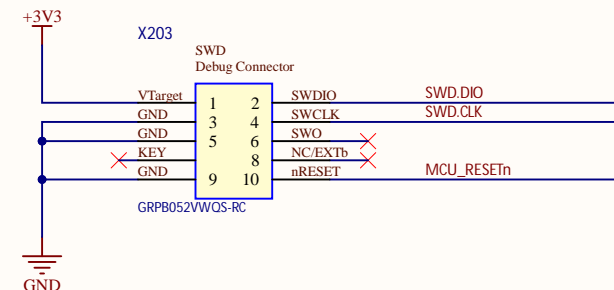
ID200 - MCU POWER, SWD AND RESET



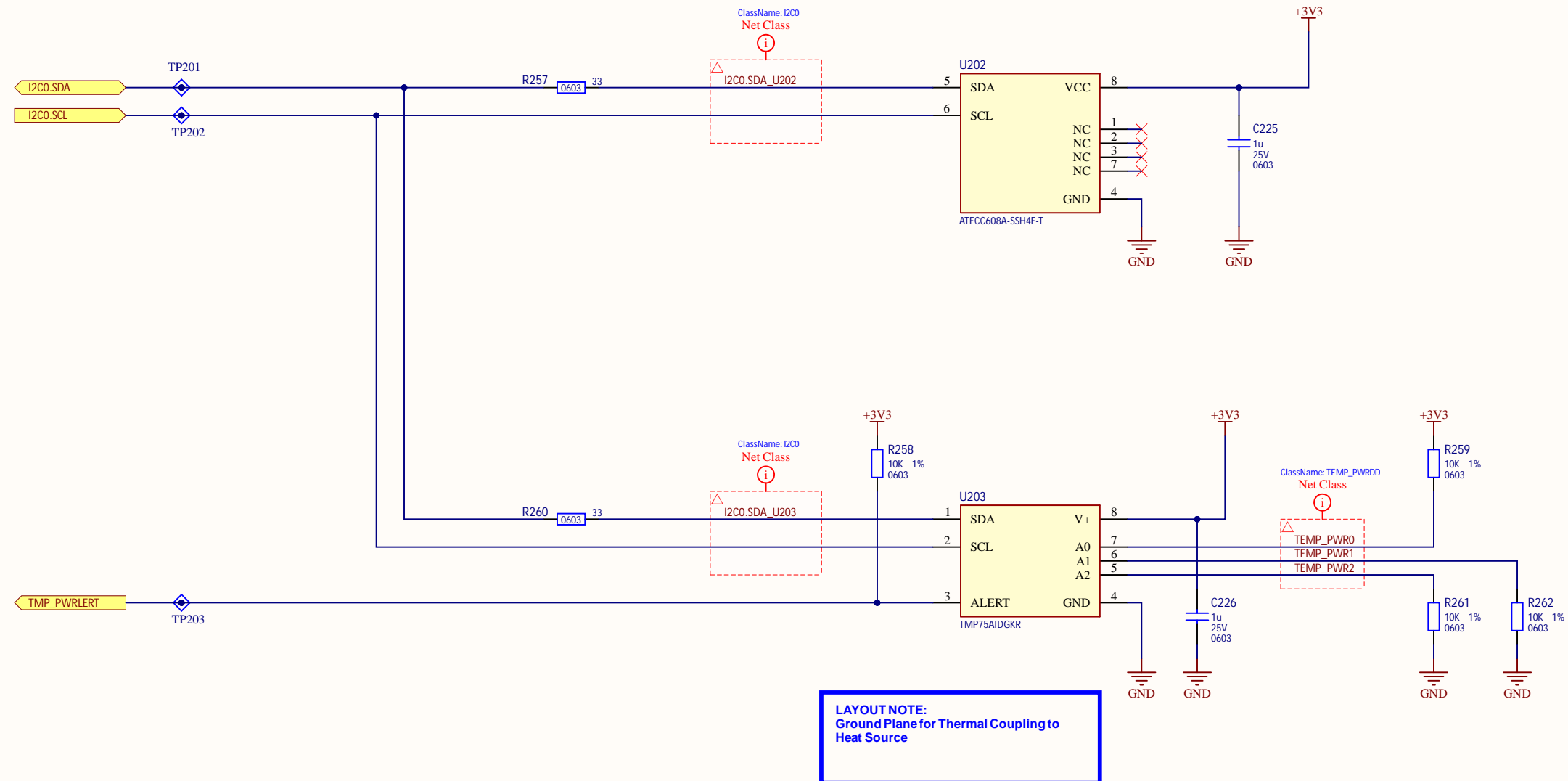
RESET SUPERVISOR



SWD DEBUG CONNECTOR

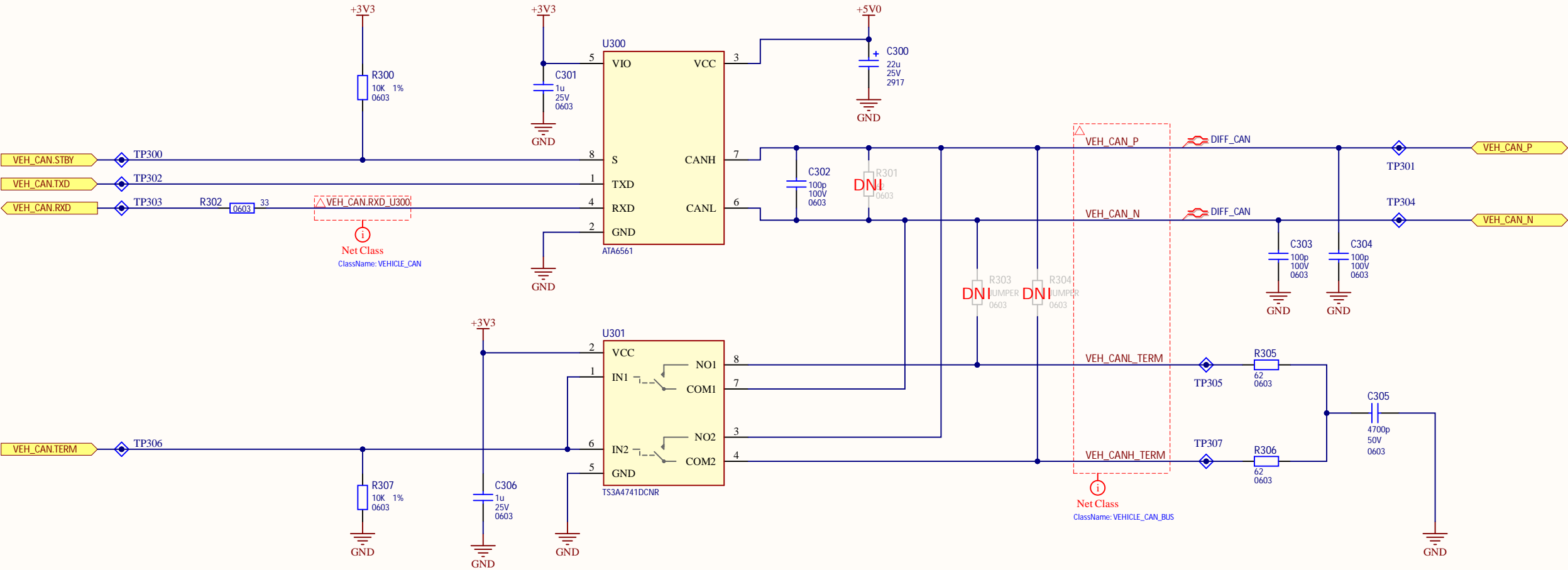


ID200 - SECURITY CHIP AND TEMPERATURE SENSOR



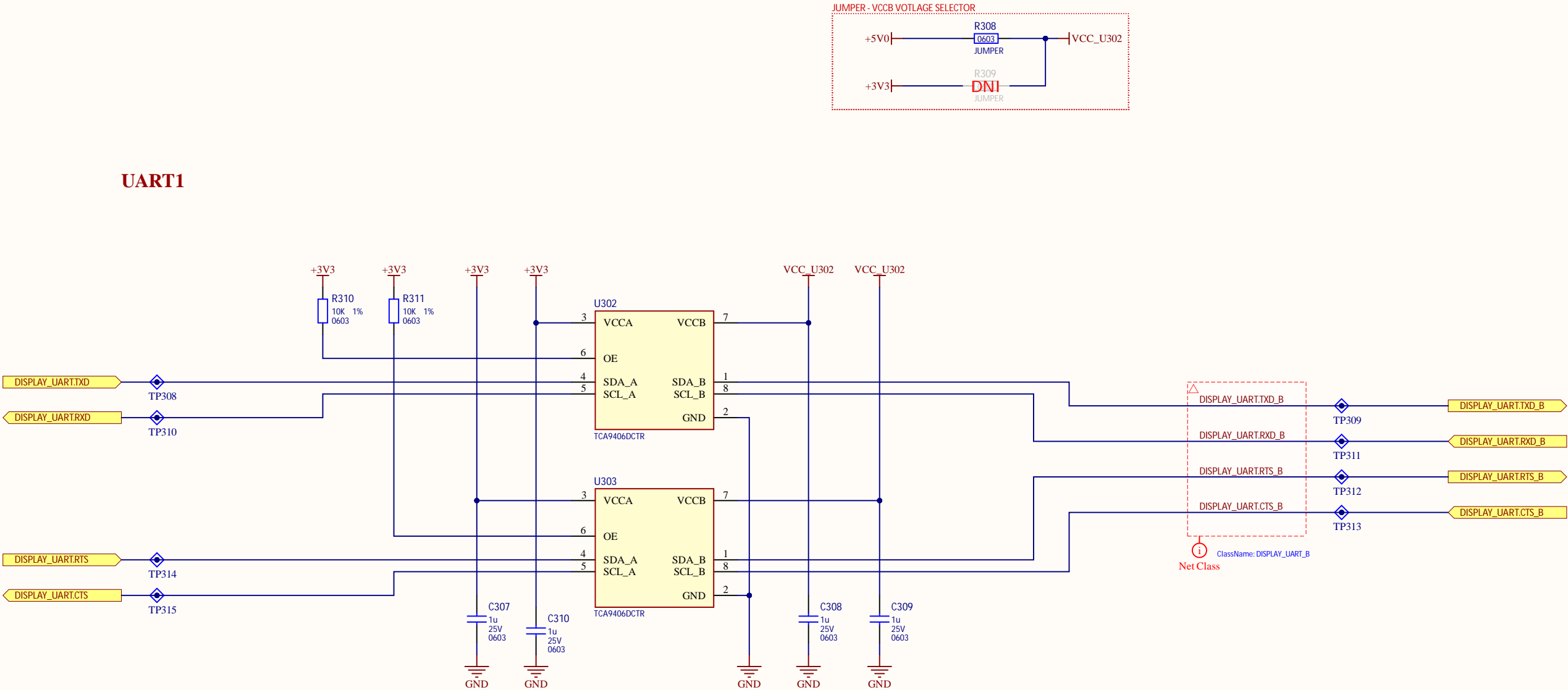
ID300 - VEHICLE INTERFACE

CAN 0 BUS



ID300 - DISPLAY INTERFACE


UART1



JUMPER - VCCB VOLTAGE SELECTOR

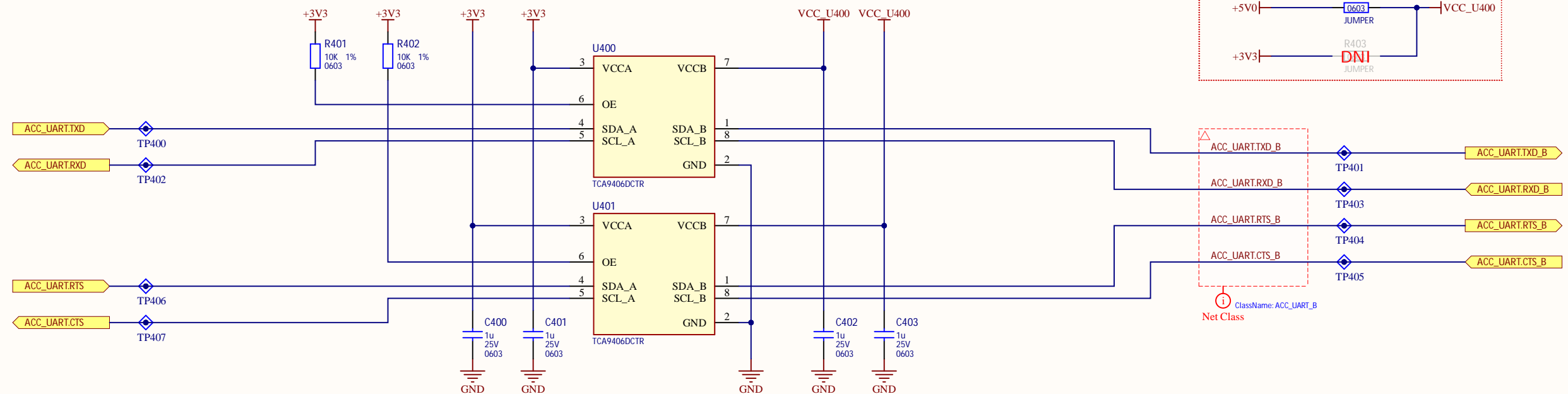
+5V0 — R312 0603 JUMPER — VCC_U304

+3V3 — R313 DNI JUMPER — VCC_U304

Title: *			Dott (emTransit B.V.) Westerdok Van Diemenstraat 292 1013 CR, Amsterdam The Netherlands	
Date: 11/04/2022	Engineer: FG	Part Number: *xxxxx		
		Revision: 2		
Size: A3 Sheet 17 of 18	Version: 0	Rev. date: *Param		
Project: LIDO	File: LIDO-HW.300.StatusLightInterface.SchDoc			

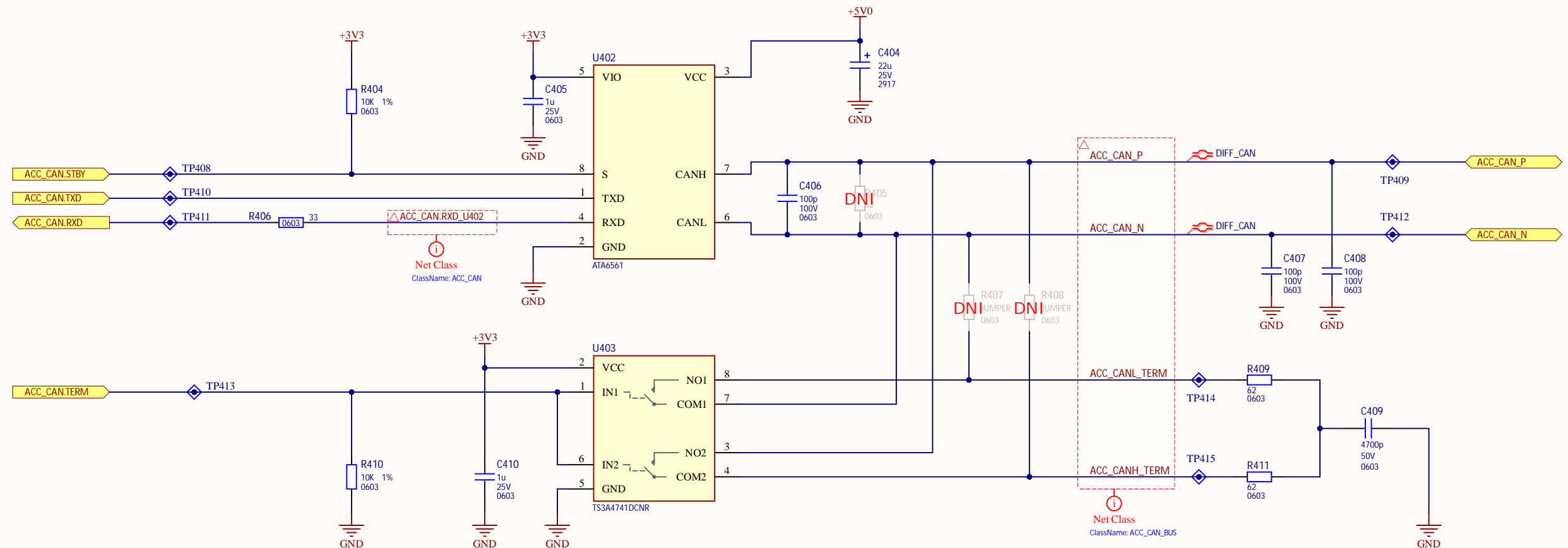
ID400 - ACCESSORY INTERFACES

ACCESSORY DISPLAY UART 0



ACCESSORY CAN BUS

CAN 1



Title: *				Dott (emTransit B.V.)	
Date: 11/04/2022		Engineer: FG		Part Number: *xxxxx	
Size: A3		Sheet 18 of 18		Revision: .2	
Version: 0				Rev. date: *Param	
Project: LIDO		File: LIDO-HW.400.AccessoryInterfaces.SchDoc			
				Westerdok	
				Van Diemenstraat 292	
				1013 CR, Amsterdam	
				The Netherlands	

