►Solution◄

Question 1: (5 points)
Write the C code that best corresponds to the following MIPS assembly code. Assume that registers $s1, $s2, and $s3 store the values of 32-bit integers x, y, and z.

```
L1: beq $s1, $zero, L2
    addu $s1, $s1, $s2
    addu $s1, $s1, $s3
L2:
```

> **Solution:**
>
> ```
> if (x != 0) {
>  x = x + y;
>  x = x + z;
> }
> ```