

Question 2 (20 points): At the Hot Chips Conference in Cupertino, CA, in August 2017, Microsoft announced that its Project Brainwave would leverage Field-Programmable Gate Arrays (FPGAs) to deliver real-time Artificial Intelligence through Deep Neural Networks. Microsoft disclosed that this new hardware would use a Microsoft custom 8-bit floating point format (ms-fp8). While no details have been disclosed for this format, earlier publications have proposed FP8 with one bit for sign, four bits for exponent and three bits for mantissa. Assuming that Microsoft made this design decision and that no representation is reserved for special values such as NaN and $\pm\infty$, we can assume the following FP8 format definition.

7	6	3	2	0
S	<i>biasedexponent</i>	<i>fraction</i>		

$$N = \begin{cases} (-1)^S \times 0.\textit{fraction} \times 2^{-6} & \text{if } \textit{biasedexponent} = 0 \\ (-1)^S \times 1.\textit{fraction} \times 2^{\textit{biasedexponent}-7} & \text{if } 0 < \textit{biasedexponent} \leq 15 \end{cases}$$

1. (6 points) What is the largest positive number that can be represented in this FP8 format? Provide both the binary representation and the decimal value.
2. (6 points) What is the smallest positive non-zero number that can be represented in this FP8 format? Provide both the binary representation and the decimal value.
3. (8 points) Let $A = 0x41$ and $B = 0x39$ be the binary representation of two numbers in this FP8 format. What is the representation of $A + B$, expressed in hexadecimal? Assume that the hardware that performs this addition has a guard, a round, and a sticky bit. Also assume that the round-to-the-closest-even strategy is used when needed.