

# CMPUT 229 (A1) - Computer Organization And Architecture I

## Quiz #0 — Fall 2013

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### CMPUT 229 Honor Code

By turning in the quiz solution for grading, I certify that I have produced the solution in accordance to the academic integrity policies in Section 26.1 of the University of Alberta 2013/2014 Calendar.

Edmonton, September 11, 2013.

#### Question 1 (100 points):

This is a **true** or **false** question. For each statement you have to indicate whether the statement is true or false. Use the capital letter T to indicate true, and the capital letter F to indicate false. You win 20 points for each correct answer. **You lose 10 points for each incorrect answer.** You are neither rewarded nor penalized for statements that you do not mark. If you mark all statements correctly you will earn a maximum of 100 points in this question. Regardless of how many statements you mark wrong, your score in this question cannot be below zero.

- a. ( **T** ) A *volatile* memory retains data only if it is receiving power.
- b. ( **F** ) The *application binary interface* defines a high-level intermediate representation used by compilers to optimize a computer program.
- c. ( **F** ) A *compiler* is a program that inserts high-level comments to document an assembly language program.
- d. ( **T** ) A *cache memory* is smaller and faster than the main memory in a computer. The cache memory acts as a buffer between the processing speed of the processor and the speed of operation of the computer's main memory.
- e. ( **T** ) *Assembly language* is the symbolic representation of machine instructions while *machine language* is the binary representation of machine instructions.