

CMPUT 229 - Computer Organization And Architecture I

Quiz #0 — Winter 2014

Jacqueline Smith
Computing Science Department - University of Alberta

Name: Solution

CMPUT 229 Honor Code

By turning in the quiz solution for grading, I certify that I have produced the solution in accordance to the academic integrity policies in Section 26.1 of the University of Alberta 2013/2014 Calendar.

Question 1 (10 points):

This is a true or false question. For each statement you have to indicate whether the statement is true or false. Use the capital letter T to indicate true, and the capital letter F to indicate false. You win 2 points for each correct answer. You lose 1 point for each incorrect answer. You are neither rewarded nor penalized for statements that you do not mark. If you mark all statements correctly you will earn a maximum of 10 points in this question. Regardless of how many statements you mark wrong, your score in this question cannot be below zero.

- a. (T) The *assembler* translates symbolic, human-readable instructions into the binary version the machine understands.
- b. (F) *Embedded* applications have the same power and reliability requirements as general-purpose computers.
- c. (T) The *instruction set architecture* of a computer is the abstraction from hardware to low-level software.
- d. (T) *Volatile* memory holds data and programs while the machine is running, but should not be used for storage between runs.
- e. (F) The *datapath* component of the processor controls the processor, memory and I/O devices according to a program's instructions.