Question 1 (20 points): Write MIPS assembly code, using the minimal number of instructions, for each of the C statements below. The code for each of the statements is independent of the code for any other statement. Assume that the C program contains the declarations below.

| <pre>int **records; int i; char A[100]; int B[100];</pre> | | |
|---|---|------------------|
| Assume that tregister usage: | these variables have already been loaded into registers according | to the following |
| \$s0: records \$s1: i \$s2: A \$s3: B | | |
| a. (5 points) | A[i] = 'c'; | |
| b. (5 points) | A[B[i]] = 0; | |
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| c. | (5 points) | (*records)[25] = B; |
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| d. | (5 points) | A[(*records)[32]] = A[B[10]]; |
| d. | (5 points) | A[(*records)[32]] = A[B[10]]; |
| d. | (5 points) | A[(*records)[32]] = A[B[10]]; |
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| d. | (5 points) | A[(*records)[32]] = A[B[10]]; |