

Question 4 (20 points): For your Science Internship Program you have joined a startup that is developing a mobile hardware to be installed in the cashier of coffee shops and restaurants that participate in the PayItForward initiative. The idea of this initiative is that customers can anonymously pay a coffee or a meal to someone that cannot afford it.¹ The hardware is needed to allow independent scrutiny of the balances in each place. This startup has two versions of the hardware and two versions of the compiler to support this system. The main app run in this hardware is PIFStatus. The instructions executed by PIFStatus can be divided into four classes. The table below shows the average number of cycles per instruction for the two versions of hardware (with no L2 cache and with L2 cache). It also shows the total number of instructions executed when PIFStatus is compiled with optimization -O0 and -O3.

		Instruction Class			
		ALU	Mul	Branches	L/S
Cycles	No L2	35	54	40	138
	with L2	16	25	19	56
# instructions (in millions)	-O0	10	2	1	5
	-O3	8	1	1	

1. **(6 points)** What is the CPI when PIFStatus is compiled at the -O0 level and runs in the hardware with no L2?
2. **(7 points)** You have experimentally determined that PIFStatus compiled at the -O3 level and run with L2 is three times as fast as PIFStatus compiled at the -O0 level and run in the hardware with no L2. How many L/S instructions are executed when PIFStatus is compiled at the -O3 level?
3. **(7 points)** If the execution time of PIFStatus compiled at the -O0 level and run on the hardware with no L2 is 594 milliseconds (1 millisecond = 10^{-3} seconds), what is the frequency of clock, expressed in *GHz* (1 *GHz* = 10^9 Hz) in which the processor is executing?

¹This question is inspired on The Nook Cafe in downtown Edmonton.