Rigoberto Orozco-Diaz
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Curriculum Vitae

Objective
To obtain an internship in hardware/software engineering to build on the skills I've learned through coursework and to gain experience as an engineer.

Education
University of Washington, Seattle, WA
- B.S, Electrical Engineering GPA 3.5, in progress
- Concentration on Embedded Systems
- Expected Graduation: June 2016

Related Course Work:
- Digital Circuits and Systems
- Semiconductor Diode and Field-effect Transistor Circuit Design
- Bipolar Transistor Circuit Design
- Design of Digital Circuits and Systems
- Computer Network Architectures and Protocols
- Microcomputer Systems

Skills
Programming languages: Verilog, C, Java, Matlab
Software: Quartus, ModelSim, MultiSim (SPICE)
2D/3D Drafting and Modeling: AutoDesk AutoCAD and Revit
Web Developing: HTML, CSS
Spoken Languages: Spanish (fluent), Portuguese (elementary)

Work Experience
Engineers Without Borders, UW Chapter, Seattle, WA
English-Spanish Translator Volunteer (March 2015 – Present)
- Explain details of building a multi-purpose outdoor auditorium in Guatemala

Speak with construction managers regarding present problems and/or future plans

The Fisherman's Restaurant, Seattle, WA
Cocktail Waiter (June 2013 – Present)
- Wait 7+ tables
- Possess knowledge of drinks in order to provide accurate descriptions to customers
- Work as a team member

References
Bob Strecker (Fraternity Chapter Advisor, 3 years)
Cell phone: (206) 778-1826
Email: bobstrecker@comcast.net

Luli Olivera (Family Member, 21+ years)
Work phone: (206) 922-0289
Email: lulio@amazon.com

Project Experience
Bluetooth Controlled Autonomous RoboTank: Created drivers and software in C to program the tank to be Bluetooth controlled and autonomous to avoid collisions. Technologies used: Embedded C, Real Time Operating System (FreeRTOS)

Universal Asynchronous Receive/Transmitter: built for a microprocessor application on a FPGA used to send and receive byte sized data over a serial link. Technologies used: C, FPGA Programming

Arithmetic Logic Unit: performed bitwise operations of addition, subtraction, multiplication and division of up to 8-bit numbers on a FPGA Technologies used: FPGA Programming

Variable Gain Amplifier: built on an Op-amp design using BJTs to amplify a signal from an iPhone to a speaker using approximately 1 Watt

AC to DC Converter: Fully functional AC to DC converter using a boost topology that can convert 7.5VAC a selectable DC voltage between 10V to 20V

Leadership Experience
Alpha Sigma Phi, Mu Chapter, Seattle, WA
Physical Plant Manager (September 2013 – April 2014)
- Attend monthly non-profit board meetings, managing a multi-million dollar property
- Discuss important topics regarding the infrastructure of the chapter house with alumni
- Contact contractors and organize repairs and maintenance
- Lead group projects where brothers help renovate the building

Honors and Awards
- Washington State Opportunity Scholarship
- Alpha Sigma Phi, Mu Chapter Foundation 2013

Justin Stapp (Fisherman's Restaurant Manager, 3 years)
Work phone: (206) 623-3500
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