

*Table 4.2: LDO performance summary*

	<b>Schematic</b>	<b>Post-layout</b>
PSSR	-36 dB @ 13.56 MHz	-59 dB @ 13.56 MH
Phase margin	75°	
Gain margin	30 dB	
Power efficiency	80.9 %	81 %
Quiescent current	105 uA	114 uA
Load regulation	17 uV/mA	53 uV/mA
Line regulation	435 uV/V	-1.162 mV/V