

## Contact

Seattle  
+1 206 724 5771 (Mobile)  
rikesh@gmail.com

[www.linkedin.com/in/rikkisnah](https://www.linkedin.com/in/rikkisnah)  
(LinkedIn)

## Top Skills

Technical Consultation  
Systems Management  
Integrated Systems

## Languages

Creoles and pidgins, French-based  
(Native or Bilingual)  
English (Native or Bilingual)  
French (Native or Bilingual)  
Hindi (Elementary)

## Certifications

Java Developer  
MCPD  
AWS Solutions Architect  
PMP

# Rik Kisnah

Senior Principal Engineer (HPC/GPU) at Oracle Cloud Infrastructure  
(OCI) for AI/ML Infrastructure  
Seattle, Washington, United States

## Summary

Experienced Engineer with domain expertise in DevOps/CICD/  
Testing/Automation/HPC/GPU/ML/AI in the Cloud. My passion is:  
Building teams, solving big-scale problems. Products I have worked  
and delivered chronologically: Motorola phones, Aeroflex PXI,  
Amazon Kindle, AWS EC2 Windows, AWS Workspaces, AWS Load  
Balancers, Oracle Cloud Bare Metal, Oracle HPC/GPU.

"In God we Trust, all others bring data"

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## Experience

### Oracle

6 years 10 months

#### Consulting Member of Technical Staff

August 2024 - Present (1 year 3 months)

Seattle, Washington, United States

Tech lead for AI/ML Infra for HPC/GPU Product offerings

#### Principal Engineer

January 2021 - August 2024 (3 years 8 months)

Seattle, Washington, United States

Tech lead for OCI - Compute - HPC/GPU organization.

#### Principal Engineer (DevOps/SRE)

January 2019 - December 2020 (2 years)

Seattle, Washington, United States

Team: <https://www.oracle.com/cloud/compute/>

Product: <https://www.oracle.com/cloud/hpc/> <https://www.oracle.com/cloud/compute/bare-metal.html>

### Amazon Web Services

#### Senior System Development Engineer

December 2012 - December 2018 (6 years 1 month)

Seattle

Team: <https://aws.amazon.com/elasticloadbalancing/>

Previous:

AWS Workspaces: <https://aws.amazon.com/workspaces/>

Amazon Kindle

## Aeroflex

Principal Staff Development Engineer

June 2010 - December 2012 (2 years 7 months)

Develop PXI Applications solutions for RF tests for Qualcomm chipsets

Architect test solutions for APAC customers

Pre-sales support to showcase PXI capabilities

## Motorola

Senior Staff Software Engineer

January 2002 - June 2009 (7 years 6 months)

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## Education

The University of Texas at Austin

Postgraduate Degree, Artificial Intelligence · (2019 - 2020)

University of Wales, Aberystwyth

MSc, Computer Science · (2007)

Nanyang Technological University

BASc, Computer Engineering

Massachusetts Institute of Technology

Postgraduate Degree, Artificial Intelligence · (2024 - 2026)