# **Riley Turner**

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### **OBJECTIVE**

Self motivated computer engineering student with great soft-skills and communication within team environments. Interested in obtaining a position to extend knowledge of communication systems and software/hardware applications.

#### **EXPERIENCE**

### **Software Engineering Intern**

October 2021 - Present

**ECP Power Corporation** 

Poway, CA

Power conversion system provider for grid scale energy storage, microgrids, and solar applications.

- Program unit tests using Verilog and SystemVerilog and simulate outputs within ModelSim.
- Create unit test builds within the command line using VUnit framework for more efficient unit testing.
- Implement a system to automate unit testing HDL files using Github Actions CI/CD pipelines via a push.
- Use JIRA and Confluence to organize tasks within daily scrum meetings.

# **Automation and Controls Engineering Intern**

June 2021 - August 2021

Tasteful Selections Arvin, CA

Leading grower, packer, and distributor of bite-sized potatoes within the United States.

- Develop SCADA software and use scripting language for seven case packing robots.
- Assist programming PLCs to modify and refine logic throughout the facility.
- Create an HTML website consisting of frequently used links, documents, and file paths for engineers.
- Participate in daily meetings with an engineering team to update and articulate daily and weekly work orders.

## **Social Engagement Director**

**August 2020 - May 2021** 

Institute of Electrical and Electronics Engineers SDSU Branch

San Diego, CA

Student organization specialized in issuing collaborative engineering events and technical engineering activities.

- Plan and conduct bi-weekly social events among 50 club members.
- Create and distribute online flyers through website homepage and social media accounts.
- Engage in bi-weekly officer meetings, document accomplishments and brainstorm ways to improve involvement.

#### **PROJECTS**

### **USMC Small Ground Sensor**

**August 2021 - Present** 

SDSU Capstone Senior Design Project (Electrical/Computer Engineering Team Lead)

San Diego, CA

- Procure and execute the motion sensor, memory management, and communications via WiFi into FTP server in C.
- Plan entire system software and implement functionality using dual-core processing of a microcontroller.
- Communicate with ECE and joint Mechanical Engineering team while documenting progress and goals.

#### **EDUCATION**

#### San Diego State University

August 2018 - Present

Computer Engineering; Minor in Leadership **Expected Graduation Year: May 2022** 

**Academic Honors: GPA 3.61**College of Engineering Dean's List

Relevant Courses Taken: Circuit Analysis I & II, Embedded System Programming, Microprocessors,

Signals & Systems, Feedback Control Systems, Computer & Data Networks, Analog & Pulse Communications

# **SKILLS**

- MS Word, Excel, PowerPoint
- C, C++, C#
- MySQL, phpMyAdmin, PHP
- Python, MATLAB

- HTML, CSS, Bootstrap
- Verilog, SystemVerilog, ARM Assembly
- Git
- RSLogix 5000, Ignition SCADA