

$$R_{eq} = R_1 + R_2 + R_3 + \dots$$

$$\frac{1}{R_{eq}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \dots$$

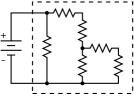
$$R_1||R_2||R_3...$$

$$\frac{R_1}{R_1 + R_2}V$$

$$\frac{R_2}{R_1 + R_2}V$$

$$\frac{R_2}{R_1 + R_2}I$$

$$\frac{R_1}{R_1 + R_2}I$$





Resistor Network Analysis 1. Use the equivalent resistance expressions (Equations 3 and 4) to find the equivalent resistance

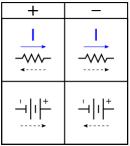
2. Use Ohm's law to calculate the current flowing through the voltage source.

of the entire network.

of all of the nodes.

- 2. Ose Offin's law to calculate the current flowing through the voltage source.
- 3. Use the current division expressions (Equations 6) at each node to determine the currents in the network.4. Use Ohm's law to determine the voltage drops across the resistors and from these, the voltages

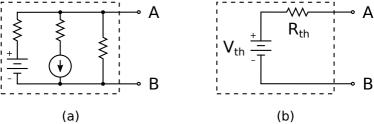
$$\sum_{\mathsf{loop}} V_i = 0$$



$$\sum_{\text{junction}} I_i = 0$$

Circuit Analysis using Kirchhoff's Rules

- 1. Draw the circuit with arrows and labels representing all of the unique currents.
- 2. Construct a set of loop-rule equations including every device in the circuit at least once.
- 3. Construct a set junction-rule equations including every current in the circuit at least once.
- 4. Given enough known quantities (usually the properties of the devices in the circuit resistances, capacitances, source voltages, e.g.), these equations can be combined algebraically to determine the unknown quantities (usually currents and potential differences in the circuit).



1. The Thévenin equivalent voltage V_{th} is the voltage across the output terminals A and B of

Finding the Thévenin Equivalent of a Circuit

the circuit with no load attached.

2. The Thévenin equivalent resistance R_{th} is the resistance of the circuit measured between the output terminals A and B, with all voltage and current sources replaced with their internal resistances. Ideal voltage sources have zero resistance, and ideal current sources have infinite resistance.

CIVIL

The mnemonic CIVIL can be used to remember that the voltage across a capacitor lags $\pi/2$ (half a period of oscillation) behind the current (CIV), and the voltage across an inductor leads the current by $\pi/2$ (VIL). The voltage across a resistor is always in phase with the current.

$$X_L = j\omega L$$

$$\omega = 2\pi f$$

$$Re(V e^{j\omega t + \phi_v}) = V \cos(\omega t + \phi_v)$$

$$Re(I e^{j\omega t + \phi_i}) = I \cos(\omega t + \phi_i)$$



 $Re(\mathbf{V} e^{j\omega t})$

 $Re(\mathbf{I} e^{j\omega t})$

$$P = I^*V = IV e^{j(\phi_v - \phi_i)}$$

$$\langle P \rangle = \frac{IV}{2} \cos(\phi_v - \phi_i) = \frac{V^2}{2Z} \cos(\phi_v - \phi_i)$$

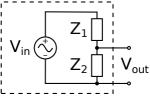
$$\phi_{\mathsf{v}} - \phi_{\mathsf{i}}$$

Phasor AnalysisEquivalent impedance calculations work the same way as equivalent resistance calculations

• Kirchhoff's rules work for phasors **V** and **I**.

(Equations 3 and 4).

• Thévenin's theorem applies to AC circuits containing only linear components. The Thévenin voltage and impedance are found via the process outlined in Section 1.5 with impedance in place of resistance.





$$\mathbf{V_{in}} - \mathbf{I}Z_1 - \mathbf{I}Z_2 = 0$$

$$\mathbf{V_{out}} = rac{Z_2}{Z_1 + Z_2} \mathbf{V_{in}}$$

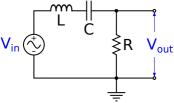


$$V_{out} = \sqrt{\mathbf{V}_{\mathbf{out}}^* \mathbf{V}_{\mathbf{out}}}$$



$$an\phi=rac{ extit{Im}\left(rac{Z_2}{Z_1+Z_2}
ight)}{ extit{Re}\left(rac{Z_2}{Z_1+Z_2}
ight)}$$

$$A_V = rac{V_{out}}{V_{in}} = rac{|\mathbf{V_{out}}|}{|\mathbf{V_{in}}|} = \left|rac{Z_2}{Z_1 + Z_2}
ight| = \sqrt{\left(rac{Z_2}{Z_1 + Z_2}
ight)^* \left(rac{Z_2}{Z_1 + Z_2}
ight)^2}$$



$$Z_2 = R$$

$$\frac{V_{out}}{V_{in}} = \sqrt{\frac{R^2}{R^2 + (\omega L - 1/\omega C)^2}}$$

 $R^2 - jR\left(\omega L - \frac{1}{\omega C}\right)$

 $\frac{\overline{Z_1 + Z_2} - \overline{R + j\left(\omega L - \frac{1}{\omega C}\right)} - R^2 + \left(\omega L - \frac{1}{\omega C}\right)^2}$

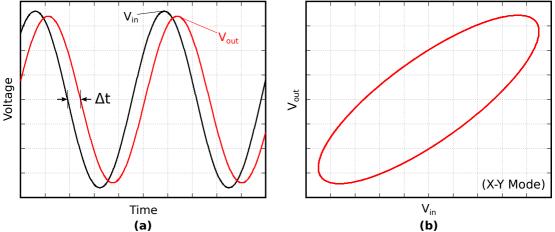
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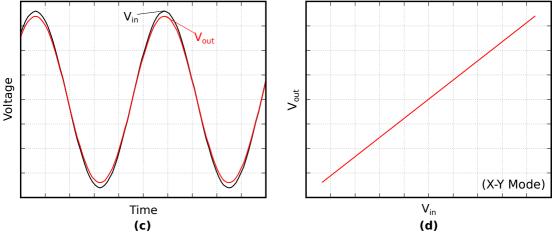
$$\Delta t = t_{V_{out}} - t_{V_{in}}$$

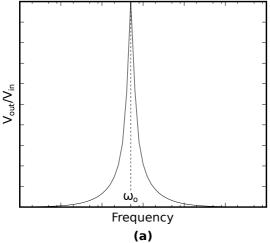
$$=rac{\phi}{\omega}=rac{\phi}{2\pi f}=$$

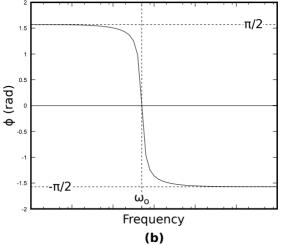
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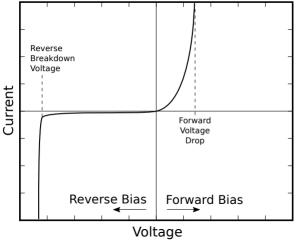




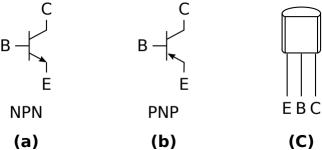


$$\omega_o = \frac{1}{\sqrt{LC}}$$

$$A_V = V_{out}/V_{in}$$

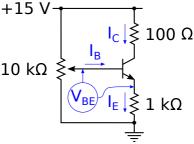






$$_{C}=I_{o}\left(e^{rac{V_{BE}}{kT/e}}-1
ight)$$

 $k = 1.38 \times 10^{-23}$



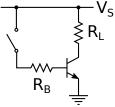
$$r_{\rm e} = \frac{dV_{BE}}{dI_C} = \frac{kT/e}{I_C}$$

$$(25 \Omega)/I_C[mA]$$

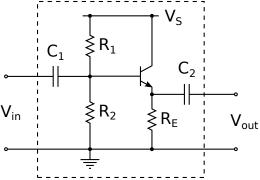
$$V_{CE} \approx 0.25$$

Basic Transistor Behavior

- In order for a transistor to function, make V_C > V_E, and keep I_B, I_C, and V_{CE} below the rated maximum values of the transistor.
 On: In saturation, V_{BE} ≈ 0.7 V, V_{CE} ≈ 0.25 V, and I_C = h_{FE}I_B, where h_{FE} ≈ 100.
 - Off: If $V_{BF} < 0.7 \text{ V (significantly)}$, $I_C \approx 0$.
- Active Region: V_{BE} is between "off" and saturation. The collector current is governed by the Ebers-Moll equation (Equation 29). The emitter resistance at room temperature is $r_e = (25 \ \Omega)/I_C [\text{mA}]$.

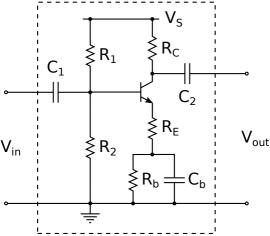


```
\approx 10 I_C/h_{fe}
```



$$V_B \approx V_E + 0.6 \text{ V}$$

$$R_1||R_2\approx h_{FE}R_E/10$$

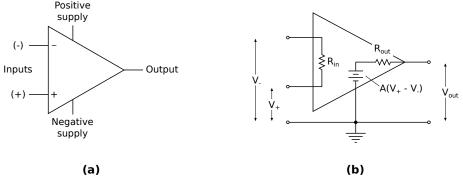


$$A_{v} = -\frac{R_{C}}{R_{E}}$$

$$pprox rac{R_b}{10}$$

$$R_1||R_2\approx h_{FE}(R_E+R_b)/10$$

$$R_C||(R_E+R_b)$$

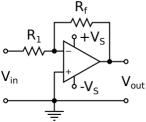


 $-V_S \leq V_{out} \leq V_S$

Basic Op Amp Behavior

- $R_{in} = \infty$, $A = \infty$, $R_{out} = 0$
 - Linear region: $V_+ = V_-, -V_S < V_{out} < V_S$

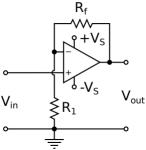
• Saturation: $V_+ > V_- \implies V_{out} = V_S$ or $V_+ < V_- \implies V_{out} = -V_S$



$$=rac{V_{in}}{R_1}=rac{-V_{out}}{R_f}$$

$$A_{v} = -\frac{R_{f}}{R_{1}}$$

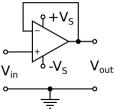
$$R_f/R_1$$

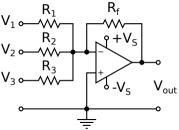


$$V_{in} = \frac{R_1}{R_1 + R_f} V_{out}$$

$$A_{v}=1+\frac{R_{f}}{R_{1}}$$

$$R_{oa}||(R_1+R_f)$$





$$\frac{V_1}{R_1}$$

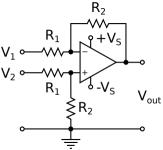
$$\frac{V_2}{R_2}$$

$$\frac{V_3}{R_3}$$

$$\frac{V_{out}}{R_f}$$

$$I_1 + I_2 + I_3 = I_f$$

$$V_{out} = -\left(\frac{R_1}{R_f}V_1 + \frac{R_2}{R_f}V_2 + \frac{R_3}{R_f}V_3\right)$$



$$I_1 + I_2 = I_3 + I_4$$

$$\frac{V_1 - V_-}{R_1}$$

$$\frac{V_{-}-V_{out}}{R_2}$$

$$\frac{V_+}{R_2}$$

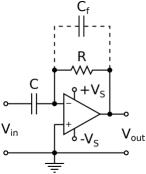
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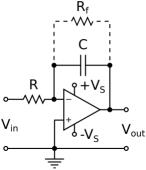
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$$V_{out} = -\frac{R_2}{R_1} \left(V_1 - V_2 \right)$$



$$V_{out} = -IR$$

$$V_{out} = -RC \frac{dV_{in}}{dt}$$



 $\frac{1}{R}\int V_{in} dt$

Q =

 $\int dQ$

$$V_{out} = -rac{1}{RC}\int V_{in}\,dt$$

$Ihing_1$

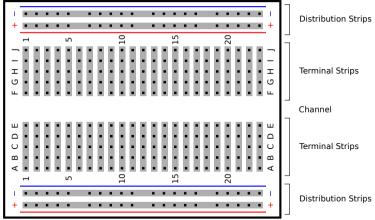
Thing₂

$$dB = 10 \log \left(\frac{\mathsf{Thing}_2}{\mathsf{Thing}_1} \right)$$

Thing₂

Thing₁

$$10 \log \left(\frac{P_{out}}{P_{in}}\right) = 10 \log \left(\frac{1}{2}\right) = 10(-0.3010) = -3.01$$





$$R = 64 \times 10^2 \ \Omega = 64 \times 100 \ \Omega = 6400 \ \Omega$$

