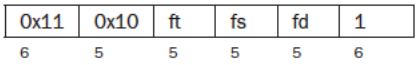
Floating-point subtract single

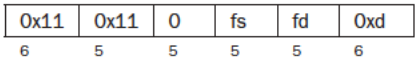
`sub.s fd, fs, ft`



Compute the difference of the floating-point doubles (singles) in registers `fs` and `ft` and put it in register `fd`.

Floating-point truncate to word

`trunc.w.d fd, fs`



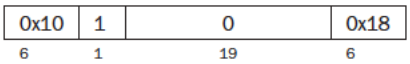
`trunc.w.s fd, fs`

Truncate the floating-point double (single) value in register `fs`, convert to a 32-bit fixed-point value, and put the resulting word in register `fd`.

Exception and Interrupt Instructions

Exception return

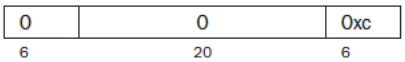
`eret`



Set the EXL bit in coprocessor 0's Status register to 0 and return to the instruction pointed to by coprocessor 0's EPC register.

System call

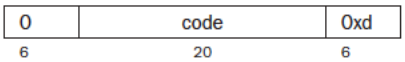
`syscall`



Register `$v0` contains the number of the system call (see Figure A.9.1) provided by SPIM.

Break

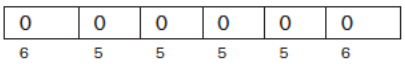
`break code`



Cause exception *code*. Exception 1 is reserved for the debugger.

No operation

`nop`



Do nothing.